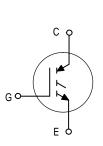
Preliminary Information

Insulated Gate Bipolar Transistor N-Channel Enhancement-Mode Silicon Gate

This Insulated Gate Bipolar Transistor (IGBT) uses an advanced termination scheme to provide an enhanced and reliable high voltage–blocking capability. The new generation provides lower On–voltage without sacrificing switching performance. Short circuit rated IGBT's are specifically suited for applications requiring a guaranteed short circuit withstand time such as Motor Control Drives. Fast switching characteristics result in efficient operation at high frequencies.

- Industry Standard High Power TO–247 Package with Isolated Mounting Hole
- High Speed: E_{off} = 167 μJ/A typical at 125°C
- High Voltage Short Circuit Capability 10 μs minimum at 125°C, 720 V
- Low On–Voltage 2.6 V typical at 10 A, 125°C
- Robust High Voltage Termination





IGBT IN TO-247 12 A @ 90°C 20 A @ 25°C 1200 VOLTS SHORT CIRCUIT RATED LOW ON-VOLTAGE



GC

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Rating		Value	Unit	
Collector–Emitter Voltage	VCES	1200	Vdc	
Collector–Gate Voltage (R_{GE} = 1.0 M Ω)	VCGR	1200	Vdc	
Gate-Emitter Voltage — Continuous	VGE	±20	Vdc	
Collector Current — Continuous @ $T_C = 25^{\circ}C$ — Continuous @ $T_C = 90^{\circ}C$ — Repetitive Pulsed Current (1)	IC25 IC90 IСМ	20 12 24	Adc Apk	
Total Power Dissipation @ T _C = 25°C Derate above 25°C	PD	123 0.98	Watts W/°C	
Operating and Storage Junction Temperature Range	TJ, Tstg	-55 to 150	°C	
Short Circuit Withstand Time (V _{CC} = 720 Vdc, V _{GE} = 15 Vdc, T _J = 125°C, R _G = 20 Ω)	t _{sc}	10	μs	
Thermal Resistance — Junction to Case – IGBT — Junction to Ambient	R _{θJC} R _{θJA}	1.0 45	°C/W	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 5 seconds	TL	260	°C	
Mounting Torque, 6–32 or M3 screw	10	10 lbf•in (1.13 N•m)		

(1) Pulse width is limited by maximum junction temperature. Repetitive rating.

This document contains information on a product under development. Motorola reserves the right to change or discontinue this product without notice.

Preferred devices are Motorola recommended choices for future use and best overall value.



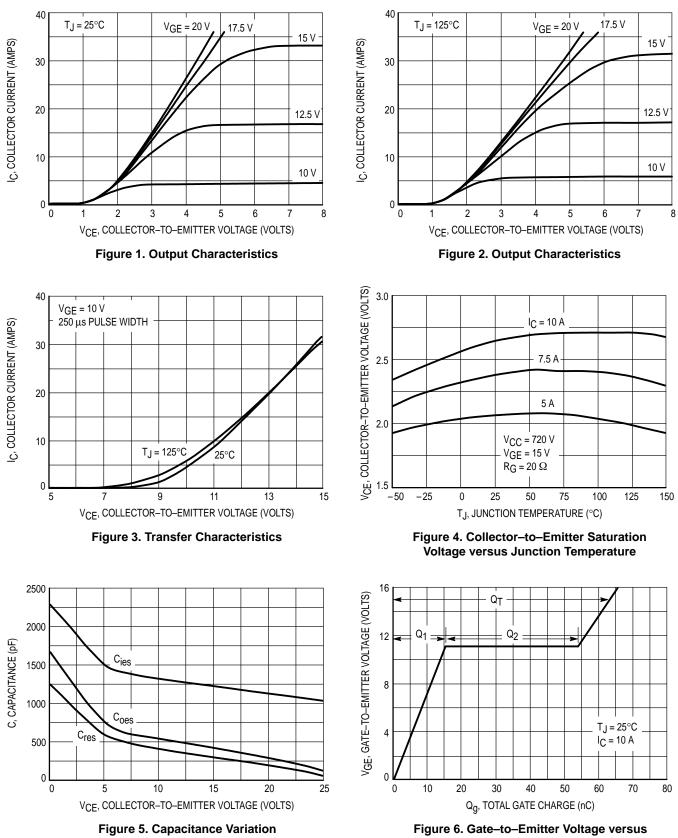
MGW12N120E

ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise noted)

Ch	aracteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS		•				
Collector-to-Emitter Breakdown V ($V_{GE} = 0 Vdc, I_C = 25 \mu Adc$) Temperature Coefficient (Positiv	C C C C C C C C C C C C C C C C C C C	V(BR)CES	1200 —	 870		Vdc mV/°C
Emitter-to-Collector Breakdown Voltage (V _{GE} = 0 Vdc, I _{EC} = 100 mAdc)		V(BR)ECS	25	—	—	Vdc
Zero Gate Voltage Collector Current ($V_{CE} = 1200 \text{ Vdc}, V_{GE} = 0 \text{ Vdc}$) ($V_{CE} = 1200 \text{ Vdc}, V_{GE} = 0 \text{ Vdc}, T_{J} = 125^{\circ}\text{C}$)		ICES			10 300	μAdc
Gate-Body Leakage Current (V _{GE} = \pm 20 Vdc, V _{CE} = 0 Vdc)		IGES	-	—	250	nAdc
ON CHARACTERISTICS (1)		•				
$\label{eq:constraint} \begin{array}{l} \mbox{Collector-to-Emitter On-State Vc} \\ \mbox{(V}_{GE} = 15 \mbox{ Vdc}, \mbox{ I}_{C} = 5.0 \mbox{ Adc}) \\ \mbox{(V}_{GE} = 15 \mbox{ Vdc}, \mbox{ I}_{C} = 5.0 \mbox{ Adc}, \mbox{ T} \\ \mbox{(V}_{GE} = 15 \mbox{ Vdc}, \mbox{ I}_{C} = 10 \mbox{ Adc}) \end{array}$	C C	V _{CE(on)}		2.0 2.1 2.6	3.0 — 3.5	Vdc
Gate Threshold Voltage ($V_{CE} = V_{GE}$, $I_C = 1.0$ mAdc) Threshold Temperature Coeffici	ent (Negative)	VGE(th)	4.0	6.0 10	8.0 —	Vdc mV/°C
Forward Transconductance (VCE	= 10 Vdc, I _C = 10 Adc)	9fe	—	5.6	—	Mhos
OYNAMIC CHARACTERISTICS		•				
Input Capacitance		C _{ies}	-	1033	—	pF
Output Capacitance	(V _{CE} = 25 Vdc, V _{GE} = 0 Vdc, f = 1.0 MHz)	C _{oes}	-	131	—	
Transfer Capacitance		C _{res}	-	64	—	
SWITCHING CHARACTERISTICS	(1)				_	
Turn–On Delay Time		^t d(on)	-	39	—	ns
Rise Time	$(V_{CC} = 720 \text{ Vdc}, I_{C} = 10 \text{ Adc},$	tr	-	36	—	
Turn–Off Delay Time	V _{GE} = 15 Vdc, L = 300 μH, R _G = 20 Ω)	^t d(off)	-	129	—	
Fall Time	Energy losses include "tail"	t _f	—	400	—	
Turn–Off Switching Loss		Eoff	-	0.96	1.5	mJ
Turn–On Delay Time	$(V_{CC} = 720 \text{ Vdc}, \text{ I}_{C} = 10 \text{ Adc}, \\ V_{GE} = 15 \text{ Vdc}, \text{ L} = 300 _{\mu}\text{H}, \\ \text{R}_{G} = 20 _{\Omega}, \text{T}_{J} = 125^{\circ}\text{C}) \\ \text{Energy losses include "tail"}$	^t d(on)	-	155	—	ns
Rise Time		tr	-	36	—	
Turn–Off Delay Time		^t d(off)	—	164	—	
Fall Time		tf	-	625	—	
Turn–Off Switching Loss		E _{off}	-	1.67	—	mJ
Gate Charge	(V _{CC} = 600 V, I _C = 10 Adc, V _{GE} = 15 Vdc)	QT	-	62	—	nC
		Q ₁	- 1	15.6	—	1
		Q ₂	-	37		1
NTERNAL PACKAGE INDUCTAN	CE					
Internal Emitter Inductance (Measured from the emitter lead	1 0.25" from package to emitter bond pad)	LE	_	13	_	nH

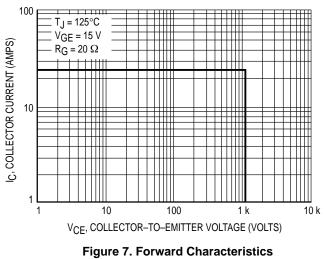
(1) Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2%.

TYPICAL ELECTRICAL CHARACTERISTICS



Total Charge

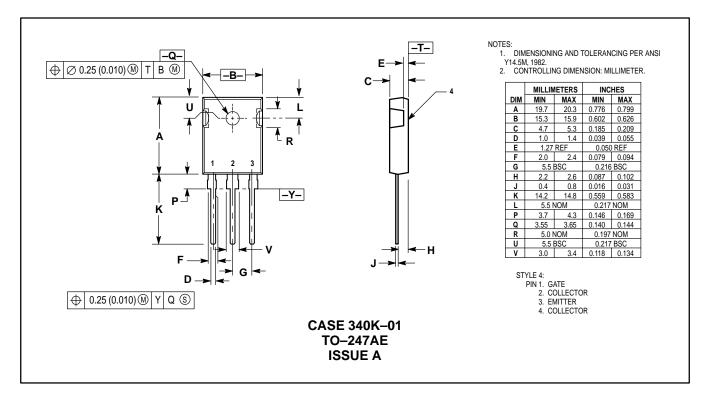
TYPICAL ELECTRICAL CHARACTERISTICS



versus Current

MGW12N120E

PACKAGE DIMENSIONS



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