

## N - CHANNEL ENHANCEMENT MODE POWER MOS TRANSISTOR

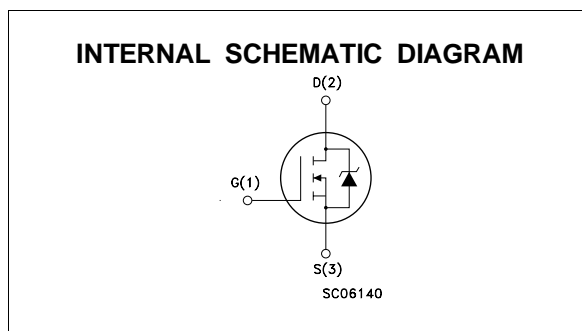
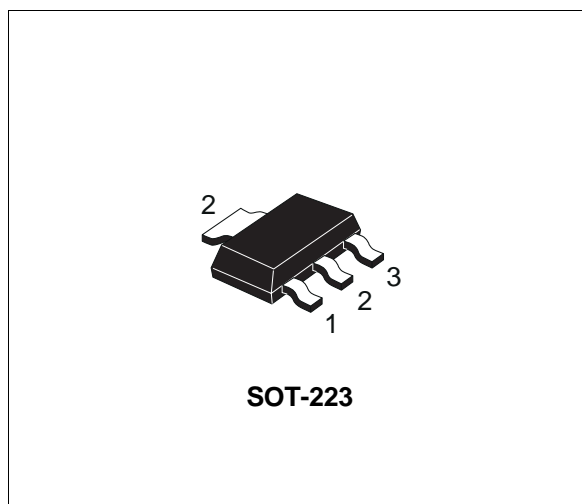
ADVANCE DATA

TYPE	V <sub>DSS</sub>	R <sub>DS(on)</sub>	I <sub>DCONT</sub>
STN2N06	60 V	< 0.250 Ω	2 A

- TYPICAL R<sub>DS(on)</sub> = 0.21 Ω
- AVALANCHE RUGGED TECHNOLOGY
- SOT-223 CAN BE WAVE OR REFLOW SOLDERED
- AVAILABLE IN TAPE AND REEL ON REQUEST
- 150 °C OPERATING TEMPERATURE
- APPLICATION ORIENTED CHARACTERIZATION

### APPLICATIONS

- HARD DISK DRIVERS
- SMALL MOTOR CURRENT SENSE CIRCUITS
- DC-DC CONVERTERS AND POWER SUPPLIES



### ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V <sub>DS</sub>	Drain-source Voltage (V <sub>GS</sub> = 0)	60	V
V <sub>DGR</sub>	Drain- gate Voltage (R <sub>GS</sub> = 20 kΩ)	60	V
V <sub>GS</sub>	Gate-source Voltage	± 20	V
I <sub>D</sub> (*)	Drain Current (continuous) at T <sub>c</sub> = 25 °C	2	A
I <sub>D</sub> (*)	Drain Current (continuous) at T <sub>c</sub> = 100 °C	1.3	A
I <sub>DM</sub> (•)	Drain Current (pulsed)	8	A
P <sub>tot</sub>	Total Dissipation at T <sub>c</sub> = 25 °C	2.7	W
	Derating Factor	0.022	W/°C
T <sub>stg</sub>	Storage Temperature	-65 to 150	°C
T <sub>j</sub>	Max. Operating Junction Temperature	150	°C

(•) Pulse width limited by safe operating area (\*) Limited by package

## STN2N06

### THERMAL DATA

$R_{thj-pcb}$	Thermal Resistance Junction-PC Board	Max	46	$^{\circ}C/W$
$R_{thj-amb}$	Thermal Resistance Junction-ambient (Surface Mounted)	Max	60	$^{\circ}C/W$
$T_l$	Maximum Lead Temperature For Soldering Purpose		260	$^{\circ}C$

### AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
$I_{AR}$	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by $T_j$ max, $\delta < 1\%$ )	2	A
$E_{AS}$	Single Pulse Avalanche Energy (starting $T_j = 25^{\circ}C$ , $I_D = I_{AR}$ , $V_{DD} = 25 V$ )	40	mJ
$E_{AR}$	Repetitive Avalanche Energy (pulse width limited by $T_j$ max, $\delta < 1\%$ )	10	mJ
$I_{AR}$	Avalanche Current, Repetitive or Not-Repetitive ( $T_c = 100^{\circ}C$ , pulse width limited by $T_j$ max, $\delta < 1\%$ )	1.3	A

### ELECTRICAL CHARACTERISTICS ( $T_{case} = 25^{\circ}C$ unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	Drain-source Breakdown Voltage	$I_D = 250 \mu A$ $V_{GS} = 0$	60			V
$I_{DSS}$	Zero Gate Voltage Drain Current ( $V_{GS} = 0$ )	$V_{DS} = \text{Max Rating}$ $V_{DS} = \text{Max Rating} \times 0.8$ $T_c = 125^{\circ}C$			250 1000	$\mu A$ $\mu A$
$I_{GSS}$	Gate-body Leakage Current ( $V_{DS} = 0$ )	$V_{GS} = \pm 20 V$			$\pm 100$	nA

ON (\*)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ $I_D = 250 \mu A$	2	3	4	V
$R_{DS(on)}$	Static Drain-source On Resistance	$V_{GS} = 10 V$ $I_D = 1 A$ $V_{GS} = 10 V$ $I_D = 1 A$ $T_c = 100^{\circ}C$		0.21	0.25 0.5	$\Omega$ $\Omega$
$I_{D(on)}$	On State Drain Current	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $V_{GS} = 10 V$	2			A

### DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$g_{fs} (*)$	Forward Transconductance	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $I_D = 1 A$	0.8	1.5		S
$C_{iss}$	Input Capacitance	$V_{DS} = 25 V$ $f = 1 MHz$ $V_{GS} = 0 V$		260	340	pF
$C_{oss}$	Output Capacitance			90	120	pF
$C_{rss}$	Reverse Transfer Capacitance			30	40	pF

**ELECTRICAL CHARACTERISTICS** (continued)**SWITCHING ON**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$ $t_r$	Turn-on Time Rise Time	$V_{DD} = 30\text{ V}$ $I_D = 4\text{ A}$ $R_G = 47\ \Omega$ $V_{GS} = 10\text{ V}$		14 75	20 100	ns ns
$(di/dt)_{on}$	Turn-on Current Slope	$V_{DD} = 48\text{ V}$ $I_D = 8\text{ A}$ $R_G = 47\ \Omega$ $V_{GS} = 10\text{ V}$		240		A/ $\mu\text{s}$
$Q_g$ $Q_{gs}$ $Q_{gd}$	Total Gate Charge Gate-Source Charge Gate-Drain Charge	$V_{DD} = 48\text{ V}$ $I_D = 8\text{ A}$ $V_{GS} = 10\text{ V}$		13 7 4	20	nC nC nC

**SWITCHING OFF**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{r(Voff)}$ $t_f$ $t_c$	Off-voltage Rise Time Fall Time Cross-over Time	$V_{DD} = 48\text{ V}$ $I_D = 8\text{ A}$ $R_G = 47\ \Omega$ $V_{GS} = 10\text{ V}$		16 22 45	25 30 60	ns ns ns

**SOURCE DRAIN DIODE**

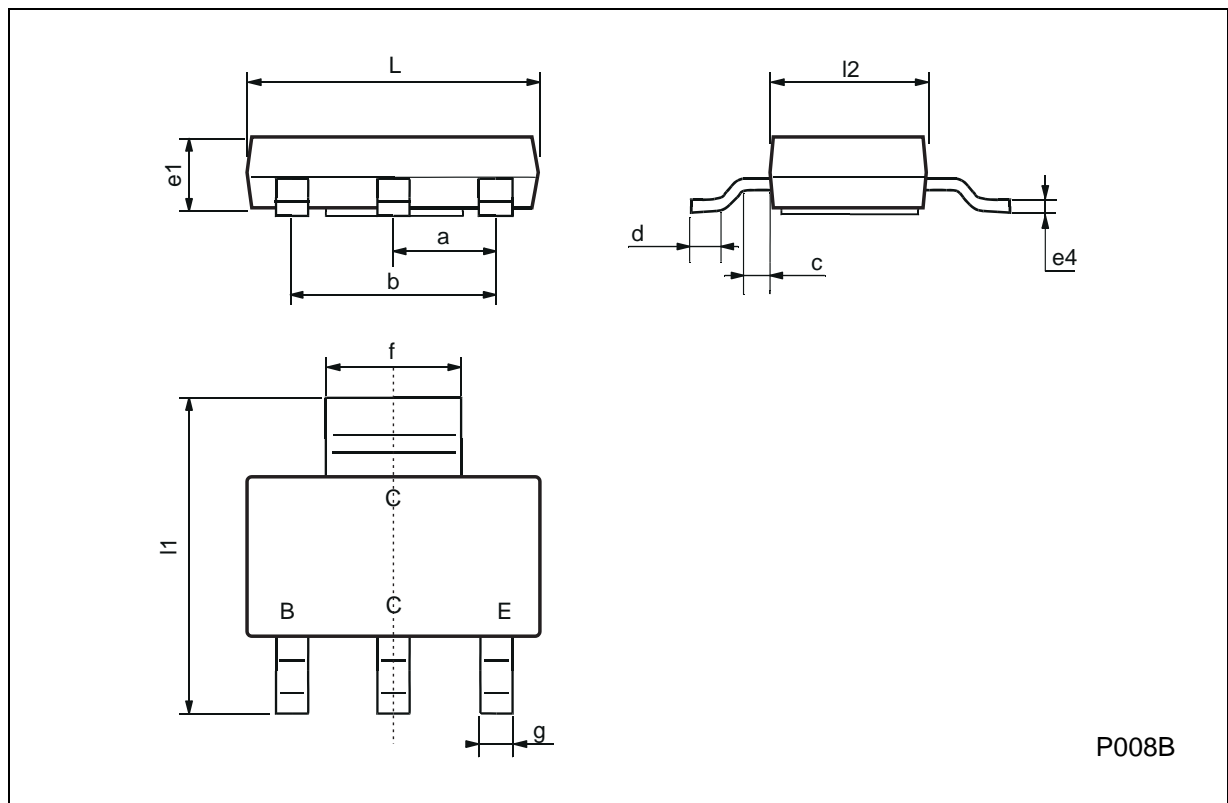
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$I_{SD}$ $I_{SDM}(\bullet)$	Source-drain Current Source-drain Current (pulsed)				2 8	A A
$V_{SD} (*)$	Forward On Voltage	$I_{SD} = 2\text{ A}$ $V_{GS} = 0$			1.5	V
$t_{rr}$ $Q_{rr}$ $I_{RRM}$	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	$I_{SD} = 8\text{ A}$ $di/dt = 100\text{ A}/\mu\text{s}$ $V_{DD} = 25\text{ V}$ $T_j = 150\text{ }^\circ\text{C}$		70 0.18 5		ns $\mu\text{C}$ A

(\*) Pulsed: Pulse duration = 300  $\mu\text{s}$ , duty cycle 1.5 %

(\bullet) Pulse width limited by safe operating area

**SOT223 MECHANICAL DATA**

DIM.	mm			mils		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
a	2.27	2.3	2.33	89.4	90.6	91.7
b	4.57	4.6	4.63	179.9	181.1	182.3
c	0.2	0.4	0.6	7.9	15.7	23.6
d	0.63	0.65	0.67	24.8	25.6	26.4
e1	1.5	1.6	1.7	59.1	63	66.9
e4			0.32			12.6
f	2.9	3	3.1	114.2	118.1	122.1
g	0.67	0.7	0.73	26.4	27.6	28.7
l1	6.7	7	7.3	263.8	275.6	287.4
l2	3.5	3.5	3.7	137.8	137.8	145.7
L	6.3	6.5	6.7	248	255.9	263.8



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