XN0431L (XN431L)

Silicon NPN epitaxial planer transistor (Tr1) Silicon PNP epitaxial planer transistor (Tr2)

For switching/digital circuits

Features

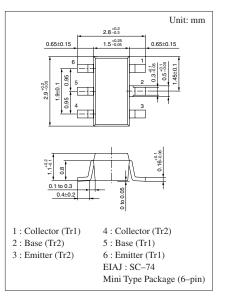
- Two elements incorporated into one package. (Transistors with built-in resistor)
- Reduction of the mounting area and assembly cost by one half.

Basic Part Number of Element

• UNR121L(UN121L) + UNR111L(UN111L)

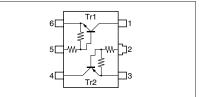
Parameter		Symbol Ratings		Unit	
Tr1	Collector to base voltage	V _{CBO}	50	V	
	Collector to emitter voltage	V _{CEO}	50	V	
	Collector current	I _C	100	mA	
Tr2	Collector to base voltage	V _{CBO}	-50	V	
	Collector to emitter voltage	V _{CEO}	-50	V	
	Collector current	I _C	-100	mA	
Overall	Total power dissipation	P _T	300	mW	
	Junction temperature	Tj	150	°C	
	Storage temperature	T _{stg}	-55 to +150	°C	

Absolute Maximum Ratings (Ta=25°C)



Marking Symbol: FI

Internal Connection



Note.) The Part number in the Parenthesis shows conventional part number.

Electrical Characteristics (Ta=25°C)

• Tr1

Parameter	Symbol	Conditions	min	typ	max	Unit
Collector to base voltage	V _{CBO}	$I_{C} = 10 \mu A, I_{E} = 0$	50			V
Collector to emitter voltage	V _{CEO}	$I_{\rm C} = 2mA, I_{\rm B} = 0$	50			V
C-llis stern systeff summert	I _{CBO}	$V_{CB} = 50V, I_E = 0$			0.1	μΑ
Collector cutoff current	I _{CEO}	$V_{CE} = 50V, I_B = 0$			0.5	μΑ
Emitter cutoff current	I _{EBO}	$V_{EB} = 6V, I_C = 0$			2.0	mA
Forward current transfer ratio	h _{FE}	$V_{CE} = 10V, I_C = 5mA$	20			
Collector to emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 10 {\rm mA}, I_{\rm B} = 0.3 {\rm mA}$			0.25	V
Output voltage high level	V _{OH}	$V_{CC} = 5V, V_B = 0.5V, R_L = 1k\Omega$	4.9			V
Output voltage low level	V _{OL}	$V_{CC} = 5V, V_B = 2.5V, R_L = 1k\Omega$			0.2	V
Input resistance	R ₁		-30%	4.7	+30%	kΩ
Resistance ratio	R ₁ /R ₂		0.8	1.0	1.2	
Transition frequency	f _T	$V_{CB} = 10V, I_E = -2mA, f = 200MHz$		150		MHz

• Tr2

Parameter	Symbol	Conditions	min	typ	max	Unit
Collector to base voltage	V _{CBO}	$I_{\rm C} = -10\mu A, I_{\rm E} = 0$	-50			V
Collector to emitter voltage	V _{CEO}	$I_{\rm C} = -2mA, I_{\rm B} = 0$	-50			V
	I _{CBO}	$V_{CB} = -50V, I_E = 0$			- 0.1	μΑ
Collector cutoff current	I _{CEO}	$V_{CE} = -50V, I_B = 0$			- 0.5	μΑ
Emitter cutoff current	I _{EBO}	$V_{EB} = -6V, I_C = 0$			-2.0	mA
Forward current transfer ratio	h _{FE}	$V_{CE} = -10V, I_C = -5mA$	20			
Collector to emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = -10 {\rm mA}, I_{\rm B} = -0.3 {\rm mA}$			- 0.25	V
Output voltage high level	V _{OH}	$V_{CC} = -5V, V_B = -0.5V, R_L = 1k\Omega$	-4.9			V
Output voltage low level	V _{OL}	$V_{CC} = -5V, V_B = -2.5V, R_L = 1k\Omega$			- 0.2	V
Input resistance	R ₁		-30%	4.7	+30%	kΩ
Resistance ratio	R ₁ /R ₂		0.8	1.0	1.2	
Transition frequency	f _T	$V_{CB} = -10V, I_E = 2mA, f = 200MHz$		150		MHz

V_{CE}=10V

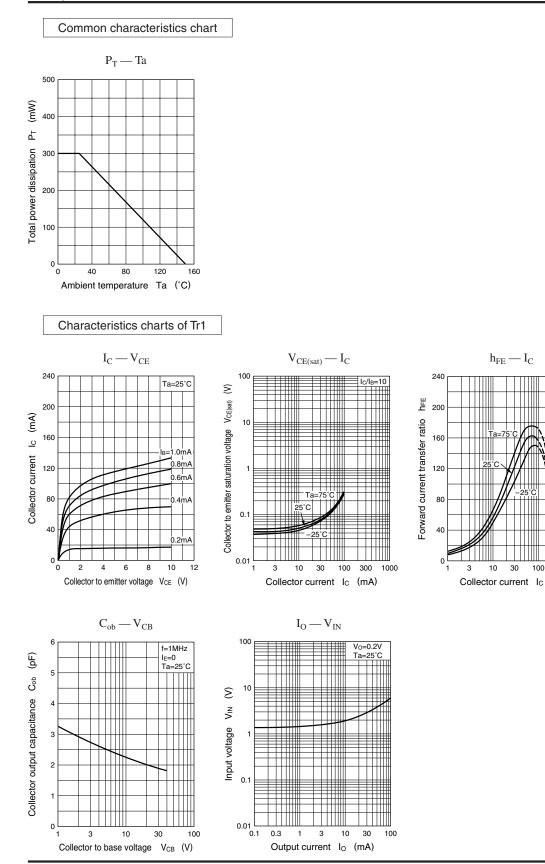
300

(mA)

1000

100

30

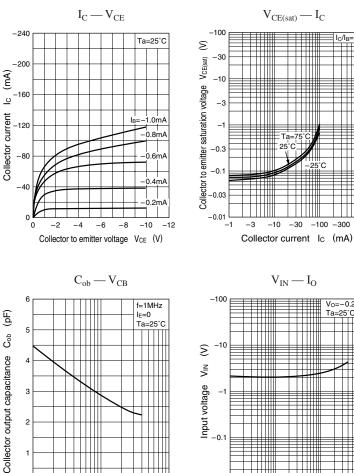


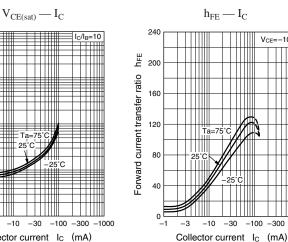
Panasonic

V_{CE}=-10V

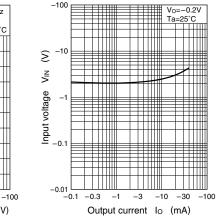
-100 -300 -1000

Characteristics charts of Tr2









2

1

0 -1

-3

Collector to base voltage

-10

-30

V_{CB} (V)

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