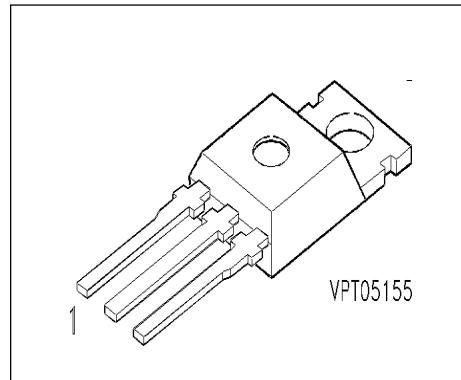


IGBT

- Low forward voltage drop
- High switching speed
- Low tail current
- Latch-up free
- Avalanche rated



Pin 1	Pin 2	Pin 3
G	C	E

Type	V_{CE}	I_C	Package	Ordering Code
SGP20N60	600V	20A	TO-220 AB	Q67040-A . . .

Maximum Ratings

Parameter	Symbol	Values	Unit
Collector-emitter voltage	V_{CE}	600	V
Collector-gate voltage	V_{CGR}	600	
$R_{GE} = 20 \text{ k}\Omega$	V_{GE}	± 20	
DC collector current $T_C = 25^\circ\text{C}$	I_C	40	A
$T_C = 100^\circ\text{C}$		20	
Pulsed collector current, $t_p = 1 \text{ ms}$ $T_C = 25^\circ\text{C}$	I_{Cpuls}	80	
$T_C = 100^\circ\text{C}$		40	
Avalanche energy, single pulse $I_C = 20 \text{ A}, V_{CC} = 50 \text{ V}, R_{GE} = 25 \Omega$ $L = 200 \mu\text{H}, T_j = 25^\circ\text{C}$	E_{AS}	18	mJ
Power dissipation $T_C = 25^\circ\text{C}$	P_{tot}	175	W

Preliminary data
Maximum Ratings

Parameter	Symbol	Values	Unit
Chip or operating temperature	T_j	-55 ... + 150	°C
Storage temperature	T_{stg}	-55 ... + 150	
IEC climatic category, DIN IEC 68-1	-	55 / 150 / 56	-

Thermal Resistance

Thermal resistance, junction - case	R_{thJC}	≤ 0.7	K/W
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Electrical Characteristics, at $T_j = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

Static Characteristics

Collector-emitter breakdown voltage $V_{GE} = 0$ V, $I_C = 0.5$ mA, $T_j = -55$ °C	$V_{(BR)CES}$	600	-	-	V
Gate threshold voltage $V_{GE} = V_{CE}$, $I_C = 0.5$ mA, $T_j = 25$ °C $V_{GE} = V_{CE}$, $I_C = 0.5$ mA, $T_j = 150$ °C	$V_{GE(th)}$	3 2	4 3	5 -	
Collector-emitter saturation voltage $V_{GE} = 15$ V, $I_C = 20$ A, $T_j = 25$ °C $V_{GE} = 15$ V, $I_C = 20$ A, $T_j = 150$ °C	$V_{CE(sat)}$	1.6 -	2 2.3	2.5 2.8	
Zero gate voltage collector current $V_{CE} = 600$ V, $V_{GE} = 0$ V, $T_j = 25$ °C $V_{CE} = 600$ V, $V_{GE} = 0$ V, $T_j = 150$ °C	I_{CES}	- -	- -	40 2500	µA
Gate-emitter leakage current $V_{GE} = 25$ V, $V_{CE} = 0$ V	I_{GES}	-	-	100	nA

Electrical Characteristics, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

AC Characteristics

Transconductance $V_{CE} = 20 \text{ V}, I_C = 20 \text{ A}$	g_{fs}	4	14	-	S
Input capacitance $V_{CE} = 25 \text{ V}, V_{GE} = 0 \text{ V}, f = 1 \text{ MHz}$	C_{iss}	-	1100	1400	pF
Output capacitance $V_{CE} = 25 \text{ V}, V_{GE} = 0 \text{ V}, f = 1 \text{ MHz}$	C_{oss}	-	110	140	
Reverse transfer capacitance $V_{CE} = 25 \text{ V}, V_{GE} = 0 \text{ V}, f = 1 \text{ MHz}$	C_{rss}	-	65	85	

Preliminary data

Electrical Characteristics, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

Switching Characteristics, Inductive Load at $T_j = 150^\circ\text{C}$

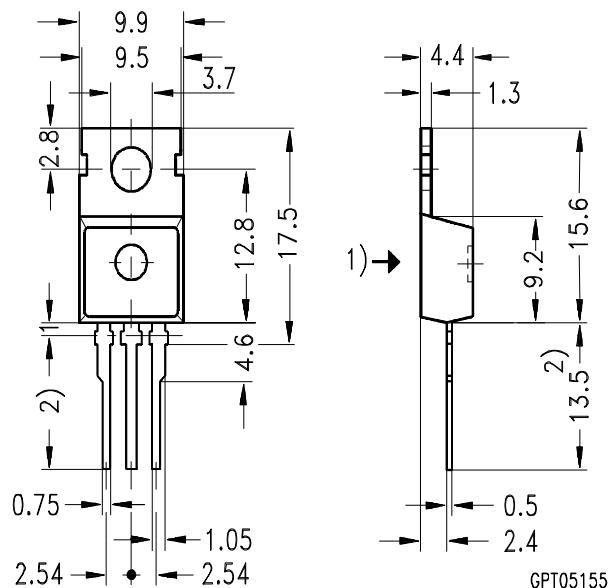
Turn-on delay time $V_{CC} = 400 \text{ V}, V_{GE} = 15 \text{ V}, I_C = 20 \text{ A}$ $R_{Gon} = 16 \Omega$	$t_{d(on)}$	-	20	30	ns
Rise time $V_{CC} = 400 \text{ V}, V_{GE} = 15 \text{ V}, I_C = 20 \text{ A}$ $R_{Gon} = 16 \Omega$	t_r	-	35	55	
Turn-off delay time $V_{CC} = 400 \text{ V}, V_{GE} = 15 \text{ V}, I_C = 20 \text{ A}$ $R_{Goff} = 16 \Omega$	$t_{d(off)}$	-	250	380	
Fall time $V_{CC} = 400 \text{ V}, V_{GE} = 15 \text{ V}, I_C = 20 \text{ A}$ $R_{Goff} = 16 \Omega$	t_f	-	63	95	
Total turn-on loss energy * $V_{CC} = 400 \text{ V}, V_{GE} = 15 \text{ V}, I_C = 20 \text{ A}$ $R_{Gon} = 16 \Omega, T_j = 150^\circ\text{C}$	E_{on}	-	1.18	1.55	mJ
Total turn-off loss energy $V_{CC} = 400 \text{ V}, V_{GE} = 15 \text{ V}, I_C = 20 \text{ A}$ $R_{Goff} = 16 \Omega, T_j = 150^\circ\text{C}$	E_{off}	-	0.49	0.65	
Total Gate Charge $V_{CC} = 480 \text{ V}, V_{GE} = 15 \text{ V}, I_C = 20 \text{ A}$	$Q_{G(on)}$	-	97	145	nC

* includes the reverse recovery losses caused by the FWD of the BUP602D

Package Outlines

Dimensions in mm

Weight:



1) punch direction, burr max. 0.04

2) dip tinning

3) max. 14.5 by dip tinning press burr max. 0.05