

FEATURES

- 1mA Supply Current per Amplifier
- 50V/ μ s Slew Rate
- 12MHz Gain-Bandwidth
- Unity-Gain Stable
- 330ns Settling Time to 0.1%, 10V Step
- 6V/mV DC Gain, $R_L = 2k\Omega$
- 2mV Maximum Input Offset Voltage
- 100nA Maximum Input Offset Current
- 1 μ A Maximum Input Bias Current
- $\pm 12V$ Minimum Output Swing into $2k\Omega$
- Wide Supply Range: $\pm 2.5V$ to $\pm 15V$
- Drives Capacitive Loads

APPLICATIONS

- Wideband Amplifiers
- Buffers
- Active Filters
- Video and RF Amplification
- Cable Drivers
- Data Acquisition Systems

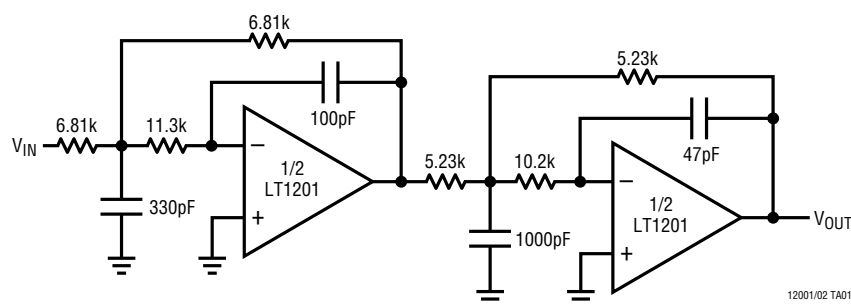
DESCRIPTION

The LT1201/LT1202 are dual and quad low power, high speed operational amplifiers with excellent DC performance. The LT1201/LT1202 feature much lower supply current than devices with comparable bandwidth and slew rate. Each amplifier is a single gain stage with outstanding settling characteristics. The fast settling time makes the circuit an ideal choice for data acquisition systems. Each output is capable of driving a $2k\Omega$ load to $\pm 12V$ with $\pm 15V$ supplies and a 500Ω load to $\pm 3V$ on $\pm 5V$ supplies. The amplifiers are also capable of driving large capacitive loads which make them useful in buffer or cable driver applications.

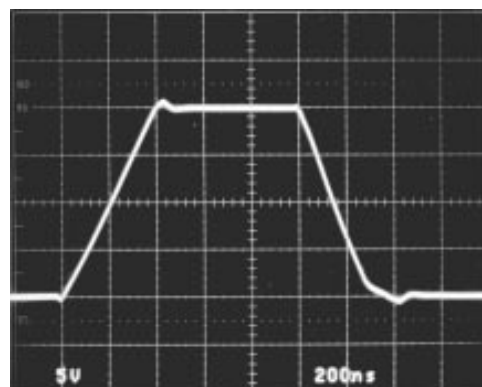
The LT1201/LT1202 are members of a family of fast, high performance amplifiers that employ Linear Technology Corporation's advanced bipolar complementary processing.

TYPICAL APPLICATION

100kHz, 4th Order Butterworth Filter



Inverter Pulse Response



1201/02 TA02

LT1201/LT1202

ABSOLUTE MAXIMUM RATINGS

| | |
|---|---|
| Total Supply Voltage (V^+ to V^-) | 36V |
| Differential Input Voltage | $\pm 6V$ |
| Input Voltage | $\pm V_S$ |
| Output Short-Circuit Duration (Note 1) | Indefinite |
| Operating Temperature Range | |
| LT1201C/LT1202C | -40°C to 85°C |

| | |
|--|--|
| Specified Temperature Range (Note 5) | |
| LT1201C/LT1202C | 0°C to 70°C |
| Maximum Junction Temperature | |
| Plastic Package | 150°C |
| Storage Temperature Range | -65°C to 150°C |
| Lead Temperature (Soldering, 10 sec) | 300°C |

PACKAGE/ORDER INFORMATION

| | | | |
|--|-------------------|---|-------------------|
| <p>N8 PACKAGE 8-LEAD PLASTIC DIP $T_{JMAX} = 150^\circ\text{C}$, $\theta_{JA} = 100^\circ\text{C/W}$</p> | ORDER PART NUMBER | <p>S8 PACKAGE 8-LEAD PLASTIC SOIC $T_{JMAX} = 150^\circ\text{C}$, $\theta_{JA} = 150^\circ\text{C/W}$</p> | ORDER PART NUMBER |
| | LT1201CN8 | | LT1201CS8 |
| | | | 1201 |
| <p>N PACKAGE 14-LEAD PLASTIC DIP $T_{JMAX} = 150^\circ\text{C}$, $\theta_{JA} = 70^\circ\text{C/W}$</p> | ORDER PART NUMBER | <p>S PACKAGE 16-LEAD PLASTIC SOIC $T_{JMAX} = 150^\circ\text{C}$, $\theta_{JA} = 100^\circ\text{C/W}$</p> | ORDER PART NUMBER |
| | LT1202CN | | LT1202CS |

ELECTRICAL CHARACTERISTICS $V_S = \pm 15V$, $T_A = 25^\circ\text{C}$, $V_{CM} = 0V$, unless otherwise noted.

| SYMBOL | PARAMETER | CONDITIONS | MIN | TYP | MAX | UNITS | |
|----------|----------------------|---|-----|-----|-----|------------------------------|----|
| V_{OS} | Input Offset Voltage | $V_S = \pm 15V$ (Note 2) 0°C to 70°C | | 0.7 | 2.0 | mV | |
| | | | | | 3.0 | mV | |
| | Input V_{OS} Drift | $V_S = \pm 5V$ (Note 2) 0°C to 70°C | | | 1.0 | 4.0 | mV |
| | | | | | | 4.5 | mV |
| | Input V_{OS} Drift | | | 11 | | $\mu\text{V}/^\circ\text{C}$ | |
| I_{OS} | Input Offset Current | $V_S = \pm 5V$ and $V_S = \pm 15V$ 0°C to 70°C | | 50 | 100 | nA | |
| | | | | | 150 | nA | |
| I_B | Input Bias Current | $V_S = \pm 5V$ and $V_S = \pm 15V$ 0°C to 70°C | | 0.5 | 1.0 | μA | |
| | | | | | 1.2 | μA | |
| e_n | Input Noise Voltage | $f = 10\text{kHz}$ | | 30 | | $\text{nV}/\sqrt{\text{Hz}}$ | |
| i_n | Input Noise Current | $f = 10\text{kHz}$ | | 0.6 | | $\text{pA}/\sqrt{\text{Hz}}$ | |

ELECTRICAL CHARACTERISTICS $V_S = \pm 15V$, $T_A = 25^\circ C$, $V_{CM} = 0V$, unless otherwise noted.

| SYMBOL | PARAMETER | CONDITIONS | MIN | TYP | MAX | UNITS |
|---------------|----------------------------------|--|-------------|-------------|---------------|--------------------------|
| R_{IN} | Input Resistance | $V_{CM} = \pm 12V$ Differential | 48 | 90 500 | | $M\Omega$ $k\Omega$ |
| C_{IN} | Input Capacitance | | | 2 | | pF |
| CMRR | Common-Mode Rejection Ratio | $V_S = \pm 15V$, $V_{CM} = \pm 12V$; $V_S = \pm 5V$, $V_{CM} = \pm 2.5V$ $0^\circ C$ to $70^\circ C$ | 92 90 | 100 | | dB dB |
| PSRR | Power Supply Rejection Ratio | $V_S = \pm 5V$ to $\pm 15V$ $0^\circ C$ to $70^\circ C$ | 80 80 | 90 | | dB dB |
| | Input Voltage Range ⁺ | $V_S = \pm 15V$ $V_S = \pm 5V$ | 12.0 2.5 | 14 4 | | V V |
| | Input Voltage Range ⁻ | $V_S = \pm 15V$ $V_S = \pm 5V$ | | -13 -3 | -12.0 -2.5 | V V |
| A_{VOL} | Large-Signal Voltage Gain | $V_S = \pm 15V$, $V_{OUT} = \pm 10V$, $R_L = 5k$ $0^\circ C$ to $70^\circ C$ | 4.0 3.5 | 8 | | V/mV V/mV |
| | | $V_S = \pm 15V$, $V_{OUT} = \pm 10V$, $R_L = 2k$ $0^\circ C$ to $70^\circ C$ | 3.0 2.5 | 6 | | V/mV V/mV |
| | | $V_S = \pm 5V$, $V_{OUT} = \pm 2.5V$, $R_L = 2k$ $0^\circ C$ to $70^\circ C$ | 2.5 2.0 | 5 | | V/mV V/mV |
| | | $V_S = \pm 5V$, $V_{OUT} = \pm 2.5V$, $R_L = 1k$ $0^\circ C$ to $70^\circ C$ | 2.0 1.6 | 4 | | V/mV V/mV |
| V_{OUT} | Output Swing | $V_S = \pm 15V$, $R_L = 2k$, $0^\circ C$ to $70^\circ C$ $V_S = \pm 5V$, $R_L = 500\Omega$, $0^\circ C$ to $70^\circ C$ | 12.0 3.0 | 13.8 4.0 | | $\pm V$ $\pm V$ |
| I_{OUT} | Output Current | $V_S = \pm 15V$, $V_{OUT} = \pm 12V$, $0^\circ C$ to $70^\circ C$ $V_S = \pm 5V$, $V_{OUT} = \pm 3V$, $0^\circ C$ to $70^\circ C$ | 6 6 | 12 12 | | mA mA |
| SR | Slew Rate | $V_S = \pm 15V$, $A_{VCL} = -2$ (Note 3) $0^\circ C$ to $70^\circ C$ | 30 27 | 50 | | V/ μs V/ μs |
| | | $V_S = \pm 5V$, $A_{VCL} = -2$ (Note 3) $0^\circ C$ to $70^\circ C$ | 20 18 | 33 | | V/ μs V/ μs |
| | Full Power Bandwidth | $V_S = \pm 15V$, 10V Peak (Note 4) $V_S = \pm 5V$, 3V Peak (Note 4) | | 0.8 1.7 | | MHz MHz |
| GBW | Gain-Bandwidth | $V_S = \pm 15V$, $f = 0.1MHz$ $V_S = \pm 5V$, $f = 0.1MHz$ | | 12 9 | | MHz MHz |
| t_r , t_f | Rise Time, Fall Time | $V_S = \pm 15V$, $A_{VCL} = 1$, 10% to 90%, 0.1V $V_S = \pm 5V$, $A_{VCL} = 1$, 10% to 90%, 0.1V | | 18 23 | | ns ns |
| | Overshoot | $V_S = \pm 15V$, $A_{VCL} = 1$, 0.1V $V_S = \pm 5V$, $A_{VCL} = 1$, 0.1V | | 25 20 | | % % |
| | Propagation Delay | $V_S = \pm 15V$, 50% V_{IN} to 50% V_{OUT} $V_S = \pm 5V$, 50% V_{IN} to 50% V_{OUT} | | 18 23 | | ns ns |
| t_s | Settling Time | $V_S = \pm 15V$, 10V Step, 0.1%, $A_{VCL} = 1$ $V_S = \pm 5V$, 5V Step, 0.1%, $A_{VCL} = 1$ | | 330 300 | | ns ns |
| R_O | Output Resistance | $A_{VCL} = 1$, $f = 0.1MHz$ | | 1.1 | | Ω |
| | Crosstalk | $V_{OUT} = \pm 10V$, $R_L = 2k$ | | -110 | -100 | dB |
| I_S | Supply Current | Each Amplifier, $V_S = \pm 5V$ and $V_S = \pm 15V$ $0^\circ C$ to $70^\circ C$ | | 1 | 1.4 1.6 | mA mA |

Note 1: A heat sink may be required to keep the junction temperature below absolute maximum when the output is shorted indefinitely.

Note 2: Input offset voltage is pulse tested with automated test equipment and is exclusive of warm-up drift.

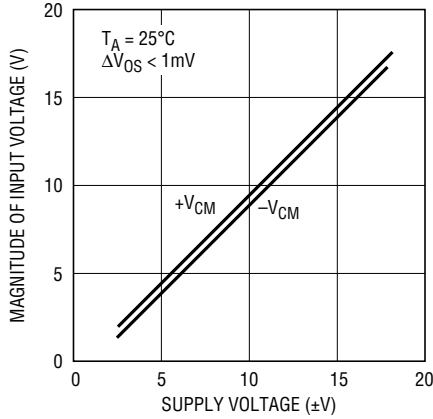
Note 3: Slew rate is measured in a gain of -2. For $\pm 15V$ supplies measure between $\pm 10V$ on the output with $\pm 6V$ on the input. For $\pm 5V$ supplies measure between $\pm 2V$ on the output with $\pm 1.75V$ on the input.

Note 4: Full power bandwidth is calculated from the slew rate measurement: $FPBW = SR/2\pi V_p$.

Note 5: Commercial grade parts are designed to operate over the temperature range of $-40^\circ C$ to $85^\circ C$ but are neither tested nor guaranteed beyond $0^\circ C$ to $70^\circ C$. Industrial grade parts specified and tested over $-40^\circ C$ to $85^\circ C$ are available on special request. Consult factory.

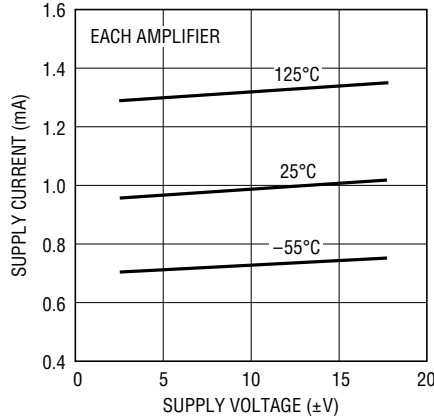
TYPICAL PERFORMANCE CHARACTERISTICS

Input Common-Mode Range vs Supply Voltage



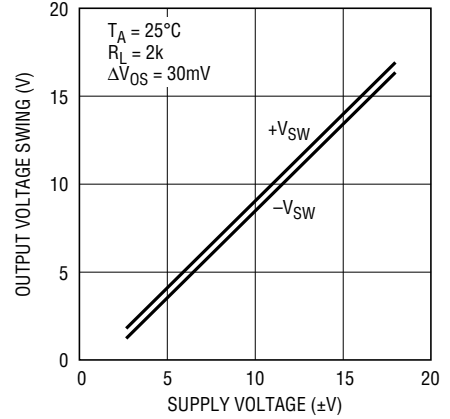
LT1201/02 G01

Supply Current vs Supply Voltage



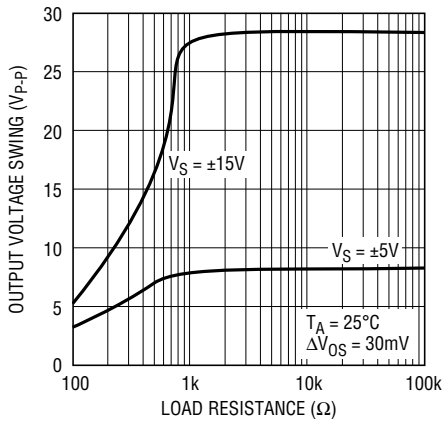
1201/02 G02

Output Voltage Swing vs Supply Voltage



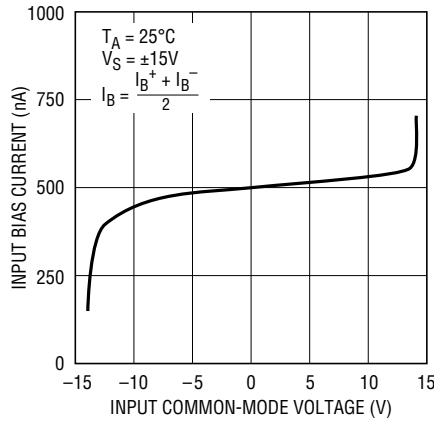
LT1201/02 G03

Output Voltage Swing vs Resistive Load



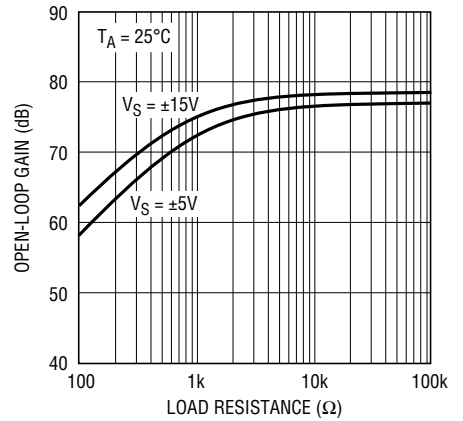
LT1201/02 G04

Input Bias Current vs Input Common-Mode Voltage



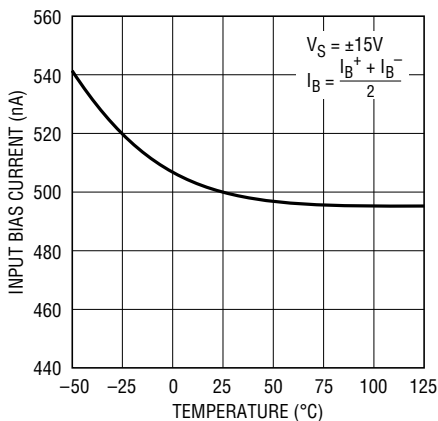
LT1201/02 G05

Open-Loop Gain vs Resistive Load



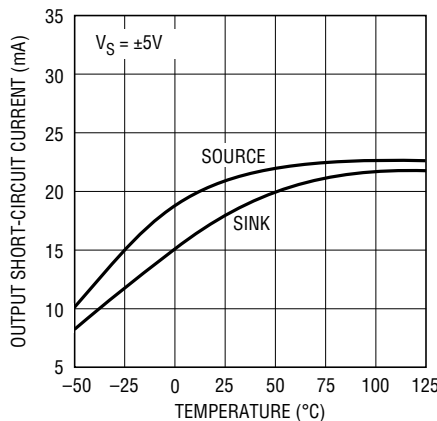
LT1201/02 G06

Input Bias Current vs Temperature



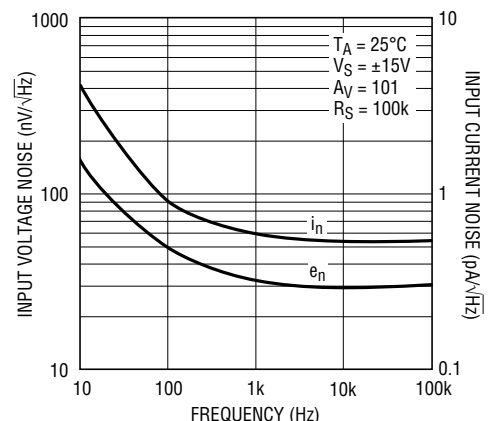
LT1201/02 G07

Output Short-Circuit Current vs Temperature



LT1201/02 G08

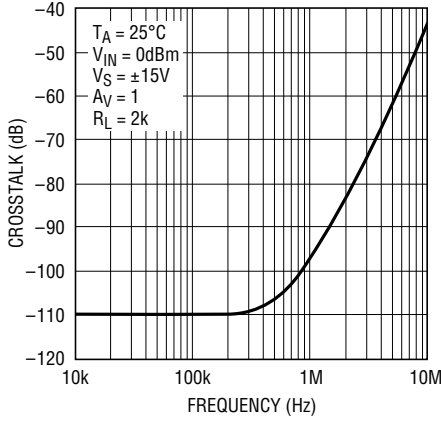
Input Noise Spectral Density



1201/02 G09

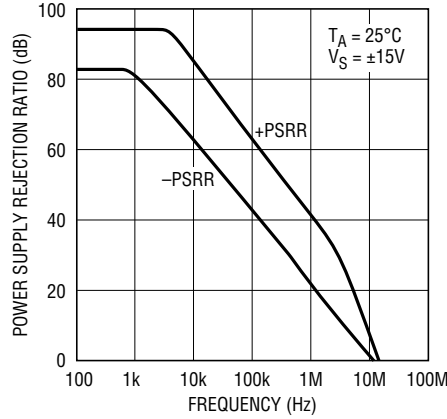
TYPICAL PERFORMANCE CHARACTERISTICS

Crosstalk vs Frequency



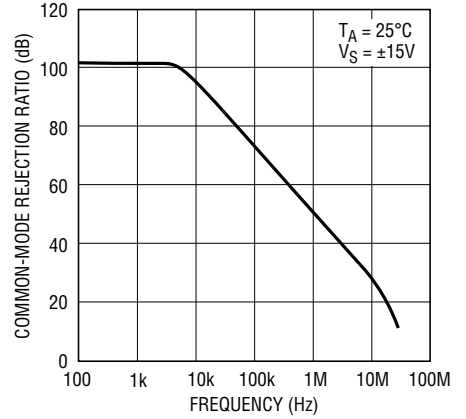
1201/02 G10

Power Supply Rejection Ratio vs Frequency



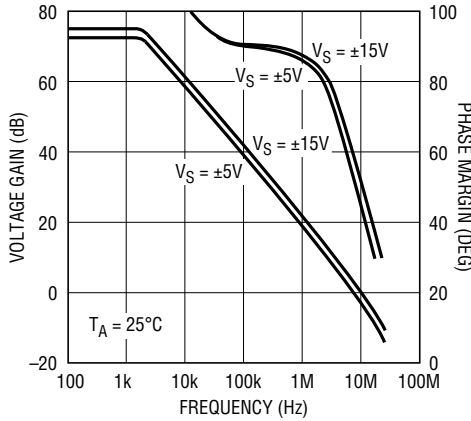
LT1201/02 G11

Common-Mode Rejection Ratio vs Frequency



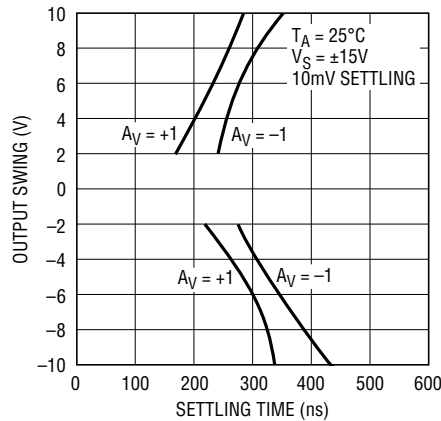
LT1201/02 G12

Voltage Gain and Phase vs Frequency



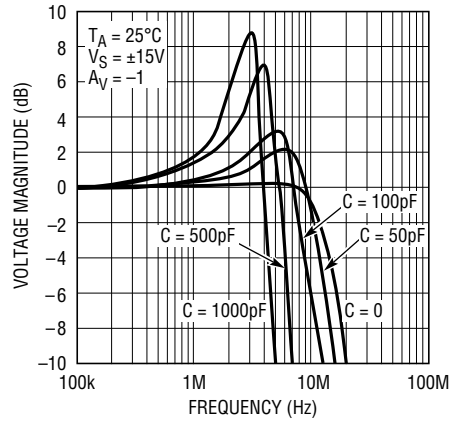
LT1201/02 G13

Output Swing vs Settling Time



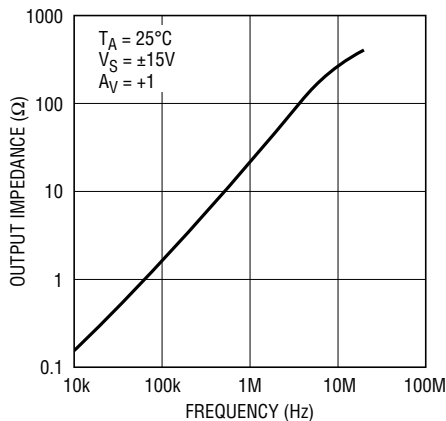
LT1201/02 G14

Frequency Response vs Capacitive Load



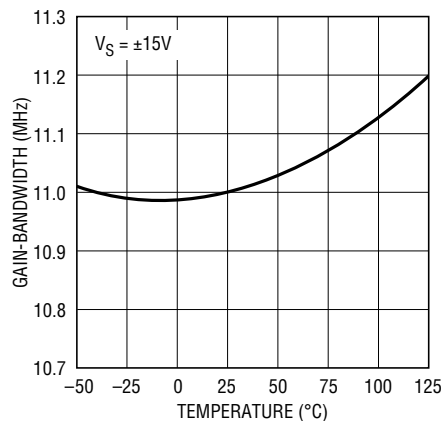
LT1201/02 G15

Closed-Loop Output Impedance vs Frequency



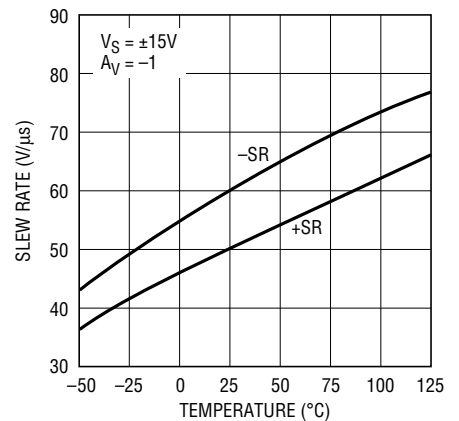
LT1201/02 G16

Gain-Bandwidth vs Temperature



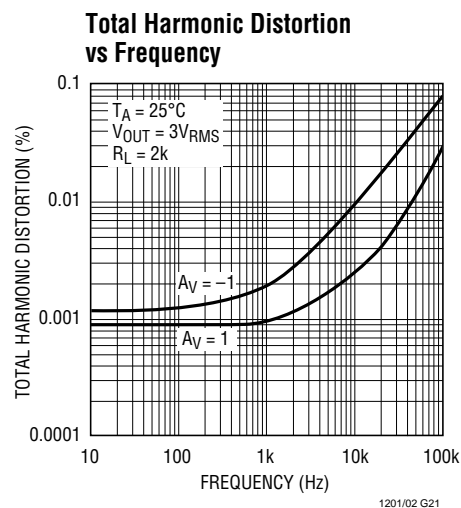
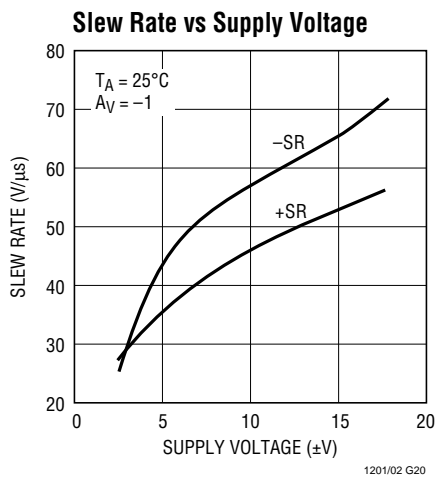
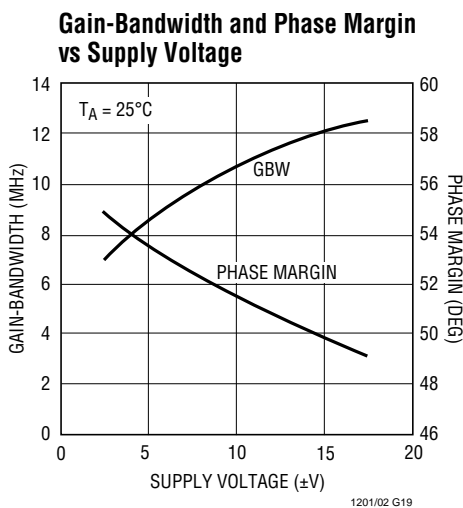
LT1201/02 G17

Slew Rate vs Temperature



LT1201/02 G18

TYPICAL PERFORMANCE CHARACTERISTICS



APPLICATIONS INFORMATION

Layout and Passive Components

As with any high speed operational amplifier, care must be taken in board layout in order to obtain maximum performance. Key layout issues include: use of a ground plane, minimization of stray capacitance at the input pins, short lead lengths, RF-quality bypass capacitors located close to the device (typically 0.01μF to 0.1μF) and low ESR bypass capacitors for high drive current applications (typically 1μF to 10μF tantalum). Sockets should be avoided when maximum frequency performance is required, although low profile sockets can provide reasonable performance up to 50MHz. For more details see Design Note 50. The parallel combination of the feedback resistor and gain setting resistor on the inverting input combine with the input capacitance to form a pole which can cause peaking. If feedback resistors greater than 5k are used, a parallel capacitor of value:

$$C_F \geq R_G \times C_{IN}/R_F$$

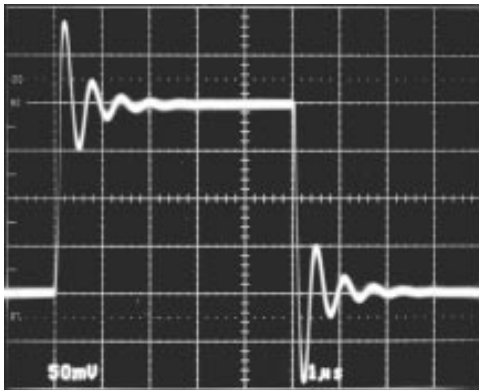
should be used to cancel the input pole and optimize dynamic performance. For unity-gain applications where a large feedback resistor is used, C_F should be greater than or equal to C_{IN} .

Capacitive Loading

The LT1201/LT1202 amplifiers are stable with all capacitive loads. This is accomplished by sensing the load induced output pole and adding compensation at the amplifier gain node. As the capacitive load increases, both the bandwidth and phase margin decrease so there will be peaking in the frequency domain and in the transient response. The photo of the small-signal response with 1000pF load shows 40% peaking. The large-signal response with a 10,000pF load shows the output slew rate being limited by the short-circuit current. To reduce peaking with capacitive loads, insert a small decoupling resistor between the output and the load, and add a capacitor between the output and inverting input to provide an AC feedback path. Coaxial cable can be driven directly, but for best pulse fidelity the cable should be doubly terminated with a resistor in series with the output. When driving a 150Ω load the minimum output current of 6mA limits the swing to ±0.9V.

APPLICATIONS INFORMATION

Small-Signal Capacitive Loading



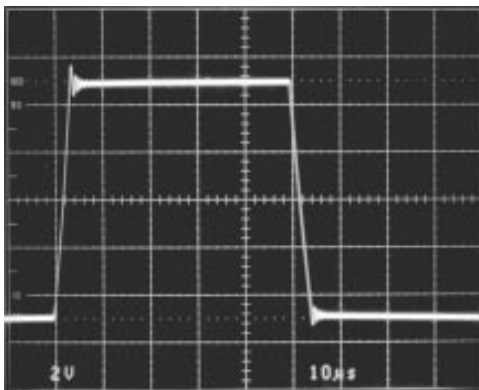
$A_V = -1$
 $C_L = 1000\text{pF}$

1201/02 AI01

caused by a second pole beyond the unity-gain crossover. This is reflected in the 50° phase margin and shows up as overshoot in the unity-gain small-signal transient response. Higher noise gain configurations exhibit less overshoot as seen in the inverting gain of one response.

The large-signal response in both inverting and non-inverting gain shows symmetrical slewing characteristics. Normally the noninverting response has a much faster rising edge due to the rapid change in input common-mode voltage which affects the tail current of the input differential pair. Slew enhancement circuitry has been added to the LT1201/LT1202 so that the falling edge slew rate is balanced.

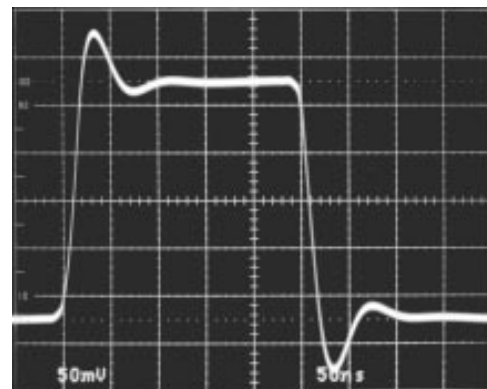
Large-Signal Capacitive Loading



$A_V = 1$
 $C_L = 10,000\text{pF}$

1201/02 AI02

Small-Signal Transient Response



$A_V = 1$

1201/02 AI03

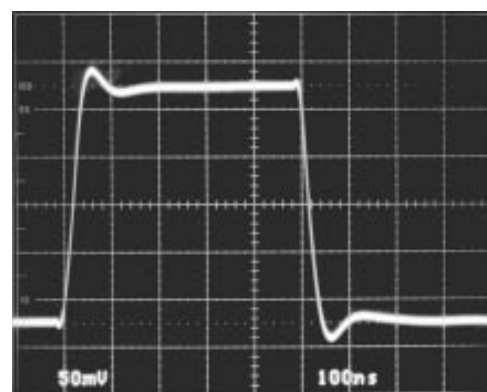
Input Considerations

Resistors in series with the inputs are recommended for the LT1201/LT1202 in applications where the differential input voltage exceeds $\pm 6\text{V}$ continuously or on a transient basis. An example would be in noninverting configurations with high input slew rates or when driving heavy capacitive loads. The use of balanced source resistance at each input is recommended for applications where DC accuracy must be maximized.

Transient Response

The LT1201/LT1202 gain-bandwidth is 12MHz when measured at 100kHz. The actual frequency response in unity-gain is considerably higher than 12MHz due to peaking

Small-Signal Transient Response

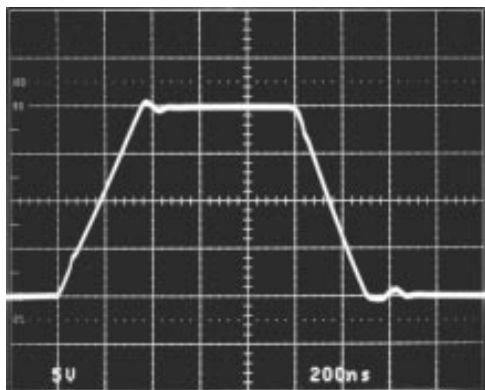


$A_V = -1$

1201/02 AI04

APPLICATIONS INFORMATION

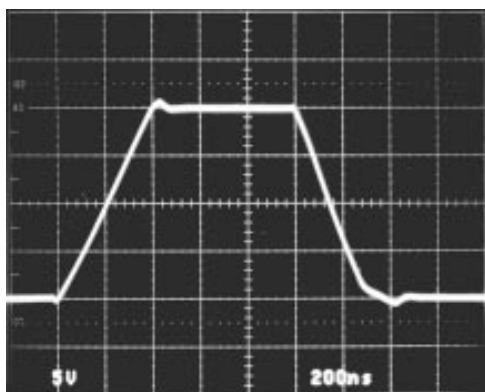
Large-Signal Transient Response



$A_V = 1$

1201/02 A105

Large-Signal Transient Response



$A_V = -1$

1201/02 A106

Low Voltage Operation

The LT1201/LT1202 are functional at room temperature with only 3V of total supply voltage. Under this condition, however, the undistorted output swing is only $0.8V_{P-P}$. A more realistic condition is operation at $\pm 2.5V$ supplies (or 5V and ground). Under these conditions at room temperature the typical input common-mode range is 2.2V to $-1.5V$, and a 1MHz, $2.5V_{P-P}$ sine wave can be accurately reproduced. With 5V total supply voltage the gain-bandwidth is reduced to 7MHz and the slew rate is reduced to $20V/\mu s$.

DAC Current-to-Voltage Converter

The wide bandwidth, high slew rate and fast settling time of the LT1201/LT1202 make them well suited for current-to-voltage conversion after current output D/A converters. A typical application with a DAC-08 type converter (full-scale output of 2mA) uses a 5k feedback resistor. A 12pF compensation capacitor across the feedback resistor is used to null the pole at the inverting input caused by the DAC output capacitance. The combination of the LT1201/LT1202 and DAC settles to less than 40mV (1LSB) in 500ns for a 0V to 10V step or for a 10V to 0V step.

Active Filters

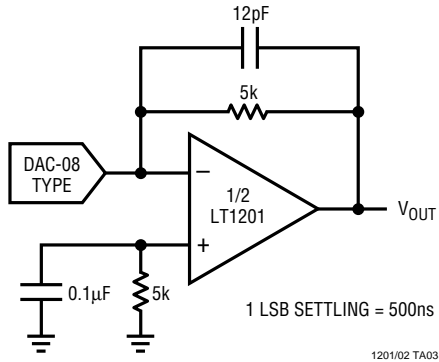
The LT1201/LT1202 are well suited to active filter applications such as the circuit shown on the front page of the data sheet. This particular example is a 4-pole Butterworth lowpass filter with a cutoff frequency of 100kHz. In choosing an amplifier for filter applications a good rule of thumb is:

$$f_0 \times Q < GBW/20$$

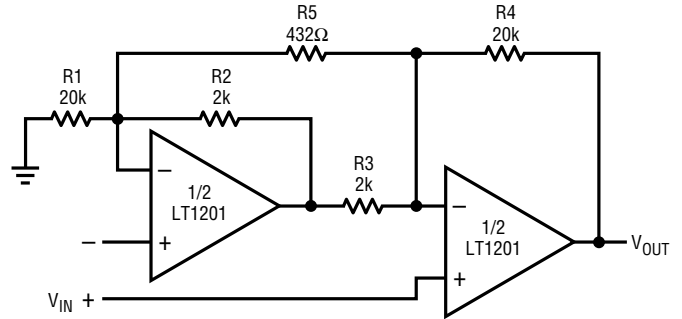
For our example the first section has $Q = 0.54$ and the second section has $Q = 1.31$, so the amplifier easily meets the gain-bandwidth requirement of 2.6MHz for $f_0 = 100kHz$. This multiple feedback configuration and the Sallen-Key configuration (as shown in the Typical Applications section) are the most commonly used topologies. The multiple feedback configuration has an advantage over the noninverting Sallen-Key configuration in many cases because the amplifier does not see a frequency varying common-mode voltage and high frequency output impedance is not critical. The result is better frequency performance beyond f_0 (for our particular example the stopband performance is dramatically better above 1MHz). Advantages of the Sallen-Key topology over the multiple feedback topology include: better gain accuracy, better DC accuracy, and unity-gain filters can be implemented more easily.

TYPICAL APPLICATIONS

DAC Current-to-Voltage Converter



Instrumentation Amplifier

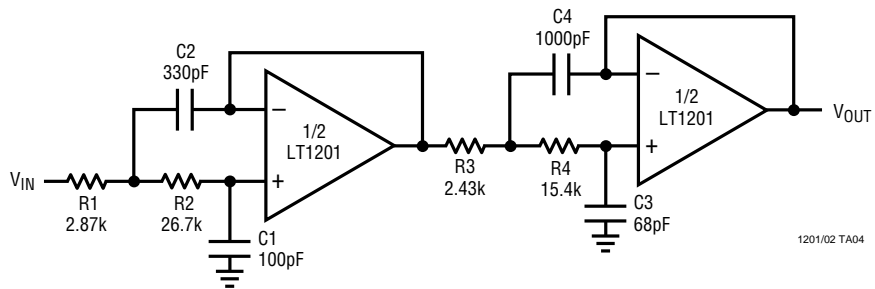


$$A_v = \frac{R4}{R3} \left[1 + \frac{1}{2} \left(\frac{R2}{R1} + \frac{R3}{R4} \right) + \frac{R2 + R3}{R5} \right] = 104$$

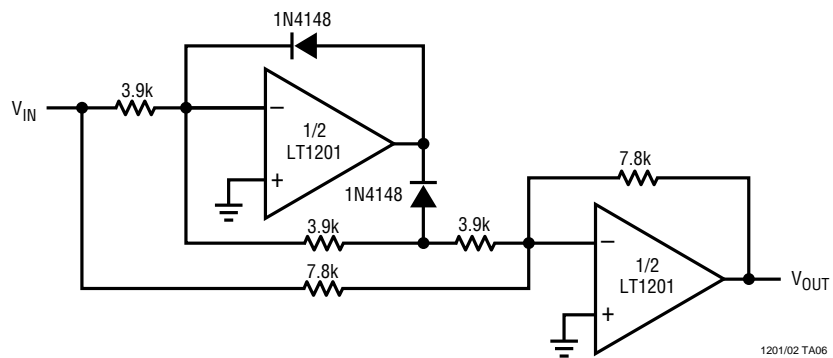
TRIM R5 FOR GAIN
 TRIM R1 FOR COMMON-MODE REJECTION
 BW = 120kHz

1201/02 TA05

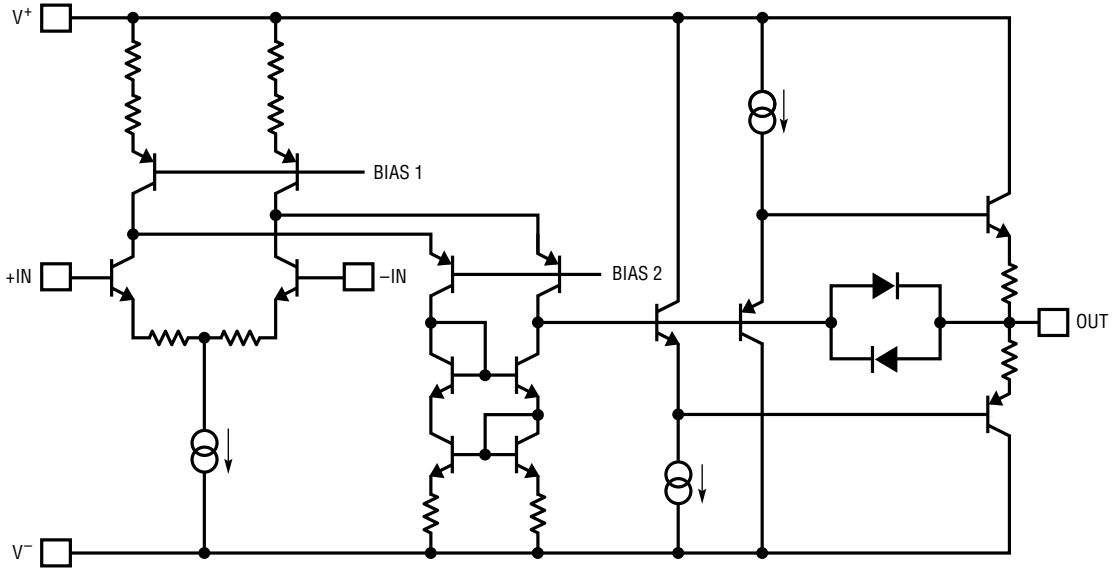
100kHz 4th Order Butterworth Filter (Sallen-Key)



Full-Wave Rectifier



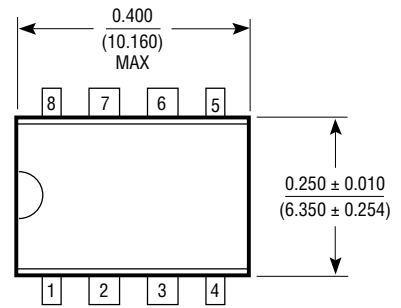
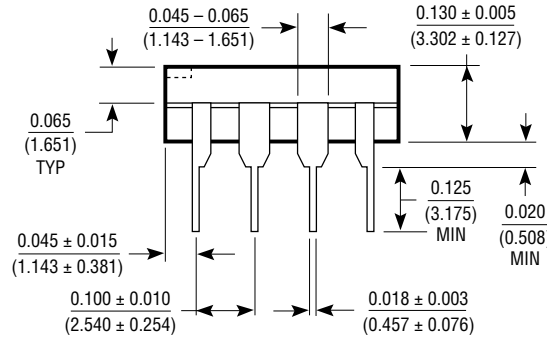
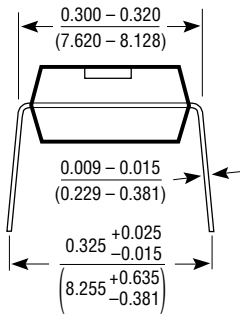
SIMPLIFIED SCHEMATIC One amplifier shown.



1201/02 SS

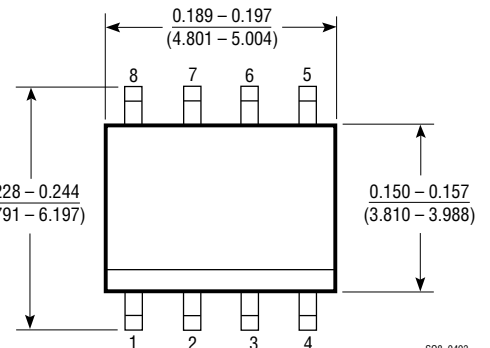
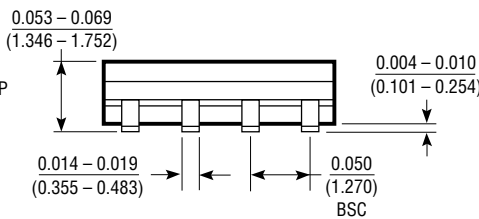
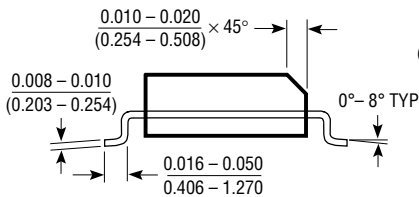
PACKAGE DESCRIPTION Dimensions in inches (millimeters) unless otherwise noted.

**N8 Package
8-Lead Plastic DIP**



N8 0392

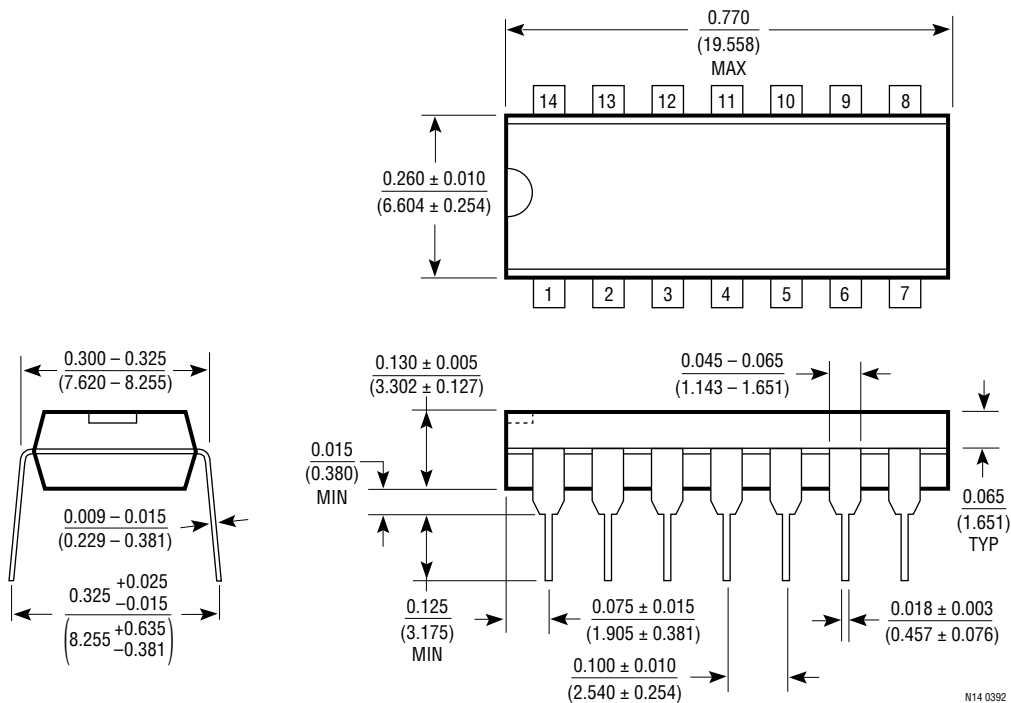
**S8 Package
8-Lead Plastic SOIC**



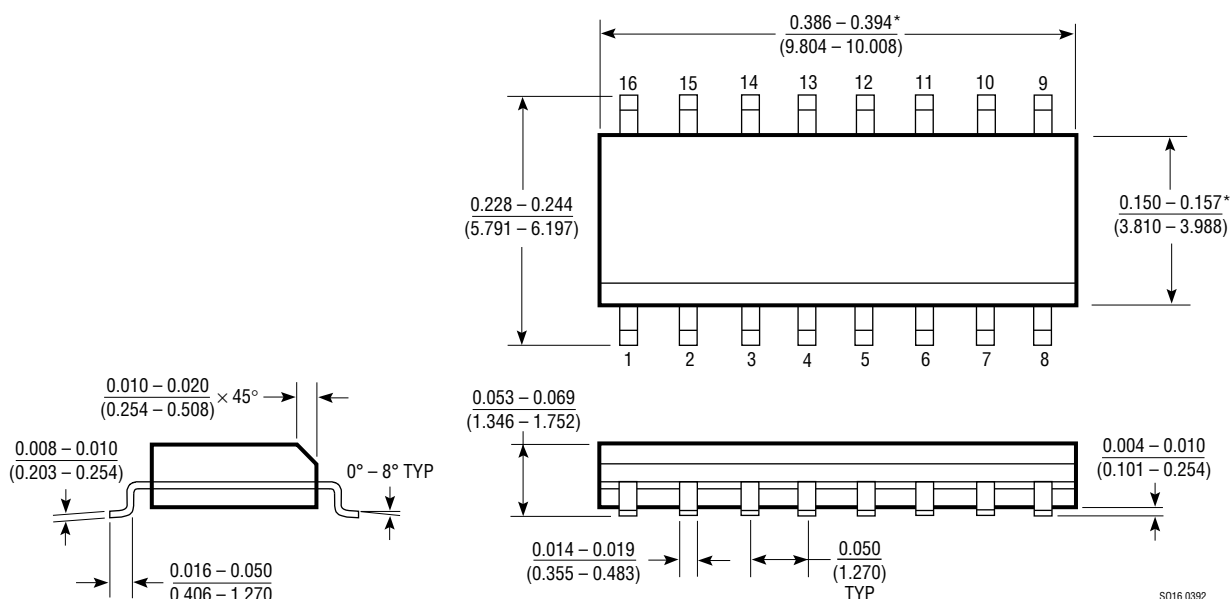
S08 0493

PACKAGE DESCRIPTION Dimensions in inches (millimeters) unless otherwise noted.

N Package
14-Lead Plastic DIP



S Package
16-Lead Plastic SOIC



*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.
MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.006 INCH (0.15mm).

U.S. Area Sales Offices

NORTHEAST REGION

Linear Technology Corporation
One Oxford Valley
2300 E. Lincoln Hwy., Suite 306
Langhorne, PA 19047
Phone: (215) 757-8578
FAX: (215) 757-5631

Linear Technology Corporation

266 Lowell St., Suite B-8
Wilmington, MA 01887
Phone: (508) 658-3881
FAX: (508) 658-2701

SOUTHEAST REGION

Linear Technology Corporation
17060 Dallas Parkway
Suite 208
Dallas, TX 75248
Phone: (214) 733-3071
FAX: (214) 380-5138

CENTRAL REGION

Linear Technology Corporation
Chesapeake Square
229 Mitchell Court, Suite A-25
Addison, IL 60101
Phone: (708) 620-6910
FAX: (708) 620-6977

SOUTHWEST REGION

Linear Technology Corporation
22141 Ventura Blvd.
Suite 206
Woodland Hills, CA 91364
Phone: (818) 703-0835
FAX: (818) 703-0517

NORTHWEST REGION

Linear Technology Corporation
782 Sycamore Dr.
Milpitas, CA 95035
Phone: (408) 428-2050
FAX: (408) 432-6331

International Sales Offices

FRANCE

Linear Technology S.A.R.L.
Immeuble "Le Quartz"
58 Chemin de la Justice
92290 Chatenay Malabry
France
Phone: 33-1-41079555
FAX: 33-1-46314613

GERMANY

Linear Technolgy GMBH
Untere Hauptstr. 9
D-8057 Eching
Germany
Phone: 49-89-3197410
FAX: 49-89-3194821

JAPAN

Linear Technology KK
5F YZ Bldg.
Iidabashi, Chiyoda-Ku
Tokyo, 102 Japan
Phone: 81-3-3237-7891
FAX: 81-3-3237-8010

KOREA

Linear Technology Korea Branch
Namsong Building, #505
Itaewon-Dong 260-199
Yongsan-Ku, Seoul
Korea
Phone: 82-2-792-1617
FAX: 82-2-792-1619

SINGAPORE

Linear Technology Pte. Ltd.
101 Boon Keng Road
#02-15 Kallang Ind. Estates
Singapore 1233
Phone: 65-293-5322
FAX: 65-292-0398

TAIWAN

Linear Technology Corporation
Rm. 801, No. 46, Sec. 2
Chung Shan N. Rd.
Taipei, Taiwan, R.O.C.
Phone: 886-2-521-7575
FAX: 886-2-562-2285

UNITED KINGDOM

Linear Technology (UK) Ltd.
The Coliseum, Riverside Way
Camberley, Surrey GU15 3YL
United Kingdom
Phone: 44-276-677676
FAX: 44-276-64851

World Headquarters

Linear Technology Corporation
1630 McCarthy Blvd.
Milpitas, CA 95035-7487
Phone: (408) 432-1900
FAX: (408) 434-0507

04/15/93