2SD1996

Silicon NPN epitaxial planer type

For low-voltage output amplification For muting For DC-DC converter

Features

- Low collector to emitter saturation voltage V_{CE(sat)}.
- Low ON resistance R_{on}.
- High foward current transfer ratio h_{FE}.
- Allowing supply with the radial taping.

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	25	V
Collector to emitter voltage	V_{CEO}	20	V
Emitter to base voltage	$V_{\rm EBO}$	12	V
Peak collector current	I_{CP}	1	A
Collector current	I_{C}	0.5	A
Collector power dissipation	P_{C}	600	mW
Junction temperature	T _j	150	°C
Storage temperature	T_{stg}	−55 ~ +150	°C

Unit: mm 0.65 max. $0.45^{+0.10}_{-0.05}$ $0.45^{+0.10}_{-0.05}$ 2.5±0.5 1:Emitter Note: In addition to the lead type shown in 2:Collector the upper figure, the 3:Base MT-1-A1 Package type as shown in the lower figure is also available. (HW type)

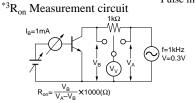
Electrical Characteristics (Ta=25°C)

Parameter	Symbol	Conditions min		typ	max	Unit
Collector cutoff current	I_{CBO}	$V_{CB} = 25V, I_{E} = 0$			100	nA
Collector to base voltage	V _{CBO}	$I_{\rm C} = 10\mu A, I_{\rm E} = 0$	25			V
Collector to emitter voltage	V _{CEO}	$I_{\rm C} = 1 \text{mA}, I_{\rm B} = 0$	20			V
Emitter to base voltage	V _{EBO}	$I_{\rm E} = 10 \mu A, I_{\rm C} = 0$	12			V
Forward current transfer ratio	h _{FE1} *1	$V_{CE} = 2V, I_C = 0.5A^{*2}$	200		800	
	h _{FE2}	$V_{CE} = 2V, I_C = 1A^{*2}$	60			
Collector to emitter saturation voltage	V _{CE(sat)}	$I_C = 0.5A, I_B = 20mA$		0.13	0.4	V
Base to emitter saturation voltage	V _{BE(sat)}	$I_C = 0.5A, I_B = 50mA$			1.2	V
Transition frequency	f_T	$V_{CB} = 10V, I_E = -50mA, f = 200MHz$		200		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10V, I_{E} = 0, f = 1MHz$		10		pF
ON resistanse	R _{on} *3			1.0		Ω

*1hFE1 Rank classification

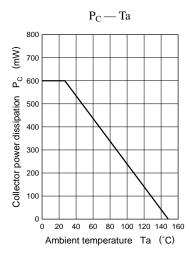
Rank	R	S	T
h _{FE1}	200 ~ 350	300 ~ 500	400 ~ 800

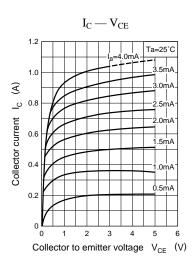
*2 Pulse measurement

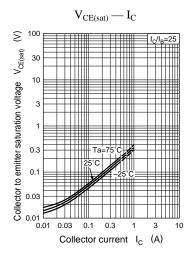


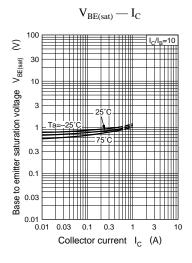
Panasonic 659

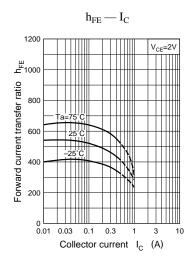
Transistor 2SD1996

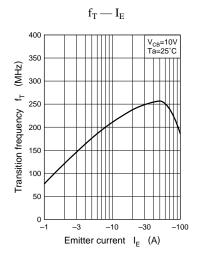


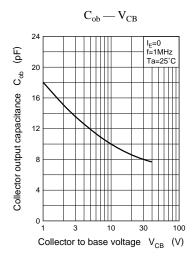


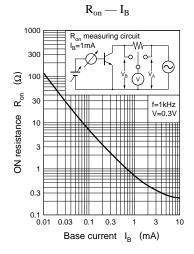












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