2SB860

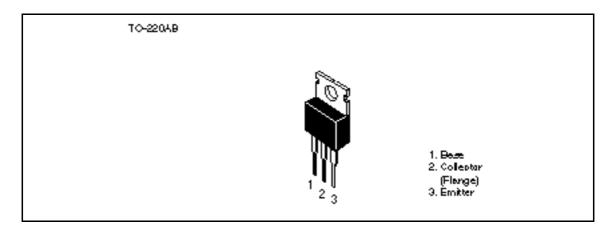
Silicon PNP Triple Diffused

HITACHI

Application

Low frequency power amplifier TV vertical deflection output complementary pair with 2SD1137

Outline



Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Rating	Unit
Collector to base voltage	V _{CBO}	-100	V
Collector to emitter voltage	V _{CEO}	-100	V
Emitter to base voltage	V_{EBO}	-4	V
Collector current	I _c	-4	A
Collector peak current	I _{C(peak)}	- 5	А
Collector power dissipation	P _c	1.8	W
	P _c *1	40	W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-45 to +150	°C

Note: 1. Value at $T_c = 25$ °C

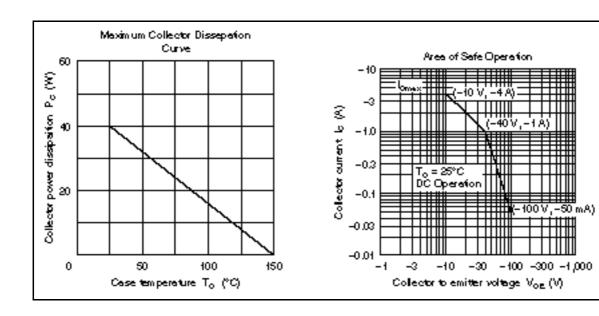


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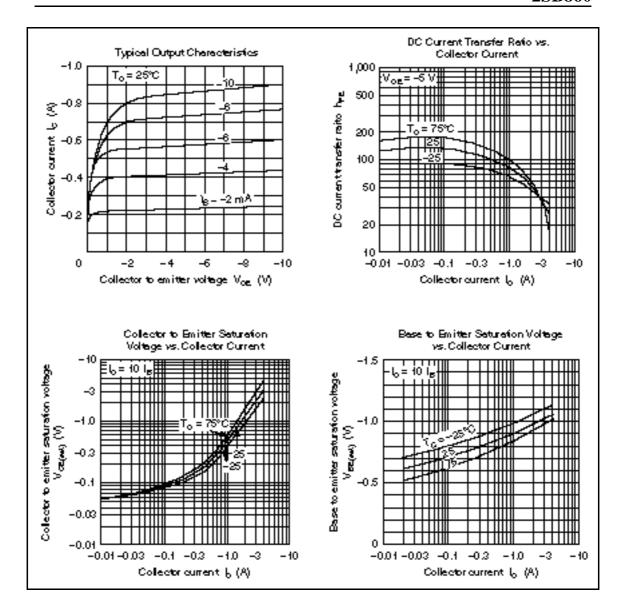
Electrical Characteristics ($Ta = 25^{\circ}C$)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to emitter breakdown voltage	$V_{(BR)CEO}$	-100	_	_	V	$I_{C} = -10 \text{ mA}, R_{BE} =$
Emitter to base breakdown voltage	$V_{(BR)EBO}$	-4	_	_	V	$I_{E} = -1 \text{ mA}, I_{C} = 0$
Collector cutoff current	I _{CEO}	_	_	-100	μΑ	$V_{CE} = -80 \text{ V}, R_{BE} =$
Emitter cutoff current	I _{EBO}	_	_	– 50	μΑ	$V_{EB} = -3.5 \text{ V}, I_{C} = 0$
Collector to emitter saturation voltage	$V_{\text{CE(sat)}}$	_	_	-1.0	V	$I_{\rm C} = -1 \text{ A}, I_{\rm B} = -0.1 \text{ A}^{*1}$
DC current transfer ratio	h _{FE}	50	_	250		$V_{CE} = -4 \text{ V} \qquad I_{C} = -0.5 \text{ A}^{*1}$
		25	_	350		$I_{\rm C} = -50 \text{ mA}$

Note: 1. Pulse test



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