TOSHIBA

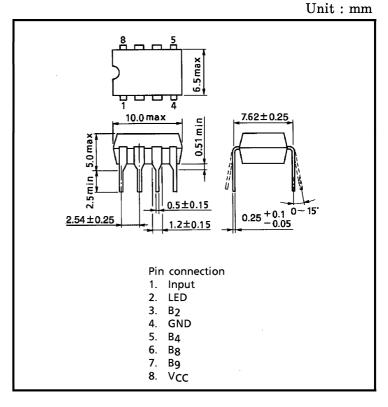
FIBER OPTIC TRANSMITTING PERIPHERAL IC

TA8513P

LED DRIVE CIRCUIT FOR OPTICAL **TRANSMITTION**

TTL interface

Data rate: Up to 20 Mb/s (NRZ code)



1. Maximum Ratings (Ta = 25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Storage Temperature	$\mathrm{T_{stg}}$	-55~150	°C
Operating Temperature	$T_{ m opr}$	-40~85	°C
Power Supply	v_{CC}	-0.5~7	V
Input Voltage	v_{IN}	$-0.5 \sim V_{\rm CC}$	V
LED Terminal Voltage	$ m V_{LED}$	$V_{CC} - 2.5 \sim V_{CC}$	V
Package Allowable Loss		0.9	W
Soldering Temperature	T_{sol}	260 (¹)	$^{\circ}\mathrm{C}$

(Note): (1) Soldering time ≤ 10 s (More than 1mm apart from the package).

2. Recommended Operating Conditions

PARAMETER	SYMBOL	MIN	TYP.	MAX	UNIT
Power Supply	v_{CC}	4.75	5.00	5.25	V
Data Rate		DC	_	20	Mb/s

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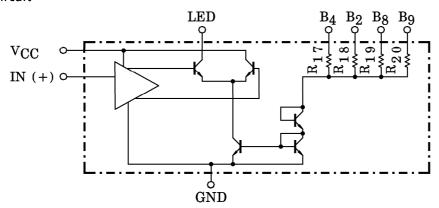
3. Electrical Characteristics (Ta = 25°C, V_{CC} = 5 V, V_{LED} = V_{CC} - 2.5V)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP.	MAX	UNIT	
	T	V _{CC} = V _{LED} = 5.25 V	$B_2, B_4, B_8, \\ B_9 = OPEN$	_	1.2	_	- mA	
			$V_{B9} = 5.25 V$	6	10	14		
Current Consumption	I_{CC}		$V_{B8} = 5.25 V$	15	26	37		
			$V_{B2} = 5.25 V$	29	42	55		
			$V_{B4} = 5.25 V$	45	64	83]	
Current Limiting Resistor	R ₁₇			_	1.8	3 —		
	R ₁₈		_	3	_	$\mathbf{k}\Omega$		
	R ₁₉			_	4.9	_	K32	
	R_{20}			_	14.8	_		
LED Output Current	${ m I_{LED}}$	$V_{B9} = 5.0 V$		5	7	9		
		$V_{B8} = 5.0 V$		15	20	25	mA	
		$V_{B2} = 5.0 V$		24	32	40		
		$V_{B4} = 5.0 \text{ V}$		37	50	63		
LED Cut-off Current	$I_{ m off}$			_		12	μ A	

4. Input Logic Part (Ta = 25°C, V_{CC} = 5 V, V_{LED} = V_{CC} - 2.5 V)

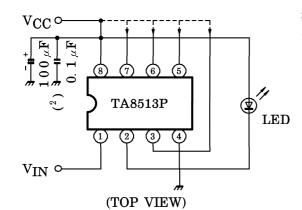
	PARAMETER	SYMBOL	CONDITIONS	MIN	TYP.	MAX	UNIT
	Low Level Input Current	${ m I}_{ m IL}$	$V_{ m IL} = 0.4 m V$	_	_	-0.4	mA
	High Level Input	Terr	$ m V_{IH} = 2.4~V$		_	40	
/D/DIT	Current	${ m I}_{ m IH}$	$ m V_{IH} = 2.7 V$	_	_	20	μA
TTL Input	Maximum High Level	TTTT3 # A 37	$V_{IH} = V_{CC} = 5 V$			10	μ A
Unit	Input Current	I _{IHMAX}	1H - 1CC - 2 1				μ A
	Low Level Input Voltage	$ m v_{IL}$			_	0.8	V
	High Level Input Voltage	$ m V_{IH}$		2.0	_		V
	Input Clamp Voltage	$ m v_{IK}$	$V_{CC} = 4.75 \text{V}, I_{IL} = -10 \text{mA}$		_	-1.5	V

5. Equivalent Circuit



6. Application Circuit

Example of a recommended circuit (VLED $\leq 2.5 \, \text{V}$)



See item next figure for connection method of pins No.3, 5, 6 and 7.

Pin No.	LED OUTPUT CURRENT ($V_{ m CC}=5~{ m V}$, Turn ON, Typ.)
3	32 mA
5	50 mA
6	20 mA
7	7 mA

(2) Install 0.1 μ F capacitor within 5 mm from No.8 pin and 100 μ F capacitor within 15 mm from No.8 pin.

7. IC Logic

INPUT LEVEL	OPTICAL OUTPUT (LED OUTPUT CURRENT)
Hi	ON
Lo	OFF

8. Precautions for Operation

- (1) The maximum ratings show the limits, which must not be exceeded even momentarily regardless of the external condition.
 - Operation beyond the limit of the maximum rating may cause failure of the devices. Therefore, special attention should be given to the maximum ratings.
- (2) Do not use acid or alkaline soldering flux cleaner solvent.
- (3) Ground all GND pins.