# Single USB Switch with Autoreset and Fault Blanking in Tiny QFN 

## General Description

The MAX1946 single current-limited switch with autoreset supplies a guaranteed 500 mA load in accordance with USB specifications. The MAX1946 operates from a 2.7 V to 5.5 V input supply and consumes only $40 \mu \mathrm{~A}$ of quiescent current when operating and only $3 \mu \mathrm{~A}$ in shutdown. Selectable active-high/active-low control logic and shutdown control provide additional flexibility. An autoreset feature latches the switch off in the event of a short circuit, saving system power. The switch reactivates upon removal of the shorted condition.

The MAX1946 provides several safety features to protect the USB port. Built-in thermal-overload protection turns off the switch when the die temperature exceeds $+160^{\circ} \mathrm{C}$. Accurate internal current-limiting circuitry protects the input supply against both overload and shortcircuit conditions. An open-drain fault signal, ( $\overline{\mathrm{FAULT}}$ ), notifies the microprocessor ( $\mu \mathrm{P}$ ) when a thermal overload, current-limit, undervoltage lockout (UVLO), or short-circuit fault occurs. A 20ms fault-blanking feature enables the circuit to ignore momentary faults, such as those caused when hot-swapping a capacitive load, preventing false alarms to the host system. The faultblanking feature prevents fault signals from being issued when the device powers up the load.
The MAX1946 is available in a space-saving 8-pin thin QFN package and operates over the extended $\left(-40^{\circ} \mathrm{C}\right.$ to $+85^{\circ} \mathrm{C}$ ) temperature range.

## Applications

USB Ports and USB Hubs
Notebook and Desktop Computers
PDAs and Palmtop Computers
Digital Cameras
Docking Stations
Pin Configuration


- Single USB Switch in $3 \mathrm{~mm} \times 3 \mathrm{~mm}$ Package
- Autoreset Feature Saves System Power
- Guaranteed 500mA Load Current
- Built-In 20ms Fault-Blanking Circuitry
- Active-High or Active-Low Control Logic
- Fully Compliant to USB Specifications
- 2.7V to 5.5V Input Voltage Range
- Fault-Indicator Output
- Thermal-Overload Protection
- UL Certification Pending


## Ordering Information

| PART | TEMP RANGE | PIN-PACKAGE |
| :---: | :--- | :--- |
| MAX1946ETA | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 8 Thin QFN $3 \mathrm{~mm} \times 3 \mathrm{~mm}$ |

Typical Operating Circuit


## Single USB Switch with Autoreset and Fault Blanking in Tiny QFN

## ABSOLUTE MAXIMUM RATINGS

IN, ON, OUT, SEL to GND.......................................-0.3V to +6V FAULT to GND -0.3 V to $(\mathrm{V}$ IN $+0.3 \mathrm{~V})$ IN to OUT.<br>$\qquad$ OUT Continuous Switch Current (internally limited)..............1.3A<br>FAULT DC Current. .10 mA

| Continuous Power Dissipation ( $\left.\mathrm{T}_{\mathrm{A}}=+70^{\circ} \mathrm{C}\right)$ |  |
| :---: | :---: |
| Exposed Pad Must be Soldered to PC Board |  |
| 8-Pin QFN (derate $24.4 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $+70^{\circ} \mathrm{C}$ ) | 1mW |
| Operating Temperature Range | .. $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Junction Temperature | $+150^{\circ} \mathrm{C}$ |
| Storage Temperature Range | $65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| Lead Temperature (soldering | $300^{\circ}$ |

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

$\left(V_{I N}=5 \mathrm{~V}, \mathrm{CIN}_{\mathrm{IN}}=0.1 \mu \mathrm{~F}\right.$, COUT $=1 \mu \mathrm{~F}, \mathbf{T}_{\mathbf{A}}=\mathbf{0}^{\circ} \mathbf{C}$ to $+\mathbf{8 5}{ }^{\circ} \mathbf{C}$, unless otherwise noted. Typical values are at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.) (Note1)

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply Voltage Range | VIN |  | 2.7 |  | 5.5 | V |
| Switch On-Resistance | Ron | $\mathrm{V}_{\text {IN }}=5, \mathrm{~T}_{\text {A }}=+25^{\circ} \mathrm{C}$ |  | 75 | 90 | $\mathrm{m} \Omega$ |
|  |  | $\mathrm{V}_{\text {IN }}=5 \mathrm{~V}$ |  |  | 110 |  |
|  |  | $\mathrm{V}_{\text {IN }}=3.3 \mathrm{~V}$ |  |  | 125 |  |
| Standby Supply Current |  | Switch disabled |  | 3 | 10 | $\mu \mathrm{A}$ |
| Quiescent Supply Current | IIN | Switch enabled, IOUT = 0 |  | 40 | 60 | $\mu \mathrm{A}$ |
| OUT Off-Leakage Current | ILKG | Switch disabled, V ${ }_{\text {OUT }}=0, \mathrm{~T}_{\text {A }}=+25^{\circ} \mathrm{C}$ |  | 0.01 | 1 | $\mu \mathrm{A}$ |
|  |  | Switch disabled, VOUT $=0, \mathrm{~T}_{\text {A }}=+85^{\circ} \mathrm{C}$ |  | 0.2 |  |  |
| Undervoltage Lockout Threshold | VULVO | Rising edge, 3\% hysteresis | 2.3 | 2.5 | 2.7 | V |
| Continuous Load Current |  |  | 500 |  |  | mA |
| Continuous Current Limit | ILIM | VIN_ - VOUT $=0.5 \mathrm{~V}$ | 0.74 | 1.1 | 1.20 | A |
| Short-Circuit Current Limit | Isc | Vout $=0$ (lout pulsing) | 0.8 | 1.2 | 1.6 | APK |
|  |  |  | 0.35 |  |  | ARMS |
| Short-Circuit Detect Threshold |  | (Note 2) |  | 1 |  | V |
| Continuous Current-Limit Blanking Timeout Period |  | From continuous current-limit condition to FAULT asserted | 10 | 20 | 35 | ms |
| Short-Circuit Blanking Timeout Period |  | From short-circuit current-limit condition to FAULT asserted | 7.5 | 18 | 35.0 | ms |
| Turn-On Delay | ton | ROUT $=10 \Omega$, time from ON to $10 \%$ of VOUT, does not include rise time | 0.25 | 0.6 | 1.50 | ms |
| Output Rise Time | trise | ROUT $=10 \Omega$, from $10 \%$ to $90 \%$ of VOUT |  | 1.2 |  | ms |
| Turn-Off Delay | toff | ROUT $=10 \Omega$, time from ON to $90 \%$ of VOUT, does not include fall time |  | 0.8 | 2 | ms |
| Output Fall Time | tFALL | ROUT $=10 \Omega$, from $90 \%$ to $10 \%$ of VOUT |  | 3 |  | ms |
| Thermal-Shutdown Threshold |  | $15^{\circ} \mathrm{C}$ hysteresis |  | 160 |  | ${ }^{\circ} \mathrm{C}$ |

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## ELECTRICAL CHARACTERISTICS (continued)

$\left(\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{C}_{\mathrm{IN}}=0.1 \mu \mathrm{~F}, \mathrm{COUT}=1 \mu \mathrm{~F}, \mathbf{T}_{\mathbf{A}}=\mathbf{0}^{\circ} \mathbf{C}\right.$ to $+\mathbf{8 5} \mathbf{5}^{\circ} \mathbf{C}$, unless otherwise noted. Typical values are at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.) (Note1)

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ON, SEL Input High Level | VIH | $\mathrm{V}_{\text {IN }}=2.7 \mathrm{~V}$ to 4 V | 1.6 |  |  | V |
|  |  | $\mathrm{V}_{\mathrm{IN}}=4 \mathrm{~V}$ to 5.5 V | 2 |  |  |  |
| ON, SEL Input Low Level | VIL | V IN $=2.7 \mathrm{~V}$ to 4V |  |  | 0.6 | V |
|  |  | $\mathrm{V}_{\text {IN }}=4 \mathrm{~V}$ to 5.5 V |  |  | 0.8 |  |
| ON, SEL Input Leakage Current |  | $\mathrm{V}_{\text {ON, }} \mathrm{V}_{\text {SEL }}=0$ or $\mathrm{V}_{\text {IN }}, \mathrm{T}_{\text {A }}=+25^{\circ} \mathrm{C}$ | -1 |  | +1 | $\mu \mathrm{A}$ |
| $\overline{\text { FAULT Output Low Voltage }}$ | VOL | $\mathrm{ISINK}=1 \mathrm{~mA}, \mathrm{~V}_{\text {IN }}=2.7 \mathrm{~V}$ |  |  | 0.5 | V |
| FAULT Output High Leakage Current |  | $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {FAULT }}=5.5 \mathrm{~V}$ |  | 1 |  | $\mu \mathrm{A}$ |
| OUT Output Current Autoreset Mode |  | In latched off state, VOUT = 0 | 10 | 25 | 45 | mA |
| OUT Autoreset Threshold |  | In latched off state, Vout rising | 0.4 | 0.5 | 0.6 | V |
| OUT Autoreset Blanking Time |  | In latched off state, Vout > 0.5V | 10 | 20 | 35 | ms |

## ELECTRICAL CHARACTERISTICS

$\left(V_{I N}=5 \mathrm{~V}, \mathrm{C}_{I N}=0.1 \mu \mathrm{~F}, \mathrm{COUT}=1 \mu \mathrm{~F}, \mathbf{T}_{\mathbf{A}}=\mathbf{- 4 0 ^ { \circ }} \mathbf{C}\right.$ to $+\mathbf{8 5}{ }^{\circ} \mathbf{C}$, unless otherwise noted.) (Note1)

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Supply Voltage Range | VIN |  | 2.7 | 5.5 | V |
| Switch On-Resistance | Ron | $\mathrm{V}_{\text {IN }}=5, \mathrm{~T}_{\text {A }}=+25^{\circ} \mathrm{C}$ |  | 90 | $\mathrm{m} \Omega$ |
|  |  | $\mathrm{V}_{\text {IN }}=5 \mathrm{~V}$ |  | 110 |  |
|  |  | $\mathrm{V}_{\text {IN }}=3.3 \mathrm{~V}$ |  | 125 |  |
| Standby Supply Current |  | Switch disabled |  | 10 | $\mu \mathrm{A}$ |
| Quiescent Supply Current | IIN | Switch enabled, IJUT = 0 |  | 60 | $\mu \mathrm{A}$ |
| OUT Off-Leakage Current | ILKG | Switch disabled, VOUT $=0, \mathrm{~T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+25^{\circ} \mathrm{C}$ |  | 1 | $\mu \mathrm{A}$ |
| Undervoltage Lockout Threshold | VULVo | Rising edge, 3\% hysteresis | 2.3 | 2.7 | V |
| Continuous Load Current |  |  | 500 |  | mA |
| Continuous Current Limit | ILIM | $\mathrm{V}_{\text {IN_ }}-\mathrm{V}_{\text {OUT }}=0.5 \mathrm{~V}$ | 0.6 | 1.3 | A |
| Short-Circuit Current Limit | ISC | VOUT = 0 (lout pulsing) | 0.8 | 1.6 | APK |
| Continuous Current-Limit Blanking Timeout Period |  | From continuous current-limit condition to $\overline{\text { FAULT }}$ asserted | 10 | 35 | ms |
| Short-Circuit Blanking Timeout Period |  | From short-circuit current-limit condition to FAULT asserted | 7.5 | 35.0 | ms |
| Turn-On Delay | ton | Rout $=10 \Omega$. Time from ON to $10 \%$ of Vout. Does not include rise time. | 0.25 | 1.50 | ms |

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## ELECTRICAL CHARACTERISTICS (continued)

$\left(\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{C}_{\mathrm{IN}}=0.1 \mu \mathrm{~F}\right.$, COUT $=1 \mu \mathrm{~F}, \mathbf{T}_{\mathbf{A}}=\mathbf{- 4 0 ^ { \circ }} \mathbf{C}$ to $\mathbf{+ 8 5}{ }^{\circ} \mathbf{C}$, unless otherwise noted.) (Note1)

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Turn-Off Delay | toff | ROUT $=10 \Omega$, time from ON to $90 \%$ of VOUT, does not include fall time |  | 2 | ms |
| ON, SEL Input High Level | $\mathrm{V}_{\mathrm{IH}}$ | $\mathrm{V}_{\text {IN }}=2.7 \mathrm{~V}$ to 4V | 1.6 |  | V |
|  |  | $\mathrm{V}_{\text {IN }}=4 \mathrm{~V}$ to 5.5 V | 2 |  | V |
| ON, SEL Input Low Level | VIL | $\mathrm{V}_{\text {IN }}=2.7 \mathrm{~V}$ to 4V |  | 0.6 | V |
|  |  | $\mathrm{V}_{\text {IN }}=4 \mathrm{~V}$ to 5.5 V |  | 0.8 | V |
| ON, SEL Input Leakage Current |  | $\mathrm{V}_{\text {ON }}$, V $\mathrm{SELEL}=0$ or $\mathrm{V}_{\text {IN }}$ | -1 | +1 | $\mu \mathrm{A}$ |
| FAULT Output Low Voltage | VOL | $\mathrm{ISINK}=1 \mathrm{~mA}, \mathrm{~V}_{\text {IN }}=2.7 \mathrm{~V}$ |  | 0.5 | V |
| OUT Output Current Autoreset Mode |  | In latched off state, Vout = 0 | 10 | 45 | mA |
| OUT Autoreset Threshold |  | In latched off state, V ${ }_{\text {OUT }}$ rising | 0.4 | 0.6 | V |
| OUT Autoreset Blanking Time |  | In latched off state, Vout $>0.5 \mathrm{~V}$ | 10 | 35 | ms |

Note 1: All parts are $100 \%$ tested at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$. Limits over the operating temperature range are guaranteed by design.
Note 2: Short-circuit detect threshold is the output voltage at which the device transitions from short-circuit current limit to continuous current limit.

## Typical Operating Characteristics

(Circuit of Figure 2, $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{CIN}=0.1 \mu \mathrm{~F}, \mathrm{COUT}=1 \mu \mathrm{~F}, \mathrm{ON}=\mathrm{SEL}, \mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise noted.)


## Single USB Switch with Autoreset and Fault Blanking in Tiny QFN

Typical Operating Characteristics (continued)

(Circuit of Figure 2, $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{CIN}_{\mathrm{I}}=0.1 \mu \mathrm{~F}, \mathrm{COUT}=1 \mu \mathrm{~F}, \mathrm{ON}=\mathrm{SEL}, \mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise noted.)


## Single USB Switch with Autoreset and Fault Blanking in Tiny QFN

(Circuit of Figure 2, $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{CIN}^{2}=0.1 \mu \mathrm{~F}, \mathrm{COUT}=1 \mu \mathrm{~F}, \mathrm{ON}=\mathrm{SEL}, \mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise noted.)


A: Vout, $5 \mathrm{~V} / \mathrm{div}$
B: V $\begin{aligned} & \text { FAULT } \\ & \text {, } \\ & \text { V/div }\end{aligned}$
C: $\mathrm{I}_{\mathrm{N}}, 1 \mathrm{~A} / \mathrm{div}$
D: Iout, 1A/div
SHORT-CIRCUIT RESPONSE INTO $0 \Omega$ (EXPANDED TIME SCALE)


A: Vout, 5V/div
B: $V_{\text {FAULT }}, 5 \mathrm{~V}$ div
C: In , 2A/div
D: lout, 10A/div

## Single USB Switch with Autoreset and Fault Blanking in Tiny QFN

## Typical Operating Characteristics (continued)

(Circuit of Figure 2, $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{C}_{\mathrm{IN}}=0.1 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{OUT}}=1 \mu \mathrm{~F}, \mathrm{ON}=\mathrm{SEL}, \mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise noted.)



A: $V_{0 n}, 2 \mathrm{~V} / \mathrm{div}$
$\mathrm{B}: \mathrm{V}_{0 U T}, 2 \mathrm{~V} / \mathrm{div}$



A: Vout, 5V/div
B: V $\begin{aligned} & \text { FAULT } \\ & , 5 \mathrm{~V} / \text { div }\end{aligned}$
C: Vout, 2V/div
D: Iout, 500mA/div

# Single USB Switch with Autoreset and Fault Blanking in Tiny QFN 

## Typical Operating Characteristics (continued)

(Circuit of Figure 2, $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{CIN}_{\mathrm{IN}}=0.1 \mu \mathrm{~F}, \mathrm{COUT}=1 \mu \mathrm{~F}, \mathrm{ON}=\mathrm{SEL}, \mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise noted.)


Pin Description

| PIN | NAME | FUNCTION |
| :---: | :---: | :--- |
| 1 | $\overline{\text { FAULT }}$ | Fault-Indicator Output Switch. Open-drain output asserts low when switch enters thermal shutdown, <br> undervoltage lockout, or a sustained ( $>20 \mathrm{~ms}$ ) current-limit or short-circuit condition. |
| 2 | GND | Ground |
| 3,4 | OUT | Switch Power Output. Connect OUT pins together at the device and bypass with a 1uF ceramic <br> capacitor. Load conditions may require additional bulk capacitance. When disabled, OUT goes into <br> a high-impedance state. |
| 5,6 | IN | Switch Power Input. Connect IN pins together at the device and bypass with a 0.1رF ceramic <br> capacitor to GND. Input conditions may require additional bulk capacitance to prevent pulling the <br> input supply down. |
| 7 | ON | ON/OFF Control Input. The active polarity of ON is set by SEL. Connect SEL high to make ON active <br> high. Connect SEL to GND to make ON active low. |
| 8 | SEL | Logic Input Polarity Select. SEL sets the active polarity of the ON input. Connect SEL high to make <br> ON active high. Connect SEL to GND to make ON active low. |

## Detailed Description

The MAX1946 includes output current limiting, short-circuit protection, thermal shutdown, an enable input, and fault indicator (see the Functional Diagram). A logic input at SEL sets the active polarity of the enable input. The fault indicator notifies the system when the currentlimit, short-circuit, undervoltage lockout, or thermalshutdown threshold is exceeded.
The MAX1946 operates from a 2.7 V to 5.5 V input supply and supplies a minimum output current of 740 mA .

A built-in current limit of 1.1A limits the output current in the event of an overload condition. A built-in short-circuit detection circuit pulses the output current if the output voltage falls below 1V. This lowers RMS output current and reduces power dissipation during continuous short conditions.
An internal micropower charge pump generates a highside supply that drives the gate of an internal $75 \mathrm{~m} \Omega$ NMOS switch.

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Figure 1. Functional Diagram

## On/Off Control and Undervoltage Lockout (UVLO)

SEL sets the active polarity of the logic input of the MAX1946. Connect ON to the same voltage as SEL to enable OUT. Connect ON to the opposite voltage as SEL to disable OUT (see Table 1). The output enters a high-impedance state when disabled.
The MAX1946 includes a UVLO circuit to prevent erroneous switch operation when the input voltage goes low during startup and brownout conditions. Input voltages less than 2.5 V inhibit operation of the device. FAULT asserts low during a UVLO condition.

Output Fault Protection and Autoreset The MAX1946 senses the switch output voltage and selects continuous current limiting for Vout greater than 1V or short-circuit current limiting for Vout less than 1V. When Vout is greater than 1 V , the device operates in a continuous current-limit mode that limits output current to 1.1A. When Vout is less than 1V, the device operates in short-circuit current-limit mode, sourcing 1.2A pulses to the load. When either fault condition persists for 20 ms , the output turns off and the fault flag asserts. The output automatically restarts 20 ms after the short or overload is removed.

Table 1. On/Off Control

| SEL | ON | OUT BEHAVIOR |
| :---: | :---: | :---: |
| GND | GND | ON |
|  | $\mathrm{VIN}^{2}$ | OFF |
| $\mathrm{V}_{\mathrm{IN}}$ | GND | OFF |
|  | $\mathrm{V}_{\text {IN }}$ | ON |

The MAX1946 detects short-circuit removal by sourcing 25 mA from the output and monitoring the output voltage. When the voltage at the output exceeds 0.5 V for 20ms, the fault flag resets, the output turns back on, and the 25 mA current source turns off.

Thermal Shutdown
When the MAX1946 die temperature exceeds $+160^{\circ} \mathrm{C}$ the switch turns off and FAULT goes low. Thermal shutdown does not utilize the 20ms fault-blanking timeout period. When the junction temperature cools by $15^{\circ} \mathrm{C}$ the switch turns on again and FAULT returns high. The switch cycles on and off if the overload condition persists, resulting in a pulsed output that reduces the average system load.

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## Fault Indicator

The MAX1946 provides an open-drain fault output, $\overline{F A U L T}$. Connect $\overline{\mathrm{FAULT}}$ to IN through a $100 \mathrm{k} \Omega$ pullup resistor for most applications. $\overline{\text { FAULT }}$ asserts low when any of the following conditions occur:

- The input voltage is below the UVLO threshold.
- The switch junction temperature exceeds the $+160^{\circ} \mathrm{C}$ thermal-shutdown temperature limit.
- The switch is in current-limit or short-circuit currentlimit mode for more than 20 ms .
The $\overline{\text { FAULT }}$ output deasserts after a 20ms delay once the fault condition is removed. Ensure that the MAX1946 input bypass capacitance is sufficiently large to prevent load glitches from triggering the FAULT output. Limit the input voltage slew rate to $0.2 \mathrm{~V} / \mu \mathrm{s}$ to prevent erroneous $\overline{\text { FAULT indications. }}$
To differentiate large capacitive loads from short circuits or sustained overloads, the MAX1946 has a faultblanking circuit. When a load transient causes the device to enter current limit, an internal counter monitors the duration of the fault. For load faults exceeding the 20 ms fault-blanking time, the switch turns off, $\overline{F A U L T}$ asserts low, and the device enters autoreset mode (see the Output Fault Protection and Autoreset section). Only current-limit and short-circuit faults are
blanked. Thermal-overload faults and input voltage drops below the UVLO threshold immediately turn the switch off and assert $\overline{F A U L T}$ low.
Fault blanking allows the MAX1946 to handle USB loads that may not be fully compliant with the USB specification. The MAX1946 successfully powers USB loads with additional bypass capacitance and/or large startup currents while protecting the upstream power source. No fault is reported if the switch brings up the load within the 20 ms blanking period. See Table 2 for a summary of current limit and fault behavior.


## Applications Information

## Typical Application Circuit Input Power Supply and Capacitance

Connect both IN inputs together. IN powers the internal control circuitry and charge pump for the switch. Bypass IN to GND with a $0.1 \mu \mathrm{~F}$ ceramic capacitor. When driving inductive loads or operating from inductive sources, which may occur when the MAX1946 is powered by long leads or PC traces, larger input bypass capacitance is required to prevent voltage spikes from exceeding the MAX1946's absolute maximum ratings during short-circuit events.

## Table 2. Current Limiting and Fault Behavior

| CONDITION | MAX1946 BEHAVIOR |
| :---: | :---: |
| Output Short Circuit (VOUT < 1V) | - If a short is detected at the output, the switch turns off, and the blanking timer begins. $\overline{\text { FAULT }}$ remains high during the blanking timeout period. <br> - If the short persists during the fault-blanking period, the output pulses at 0.35ARMS. If the short is removed before the 18 ms short-circuit blanking timeout period, the next ramped current pulse soft starts the output. $\overline{\text { FAULT }}$ remains high. <br> - If the short circuit persists after the fault-blanking period, $\overline{\text { FAULT }}$ goes low, autoreset mode begins, and the output sources 25 mA . <br> - If the output voltage rises above 0.5 V for 20 ms , the switch resets, the output turns on, and $\overline{\text { FAULT }}$ goes high (see Short-Circuit Response in the Typical Operating Characteristics.) |
| Output Overload Current (Vout > 1V) | - Output current regulates at ILIM and the blanking timer turns on. $\overline{\text { FAULT }}$ remains high during the blanking timeout period. <br> - Continuous current at ILIM persists until either the 20ms blanking period expires or a thermal fault occurs. <br> - If overcurrent persists after 20ms, $\overline{\text { FAULT }}$ goes low, autoreset mode is enabled, and the output sources 25 mA . <br> - If the output voltage rises above 0.5 V for 20 ms , the switch resets, the output turns on, and $\overline{\text { FAULT }}$ goes high (see Short Overload Response in the Typical Operating Characteristics.) |
| Thermal Fault ( $\mathrm{T} J>+160^{\circ} \mathrm{C}$ ) | - A junction temperature of $+160^{\circ} \mathrm{C}$ immediately asserts $\overline{\text { FAULT }}$ low (the blanking timeout period does not apply for thermal faults) and turns off the switch. When the junction cools by $15^{\circ} \mathrm{C}$, the thermal fault is cleared and FAULT goes high. Note that if other fault conditions are present when a thermal fault clears, those fault states then take effect. |

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Figure 2. Typical Application Circuit

## Output Capacitor

Bypass OUT to GND with a $1 \mu \mathrm{~F}$ ceramic capacitor for local decoupling. Additional bulk capacitance (up to $470 \mu \mathrm{~F}$ ) reduces output-voltage transients under dynamic load conditions. Using output capacitors greater than $470 \mu \mathrm{~F}$ can assert FAULT if the current limit cannot charge the output capacitor within the 20 ms faultblanking period. In addition to bulk capacitance, smallvalue ( $0.1 \mu \mathrm{~F}$ or greater) ceramic capacitors improve the output's resilience to electrostatic discharge (ESD).

## Driving Inductive Loads

A wide variety of devices (mice, keyboards, cameras, and printers) typically connect to the USB port with cables, which can add an inductive component to the load. This inductance causes the output voltage at the USB port to oscillate during a load step. The MAX1946 drives inductive loads, but avoid exceeding the device's absolute maximum ratings. The load inductance is usually relatively small, and the MAX1946 input typically includes substantial bulk capacitance from an upstream regulator as well as local bypass capacitors, limiting overshoot. If severe ringing occurs due to large load inductance, clamp the MAX1946 output below +6 V and above -0.3 V .

## Turn-On and Turn-Off Behavior

When turned on, the MAX1946 output ramps up over 1.2 ms to eliminate load transients on the upstream power source. When turned off, the output ramps down for 3 ms . Under fault conditions, the output of the MAX1946 turns off rapidly to provide maximum safety for the upstream power source and downstream devices. Internal blocks shut down to minimize supply current when the switch is off.

## Layout and Thermal Dissipation

Keep all traces as short as possible to reduce the effect of undesirable parasitic inductance, and to optimize the switch response time to output short-circuit conditions.

Place input and output capacitors no more than 5 mm from device leads. Connect IN and OUT to the power bus with short traces. The exposed pad on the QFN package underside must be soldered to the PC board in order to realize the rated package dissipation.
An active switch dissipates little power with minimal change in package temperature. Calculate the power dissipation for this condition as follows:

$$
\text { P }=(\mathrm{IOUT})^{2} \times \text { RON }
$$

At the normal operating current (IOUT $=0.5 \mathrm{~A}$ ) and the maximum on-resistance of the switch $(90 \mathrm{~m} \Omega)$, the power dissipation is:

$$
P=(0.5 A)^{2} \times 0.09 \Omega=22.5 \mathrm{~mW}
$$

The worst-case power dissipation occurs when the output current is just below the current-limit threshold (1.2A max) with an output voltage just greater than 1 V . In this case, the power dissipated is the voltage drop across the switch multiplied by the current limit:

$$
P=\operatorname{LIM} \times(V I N-V O U T)
$$

For a 5 V input and 1 V output, the maximum power dissipation per switch is:

$$
P=1.2 \mathrm{~A} \times(5 \mathrm{~V}-1 \mathrm{~V})=4.8 \mathrm{~W}
$$

Since the package power dissipation is 1951 mW for the 8-pin thin QFN, the MAX1946 die temperature exceeds the $+160^{\circ} \mathrm{C}$ thermal-shutdown threshold. The switch output shuts down until the junction temperature cools by $15^{\circ} \mathrm{C}$. The duty cycle and period are strong functions of the ambient temperature and the PC board layout (see the Thermal Shutdown section).
If the output current exceeds the current-limit threshold, or the output voltage is pulled below the short-circuit detect threshold, the MAX1946 enters a fault state after 20 ms , at which point autoreset mode is enabled and 25 mA is sourced by the output. For a 5V input, OUT short-circuited to GND, and autoreset mode active, the power dissipation is as follows:

$$
P=0.025 \mathrm{~A} \times 5 \mathrm{~V}=0.125 \mathrm{~W}
$$

## Chip Information

TRANSISTOR COUNT: 2004
PROCESS: BiCMOS

## Single USB Switch with Autoreset and Fault Blanking in Tiny QFN

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to www.maxim-ic.com/packages.


# Single USB Switch with Autoreset and Fault Blanking in Tiny QFN 

## Package Information (continued)

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| COMMON DIMENSIONS |  |  |
| :---: | :---: | :---: |
| SYMBOL | MIN. | MAX. |
| A | 0.70 | 0.80 |
| D | 2.90 | 3.10 |
| E | 2.90 | 3.10 |
| A1 | 0.00 | 0.05 |
| L | 0.20 | 0.40 |
| k | 0.25 MIN |  |
| A2 | 0.20 REF. |  |

NOTES:

1. ALL DIMENSIONS ARE IN mm. ANGLES in DEGREES.
2. COPLANARITY SHALL NOT EXCEED 0.08 mm .
3. WARPAGE SHALL NOT EXCEED 0.10 mm .
4. PACKAGE LENGTH/PACKAGE WIDTH ARE CONSIDERED AS SPECIAL CHARACTERISTIC(S).
5. DRAWING CONFORMS TO JEDEC MO22O.


[^0] implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.


[^0]:    Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are

