
2SD1490

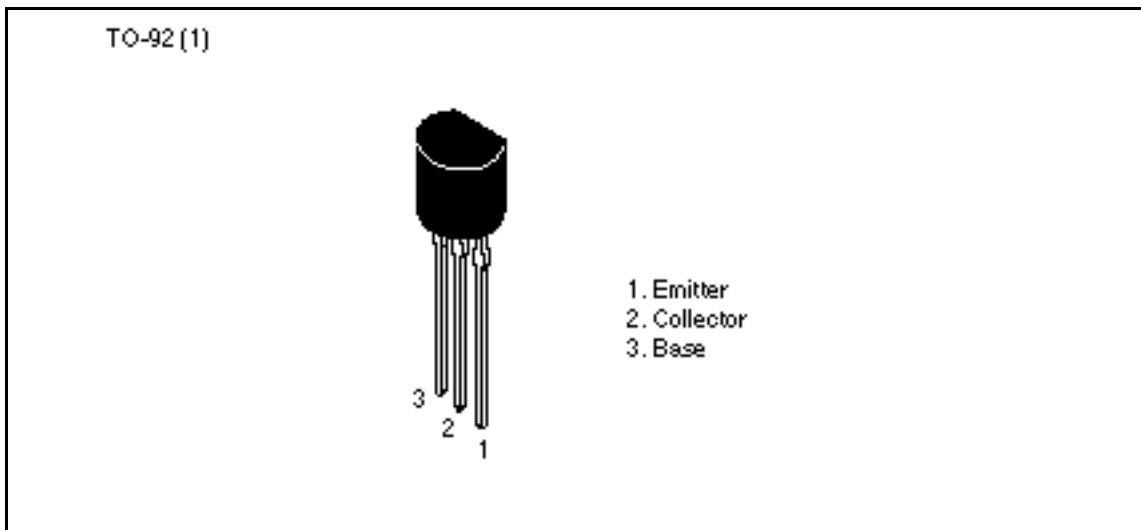
Silicon NPN Epitaxial

HITACHI

Application

- Low frequency power amplifier
- Complementary pair with 2SB1059

Outline



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	70	V
Collector to emitter voltage	V_{CEO}	50	V
Emitter to base voltage	V_{EBO}	6	V
Collector current	I_C	1	A
Collector power dissipation	P_C	0.75	W
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

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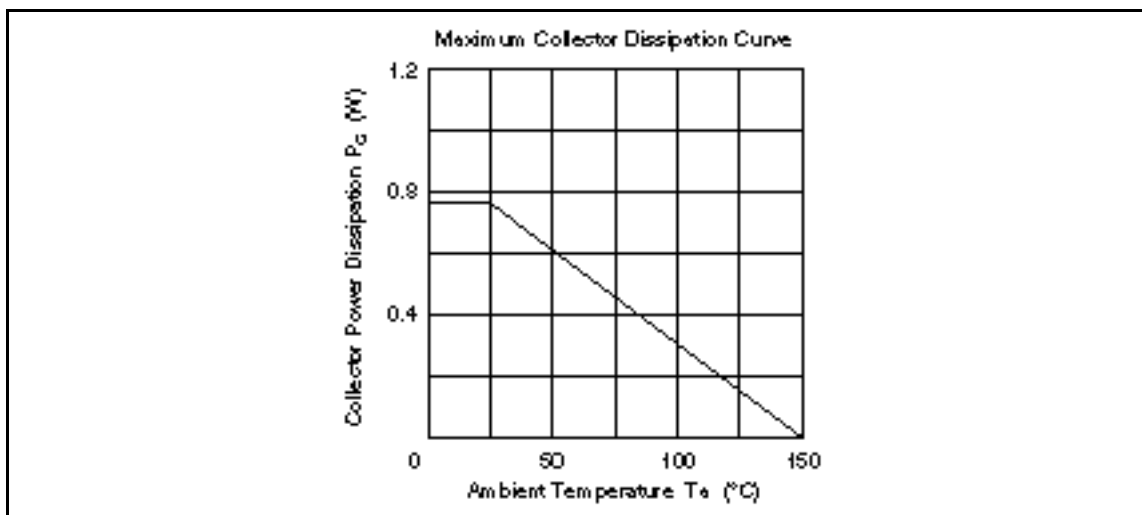
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{(BR)CBO}$	70	—	—	V	$I_C = 10 \mu A, I_E = 0$
Collector to emitter breakdown voltage	$V_{(BR)CEO}$	50	—	—	V	$I_C = 1 \text{ mA}, R_{BE} =$
Emitter to base breakdown voltage	$V_{(BR)EBO}$	6	—	—	V	$I_E = 10 \mu A, I_C = 0$
Collector cutoff current	I_{CBO}	—	—	1	μA	$V_{CB} = 80 \text{ V}, I_E = 0$
Emitter cutoff current	I_{EBO}	—	—	0.2	μA	$V_{EB} = 6 \text{ V}, I_C = 0$
DC current transfer ratio	h_{FE}^{*1}	100	—	500		$V_{CE} = 2 \text{ V}, I_C = 0.1 \text{ A}$
Collector to emitter saturation voltage	$V_{CE(sat)}$	—	—	0.3	V	$I_C = 1 \text{ A}, I_B = 0.1 \text{ A}$
Gain bandwidth product	f_T	—	80	—	MHz	$V_{CE} = 2 \text{ V}, I_C = 10 \text{ mA}$
Collector output capacitance	Cob	—	20	—	pF	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$

Note: 1. The 2SD1490 is grouped by h_{FE} as follows.

B	C	D
100 to 200	160 to 320	250 to 500

See characteristic curves of 2SD789.



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HITACHI

Hitachi, Ltd.

Semiconductor & IC Div.

Nippon Bldg., 2-5-2, Ohite-machi, Chiyoda-ku, Tokyo 100, Japan

Tel Tokyo (03) 3270-2111

Fax (03) 3270-5109

For further information write to:

Hitachi America, Ltd.

Semiconductor & IC Div.

2000 Sierra Point Parkway

Folsom, CA 94005-4835

U.S.A.

Tel 415-589-8300

Fax 415-589-4207

Hitachi Europe GmbH

Electronic Components Group

Continental Europe

Darnecker Straße 3

D-85622 Feldkirchen

München

Tel 089-9 91 80-0

Fax 089-9 29 30 00

Hitachi Europe Ltd.

Electronic Components Div.

Northern Europe Headquarters

Whitebrook Park

Lower Cookham Road

High Wycombe

Berkshire SL6 6YA

United Kingdom

Tel 0628-885000

Fax 0628-778322

Hitachi Asia Pte. Ltd.

45 Collyer Quay #20-00

Hitachi Tower

Singapore 0104

Tel 535-2100

Fax 535-1533

Hitachi Asia (Hong Kong) Ltd.

Unit 705, North Tower,

World Finance Centre

Harbour City, Canton Road

Tsim Sha Tsui, Kowloon

Hong Kong

Tel 27359218

Fax 27308074