XN04503 (XN4503)

Silicon NPN epitaxial planer transistor

For amplification of low frequency output

Features

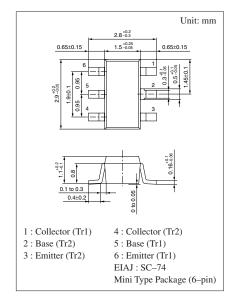
- Two elements incorporated into one package.
- Reduction of the mounting area and assembly cost by one half.

Basic Part Number of Element

• $2SD0813(2SD813) \times 2$ elements

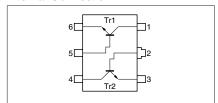
Absolute Maximum Ratings (Ta=25°C)

Parameter		Symbol	Ratings	Unit	
Rating of element	Collector to base voltage	V_{CBO}	25	V	
	Collector to emitter voltage	V_{CEO}	20	V	
	Emitter to base voltage	V_{EBO}	7	V	
	Collector current	I_{C}	0.5	A	
	Peak collector current	I_{CP}	1	A	
Overall	Total power dissipation	P_{T}	300	mW	
	Junction temperature	T_{j}	150	°C	
	Storage temperature	T_{stg}	-55 to +150	°C	



Marking Symbol: 5Y

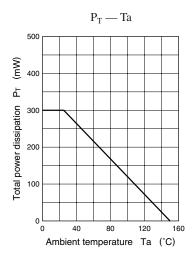
Internal Connection

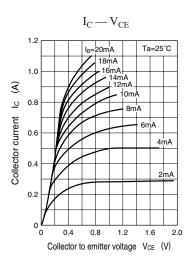


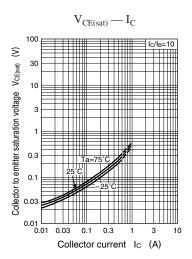
Electrical Characteristics (Ta=25°C)

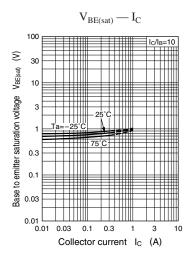
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector to base voltage	V_{CBO}	$I_C = 10 \mu A, I_E = 0$	25			V
Collector to emitter voltage	V_{CEO}	$I_C = 1 \text{mA}, I_B = 0$	20			V
Emitter to base voltage	V_{EBO}	$I_{\rm E} = 10 \mu A, I_{\rm C} = 0$	7			V
Collector cutoff current	I_{CBO}	$V_{CB} = 25V, I_E = 0$			0.1	μА
Collector cutoff current	I_{CEO}	$V_{CE} = 20V, I_{B} = 0$			1	μА
Forward current transfer ratio	h _{FE1}	$V_{CE} = 2V, I_{C} = 500 \text{mA}*$	65		350	
Forward current transfer ratio	h _{FE2}	$V_{CE} = 2V, I_C = 1A*$	50			
Collector to emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 500 \mathrm{mA}, I_{\rm B} = 20 \mathrm{mA}*$		0.2	0.4	V
Base to emitter saturation voltage	V _{BE(sat)}	$I_{\rm C} = 500 \mathrm{mA}, I_{\rm B} = 50 \mathrm{mA}*$			1.2	V
Transition frequency	f_T	$V_{CB} = 10V, I_E = -50mA, f = 200MHz$		150		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10V, I_E = 0, f = 1MHz$		6		pF

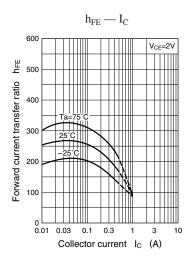
* Pulse measurement

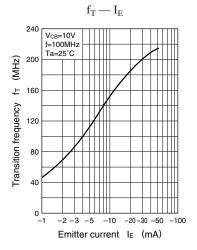


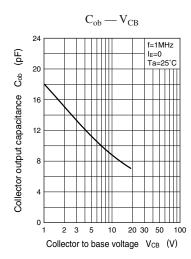












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