

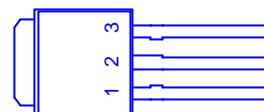
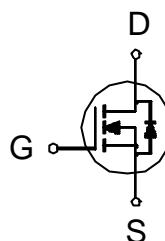
**NIKO-SEM**
**N-Channel Logic Level Enhancement  
Mode Field Effect Transistor**
**P0903BI**

TO-251

Lead-Free

**PRODUCT SUMMARY**

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
25	9.5m	50A



1. GATE
2. DRAIN
3. SOURCE

**ABSOLUTE MAXIMUM RATINGS ( $T_c = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	50	A
		35	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	200	
Avalanche Current	$I_{AR}$	40	
Avalanche Energy	$E_{AS}$	250	mJ
Repetitive Avalanche Energy <sup>2</sup>	$E_{AR}$	8.6	
Power Dissipation	$P_D$	50	W
		30	
Operating Junction & Storage Temperature Range	$T_j, T_{stg}$	-55 to 150	°C
Lead Temperature (1/16" from case for 10 sec.)	$T_L$	275	

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$	62.5	2.5	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	
Case-to-Heatsink	$R_{\theta CS}$		0.6	

<sup>1</sup>Pulse width limited by maximum junction temperature.<sup>2</sup>Duty cycle ≤ 1%**ELECTRICAL CHARACTERISTICS ( $T_c = 25^\circ\text{C}$ , Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	25			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.6	3	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 20V$			$\pm 250$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 20V, V_{GS} = 0V$			25	$\mu\text{A}$
		$V_{DS} = 20V, V_{GS} = 0V, T_c = 125^\circ\text{C}$			250	

**NIKO-SEM**
**N-Channel Logic Level Enhancement  
Mode Field Effect Transistor**
**P0903BI**

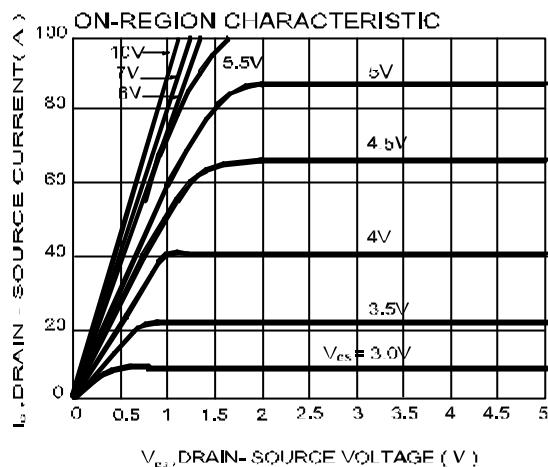
TO-251

Lead-Free

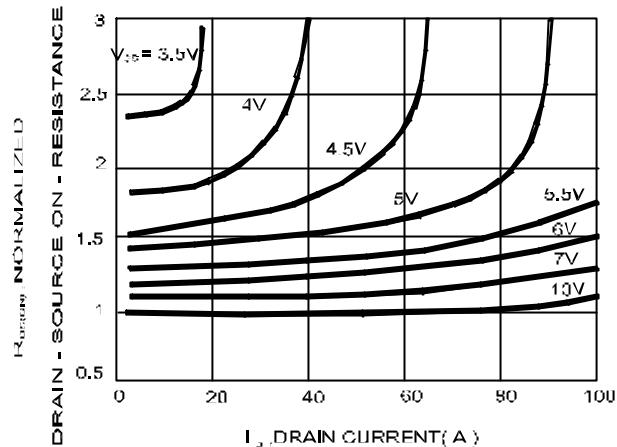
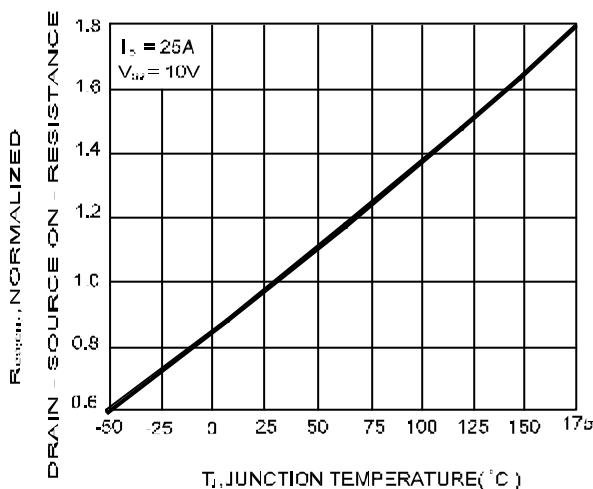
On-State Drain Current <sup>1</sup>	$I_{D(ON)}$	$V_{DS} = 10V, V_{GS} = 10V$	50			A
Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 20A$		11	16	m
		$V_{GS} = 10V, I_D = 25A$		7.5	9.5	
Forward Transconductance <sup>1</sup>	$g_f$	$V_{DS} = 10V, I_D = 25A$		32		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$		1200	1800	pF
Output Capacitance	$C_{oss}$			600	1000	
Reverse Transfer Capacitance	$C_{rss}$			350	500	
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{DS} = 10V, V_{GS} = 10V,$ $I_D = 25A$		25	50	nC
Gate-Source Charge <sup>2</sup>	$Q_{gs}$			15		
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$			10		
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	$V_{DS} = 15V, R_L = 1$ $I_D \cong 50A, V_{GS} = 10V, R_{GEN} = 24$		6	16	nS
Rise Time <sup>2</sup>	$t_r$			120	250	
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$			40	90	
Fall Time <sup>2</sup>	$t_f$			105	200	
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_C = 25^\circ C</math>)</b>						
Continuous Current	$I_S$				50	A
Pulsed Current <sup>3</sup>	$I_{SM}$				150	
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_S = 25A, V_{GS} = 0V$		0.9	1.3	V
Reverse Recovery Time	$t_{rr}$	$I_F = I_S, dI_F/dt = 100A / \mu S$		70		nS
Peak Reverse Recovery Current	$I_{RM(REC)}$			200		A
Reverse Recovery Charge	$Q_{rr}$			0.043		$\mu C$

<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu sec$ , Duty Cycle  $\leq 2\%$ .<sup>2</sup>Independent of operating temperature.<sup>3</sup>Pulse width limited by maximum junction temperature.

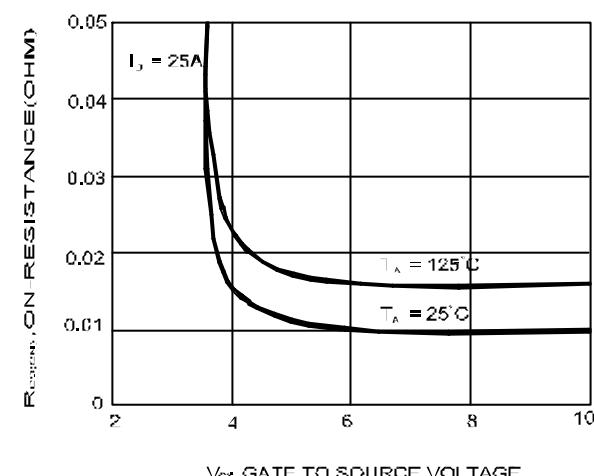
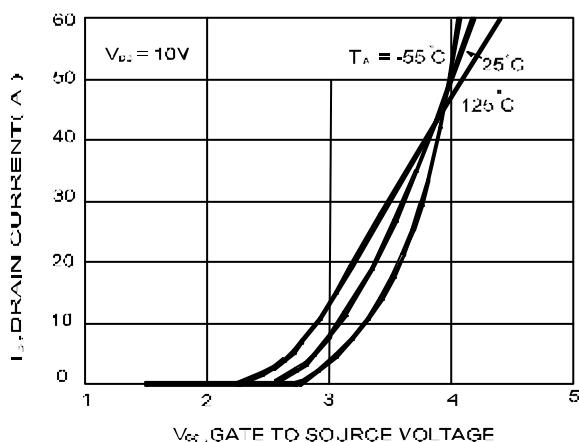
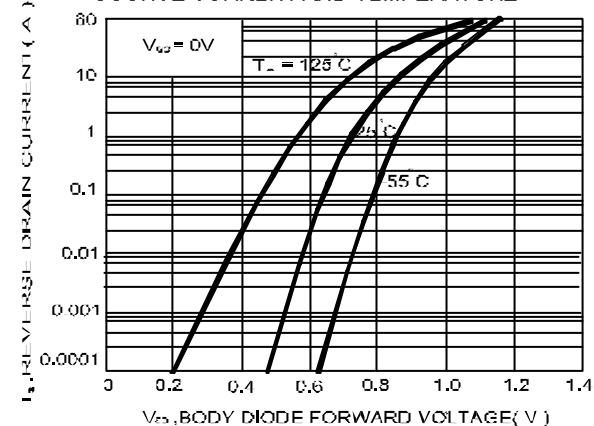
REMARK: THE PRODUCT MARKED WITH "P0903BI", DATE CODE or LOT #

**NIKO-SEM**
**N-Channel Logic Level Enhancement  
Mode Field Effect Transistor**
**P0903BI**  
**TO-251**  
**Lead-Free**
**TYPICAL CHARACTERISTICS**


ON-RESISTANCE VARIATION WITH DRAIN CURRENT AND GATE


**ON-RESISTANCE VARIATION WITH TEMPERATURE**


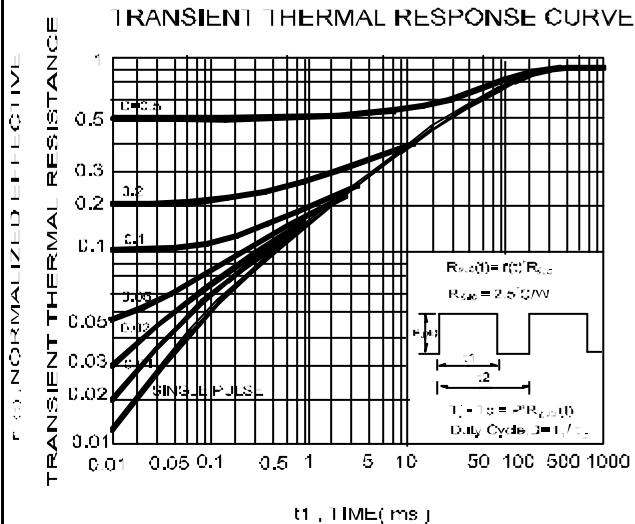
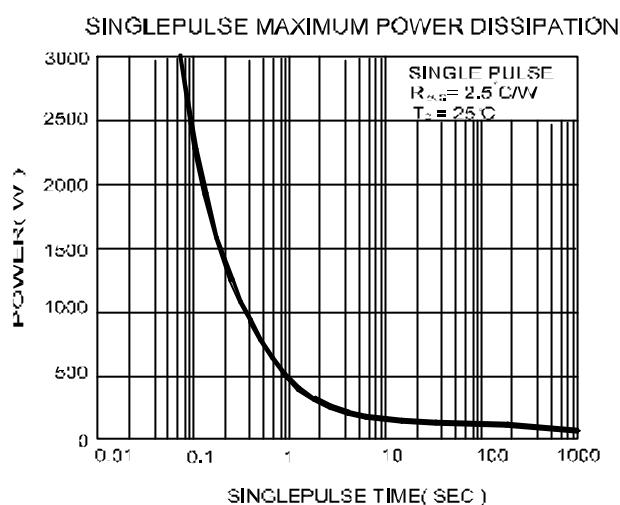
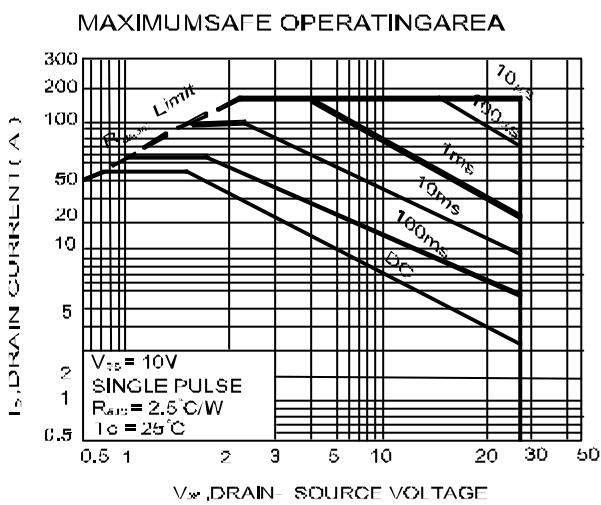
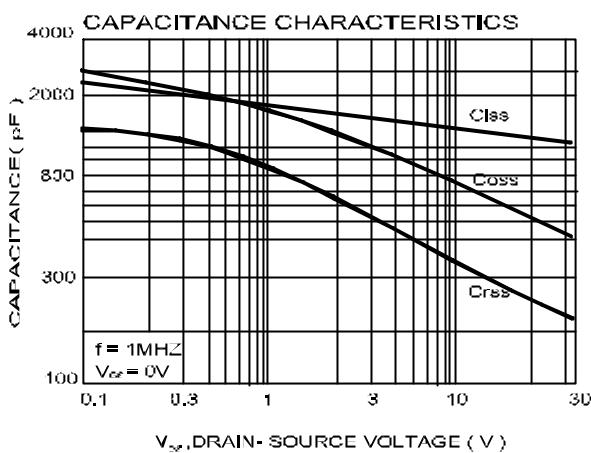
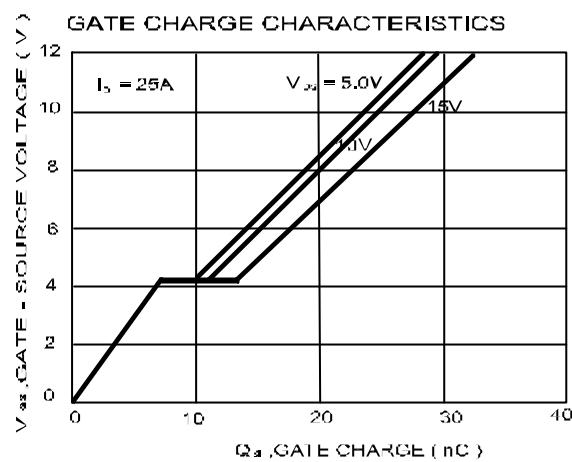
ON-RESISTANCE VARIATION WITH GATE-TO-SOURCE VOLTAGE


**TRANSFER CHARACTERISTICS**

**BODY DIODE FORWARD VOLTAGE VARIATION WITH SOURCE CURRENT AND TEMPERATURE**


**NIKO-SEM**
**N-Channel Logic Level Enhancement  
Mode Field Effect Transistor**
**P0903BI**

TO-251

Lead-Free



## TO-251 (IPAK) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	13.7		15.3	H	1.4		2
B	2.2		2.4	I	6.4		6.8
C	0.4		0.6	J	5.2		5.5
D	0.4		0.6	K	0.6		0.9
E	0.9		1.5	L	0.4		0.8
F	7.0		8.0	M		2.3	
G	5.4		5.8	N			

