

# **AGB3302**

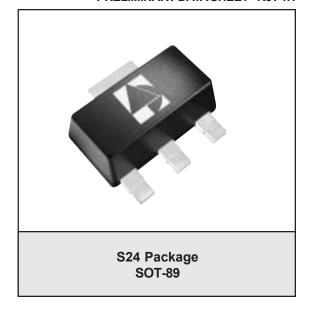
50Ω High Linearity Low Noise Wideband Gain Block PRELIMINARY DATA SHEET - Rev 1.1

# **FEATURES**

- · DC-5300 MHz Operation Bandwidth
- +36 dBm Output IP3
- · 4.5 dB Noise Figure at 850 MHz
- 16 dB Gain at 850 MHz
- +18 dBm P1dB
- SOT-89 Package
- Single +8 V to +12 V Supply
- Case Temperature: -40 to +85 °C

# **APPLICATIONS**

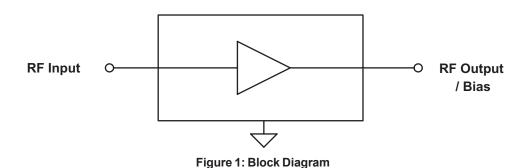
- Cellular Base Stations for W-CDMA, CDMA, TDMA, GSM, PCS and CDPD systems
- Fixed Wireless
- MMDS/WLL
- WLAN, HyperLAN



## PRODUCT DESCRIPTION

The AGB3302 is one of a series of high performance InGaP HBT amplifiers designed for use in applications requiring high linearity, low noise and low distortion. No external matching components are needed for insertion into a  $50\Omega$  system. With a high output IP3, low noise figure and wide band

operation, the AGB3302 is ideal for wireless infrastructure applications such as Cellular Base Stations, MMDS, and WLL. Offered in a low cost SOT-89 surface mount package, the AGB3302 requires a single supply voltage, and typically consumes 0.5 Watts of power using a +8 V supply.



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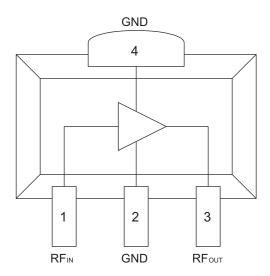


Figure 2: Pinout (X-ray Top View)

**Table 1: Pin Description** 

PIN	NAME	DESCRIPTION			
1	RFℕ	RF Input			
2	GND	Ground			
3	RFout	RF Output / Bias			
4	GND	Ground			

# **ELECTRICAL CHARACTERISTICS**

**Table 2: Absolute Minimum and Maximum Ratings** 

PARAMETER	MIN	MAX	UNIT
Device Voltage (Vcc)	0	+6	VDC
RF Input Power (P <sub>N</sub> )	-	+10	dBm
Storage Temperature (Tstg)	-40	+150	°C
Junction Temperature	-	+200	°C

Stresses in excess of the absolute ratings may cause permanent damage. Functional operation is not implied under these conditions. Exposure to absolute ratings for extended periods of time may adversely affect reliability.

**Table 3: Operating Ranges** 

PARAMETER	MIN	TYP	MAX	UNIT
Operating Frequency (f) (1)	1	1	5300	MHz
Supply Voltage (Vsupply) (2)	-	+8	-	VDC
Case Temperature (Tc)	-40	-	+85	°C

The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications.

#### Notes:

- (1) Operating frequency is defined by the output return loss (S22) having a VSWR less than 2:1.
- (2) Voltage applied through a bias resistor and inductor. Refer to Figure 3. For other supply voltages, see the APPLICATION INFORMATION section.



# Table 4: Electrical Specifications (T<sub>A</sub> = +25 °C, $V_{SUPPLY}$ = +8 VDC, $50\Omega$ system)

PARAMETER	MIN	TYP	MAX	UNIT
Gain (S <sub>21</sub> )				
850 MHz	-	15.8	-	
1950 MHz	-	15.2	-	dB
2140 MHz	-	15.0	-	d B
2450 MHz	-	14.7	-	
Output IP3 (1)				
850 MHz	-	+36	-	
1950 MHz	-	+36.2	-	dBm
2140 MHz	-	+36.6	-	ubili
2450 MHz	-	+35.6	-	
Output 1dB Compression (P1dB)				
850 MHz	-	+17.8	-	dDm
1950 MHz	-	+17.6	-	dBm
Noise Figure				
850 MHz	-	4.5	-	dB
Thermal Resistance ( θ <sub>JC</sub> ) <sup>(2)</sup>	-	155	-	°C/W
Supply Current (Icc)	-	65	-	mA

#### Notes:

- (1) OIP3 is measured with two tones at 1 MHz spacing at 0 dBm output power per tone.
- (2) The value for Thermal Resistance is based on a Device Voltage (Vcc) of +5.5 Volts.
- 3. Performance as measured on ANADIGICS test fixture (see Figure 3).

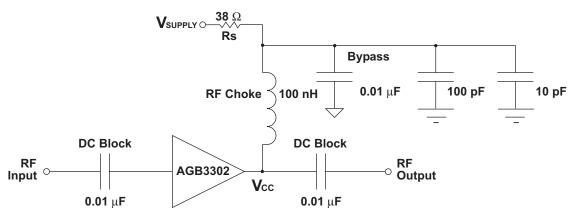


Figure 3: Application Circuit (50Ω Terminations)

## **PERFORMANCE DATA**

Figure 4: Gain vs. Frequency
De-embedded 50Ω S-parameter
(TA = +25 °C, Vcc = +5.5 V, Icc = 65 mA)

25
20
20
21
25
20
4
5
Frequency (GHz)

Figure 5: Isolation vs. Frequency De-embedded  $50\Omega$  S-parameter ( $T_A = +25$  °C,  $V_{CC} = +5.5$  V,  $I_{CC} = 65$  mA)

Figure 6: Input Return Loss vs. Frequency De-embedded 50 $\Omega$  S-parameter

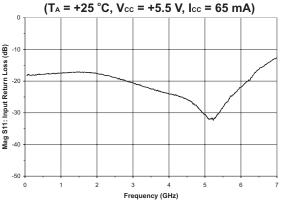
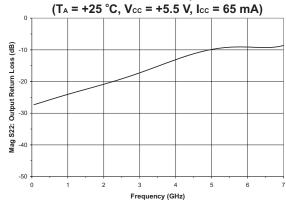


Figure 7: Output Return Loss vs. Frequency De-embedded 50 $\Omega$  S-parameter



#### AGB3302

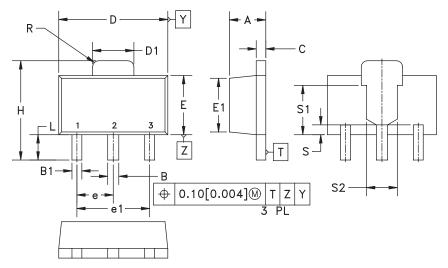
# **APPLICATION INFORMATION**

The AGB3302 is optimized for a bias current of 65 mA. Using a +8 V supply, a bias resistor (Rs) of 38  $\Omega$  will provide the appropriate bias (see Figure 3). Table 5 shows the recommended value of Rs for other supply voltages. Supply voltages below +8 V are not recommended for standard operation.

Table 5: Bias Resistor Values for Various Supply Voltages

Vsuppli	+8 V	+10 V	+12 V
Rs	38 Ω	69 Ω	100 Ω

# **PACKAGE OUTLINE**



MILLIM			NOTE	
MIN.	MAX.	MIN.	MAX.	
1.40	1.60	0.055	0.063	-
0.44	0.56	0.017	0.022	-
0.36	0.48	0.014	0.019	3
0.35	0.44	0.014	0.017	-
4.40	4.60	0.173	0.181	-
1.62	1.83	0.064	0.072	-
2.29	2.60	0.090	0.102	ı
2.13	2.29	0.084	0.090	4
1.50 BSC		0.059 BSC		ı
3.00	3.00 BSC		0.118 BSC	
3.94	4.25	0.155	0.167	ı
0.89	1.20	0.035	0.047	ı
_	0.25	_	0.010	-
0.40 NOM.		0.016	NOM.	4
2.03	NOM.	0.080	NOM.	4
1.27	NOM.	0.050	NOM.	4
	MIN. 1.40 0.44 0.36 0.35 4.40 1.62 2.29 2.13 1.50 3.00 3.94 0.89 - 0.40 2.03	1.40 1.60 0.44 0.56 0.36 0.48 0.35 0.44 4.40 4.60 1.62 1.83 2.29 2.60 2.13 2.29 1.50 BSC 3.00 BSC 3.94 4.25 0.89 1.20 - 0.25 0.40 NOM. 2.03 NOM.	MN.   MAX.   MIN.   1.40   1.60   0.055   0.44   0.56   0.017   0.36   0.44   0.014   4.40   4.60   0.173   1.62   1.83   0.064   2.29   2.60   0.090   2.13   2.29   0.084   1.50   BSC   0.059   3.00   BSC   0.118   3.94   4.25   0.155   0.89   1.20   0.035   - 0.25   - 0.40   NOM.   0.016   2.03   NOM.   0.080	MN.         MAX.         MIN.         MAX.           1.40         1.60         0.055         0.063           0.44         0.56         0.017         0.022           0.36         0.48         0.014         0.019           0.35         0.44         0.013         0.181           1.62         1.83         0.064         0.072           2.29         2.60         0.090         0.102           2.13         2.29         0.084         0.090           1.50         BSC         0.059         BSC           3.00         BSC         0.118         BSC           3.94         4.25         0.155         0.167           0.89         1.20         0.035         0.047           -         0.25         -         0.010           0.40         NOM.         0.016         NOM.           2.03         NOM.         0.080         NOM.

#### NOTES:

- 1. CONTROLLING DIMENSIONS: MILLIMETERS.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH OR MATERIAL PROTRUSIONS.
- 3. DIMENSION B1, 2 PLACES.
- 4. DIMENSIONS E1, S, S1 & S2 REFERENCE ONLY.
- 5. REFERENCE JEDEC TO-243 (AA).

Figure 8: S24 Package Outline - SOT-89

#### TOP BRAND



#### NOTES:

- 1. ANADIGICS LOGO SIZE: x=0.040±0.010 Y=0.048±0.010
- 2. PART NUMBER: FOUR NUMERIC CHARACTERS
- 3. WAFER LOT NUMBER: LLLL=FOUR NUMERIC CHARACTERS NN= TWO ALPHABETIC CHARACTERS

4. TYPE : ELITE SIZE : 2-POINT COLOR : LASER

Figure 9: Branding Specification

## ORDERING INFORMATION

PART NUMBER	TEMPERATURE RANGE	PACKAGE DESCRIPTION	COMPONENT PACKAGING
AGB3302S24Q1	-40 to +85°C	SOT-89 Package	1,000 piece Tape and Reel



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