2SA1674

Silicon PNP epitaxial planer type

For low-frequency output amplification Complementary to 2SC4391

Features

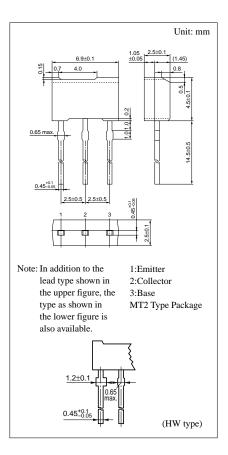
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- Low collector to emitter saturation voltage V_{CE(sat)}.
- High collector to emitter voltage V_{CEO}.
- Allowing supply with the radial taping.

Parameter	Symbol	Ratings	Unit			
Collector to base voltage	V _{CBO}	-80	V			
Collector to emitter voltage	V _{CEO}	-80	V			
Emitter to base voltage	V _{EBO}	-5	V			
Peak collector current	I _{CP}	-1.5	А			
Collector current	I _C	-1	А			
Collector power dissipation (T _C =25°C)	P_{C}^{*}	1	W			
Junction temperature	Tj	150	°C			
Storage temperature	T _{stg}	-55 ~ +150	°C			

Absolute Maximum Ratings (Ta=25°C)

Printed circuit board: Copper foil area of 1cm² or more, and the board thickness of 1.7mm for the collector portion



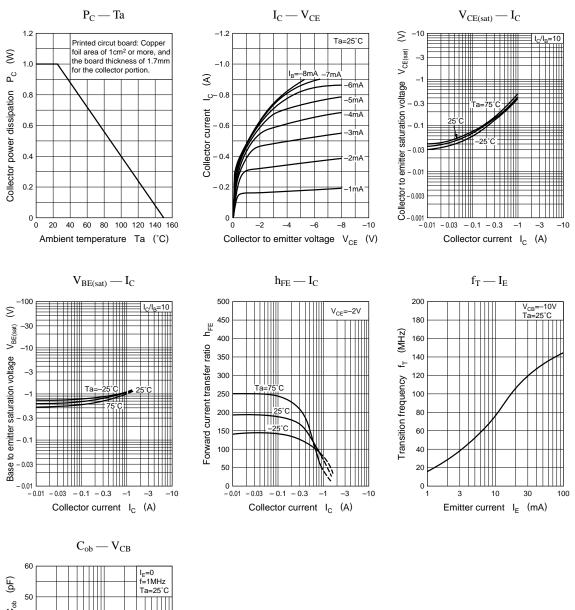
Electrical Characteristics (Ta=25°C)

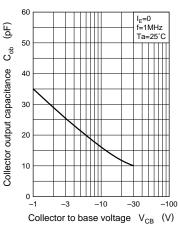
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	I _{CBO}	$V_{CB} = -40V, I_E = 0$			- 0.1	μΑ
Collector to base voltage	V _{CBO}	$I_C = -10\mu A, I_E = 0$	-80			V
Collector to emitter voltage	V _{CEO}	$I_C = -1mA$, $I_B = 0$	-80			V
Emitter to base voltage	V _{EBO}	$I_{\rm E} = -10\mu A, I_{\rm C} = 0$	-5			V
-	h _{FE1} *1	$V_{CE} = -2V, I_C = -100mA$	120		340	
Forward current transfer ratio	h _{FE2}	$V_{CE} = -2V, I_C = -500 \text{mA}^{*2}$	60			
Collector to emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = -500 {\rm mA}, I_{\rm B} = -50 {\rm mA}^{*2}$		- 0.2	- 0.3	V
Base to emitter saturation voltage	V _{BE(sat)}	$I_{\rm C} = -500 {\rm mA}, I_{\rm B} = -50 {\rm mA}^{*2}$		- 0.85	-1.2	V
Transition frequency	f _T	$V_{CB} = -10V$, $I_E = 50mA$, $f = 200MHz$		120		MHz
Collector output capacitance	C _{ob}	$V_{CB} = -10V, I_E = 0, f = 1MHz$		15	30	pF

*2 Pulse measurement

*1hFE1 Rank classification

Rank	R	S
h _{FE1}	120 ~ 240	170 ~ 340





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