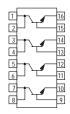
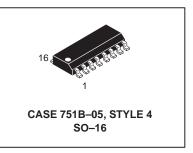
Quad Switching Transistor NPN Silicon



MMPQ2369

Motorola Preferred Device



MAXIMUM RATINGS

Rating	Symbol	Value		Unit
Collector-Emitter Voltage	VCEO	15		Vdc
Collector-Base Voltage	V _{CB}	40		Vdc
Emitter-Base Voltage	V _{EB}	4.5		Vdc
Collector Current — Continuous	IC	500		mAdc
		Each Transistor	Four Transistors Equal Power	
Total Power Dissipation @ T _A = 25°C Derate above 25°C	PD	0.4 3.2	0.72 6.4	Watts mW/°C
Total Power Dissipation @ T _C = 25°C Derate above 25°C	PD	0.66 5.3	1.92 15.4	Watts mW/°C
Operating and Storage	T _J , T _{stg}	-55 to +150		°C

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Collector-Emitter Breakdown Voltage(1) (IC = 10 mAdc, IB = 0)	V(BR)CEO	15	_	_	Vdc	
Collector-Base Breakdown Voltage (I _C = 10 μAdc, I _E = 0)	V(BR)CBO	40	_	_	Vdc	
Emitter-Base Breakdown Voltage (I _E = 10 μAdc, I _C = 0)	V(BR)EBO	4.5	_	_	Vdc	
Collector Cutoff Current (V _{CB} = 20 Vdc, I _E = 0)	ICBO	_	_	0.4	μAdc	

^{1.} Pulse Test: Pulse Width \leq 300 $\mu s;$ Duty Cycle \leq 2.0%.

Preferred devices are Motorola recommended choices for future use and best overall value.

Thermal Clad is a trademark of the Bergquist Company



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ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted) (Continued)

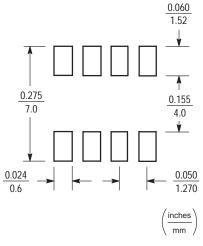
Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS			•		
DC Current Gain(1) ($I_C = 10 \text{ mAdc}$, $V_{CE} = 1.0 \text{ Vdc}$) ($I_C = 100 \text{ mAdc}$, $V_{CE} = 2.0 \text{ Vdc}$)	hFE	40 20	_ _		_
Collector-Emitter Saturation Voltage (I _C = 10 mAdc, I _B = 1.0 mAdc)	VCE(sat)	_	_	0.25	Vdc
Base-Emitter Saturation Voltage (I _C = 10 mAdc, I _B = 1.0 mAdc)	V _{BE(sat)}	_	_	0.9	Vdc
DYNAMIC CHARACTERISTICS			•		
Current-Gain — Bandwidth Product (IC = 10 mAdc, VCE = 10 Vdc, f = 100 MHz)	fT	450	550	_	MHz
Output Capacitance (V _{CB} = 5.0 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	_	2.5	4.0	pF
Input Capacitance ($V_{EB} = 0.5 \text{ Vdc}$, $I_{C} = 0$, $f = 1.0 \text{ MHz}$)	C _{ib}		3.0	5.0	pF
SWITCHING CHARACTERISTICS	-				
Turn–On Time $(V_{CC} = 3.0 \text{ Vdc}, V_{EB(off)} = 1.5 \text{ Vdc}, I_{C} = 10 \text{ mAdc}, I_{B1} = 3.0 \text{ mAdc})$	^t on	_	9.0	_	ns
Turn–Off Time ($V_{CC} = 3.0 \text{ Vdc}$, $I_{C} = 10 \text{ mAdc}$, $I_{B1} = 3.0 \text{ mAdc}$, $I_{B2} = 1.5 \text{ mAdc}$)	t _{off}		15		ns

^{1.} Pulse Test: Pulse Width \leq 300 μ s; Duty Cycle \leq 2.0%.

INFORMATION FOR USING THE SO-16 SURFACE MOUNT PACKAGE

MINIMUM RECOMMENDED FOOTPRINT FOR SURFACE MOUNTED APPLICATIONS

Surface mount board layout is a critical portion of the total design. The footprint for the semiconductor packages must be the correct size to insure proper solder connection interface between the board and the package. With the correct pad geometry, the packages will self align when subjected to a solder reflow process.



SO-16

SO-16 POWER DISSIPATION

The power dissipation of the SO–16 is a function of the pad size. This can vary from the minimum pad size for soldering to a pad size given for maximum power dissipation. Power dissipation for a surface mount device is determined by $T_{J(max)}$, the maximum rated junction temperature of the die, $R_{\theta JA}$, the thermal resistance from the device junction to ambient, and the operating temperature, T_A . Using the values provided on the data sheet for the SO–16 package, P_D can be calculated as follows:

$$P_D = \frac{T_{J(max)} - T_{A}}{R_{A,IA}}$$

The values for the equation are found in the maximum ratings table on the data sheet. Substituting these values into the equation for an ambient temperature T_A of $25^{\circ}C$, one can calculate the power dissipation of the device which in this case is 800 milliwatts.

$$P_D = \frac{150^{\circ}C - 25^{\circ}C}{156^{\circ}C/W} = 800 \text{ milliwatts}$$

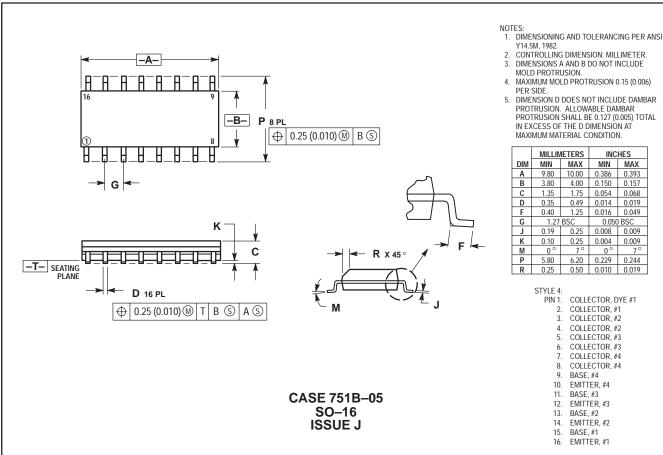
The 156°C/W for the SO–16 package assumes the use of the recommended footprint on a glass epoxy printed circuit board to achieve a power dissipation of 800 milliwatts. There are other alternatives to achieving higher power dissipation from the SO–16 package. Another alternative would be to use a ceramic substrate or an aluminum core board such as Thermal Clad™. Using a board material such as Thermal Clad, an aluminum core board, the power dissipation can be doubled using the same footprint.

SOLDERING PRECAUTIONS

The melting temperature of solder is higher than the rated temperature of the device. When the entire device is heated to a high temperature, failure to complete soldering within a short time could result in device failure. Therefore, the following items should always be observed in order to minimize the thermal stress to which the devices are subjected.

- Always preheat the device.
- The delta temperature between the preheat and soldering should be 100°C or less.*
- When preheating and soldering, the temperature of the leads and the case must not exceed the maximum temperature ratings as shown on the data sheet. When using infrared heating with the reflow soldering method, the difference shall be a maximum of 10°C.
- The soldering temperature and time shall not exceed 260°C for more than 10 seconds.
- When shifting from preheating to soldering, the maximum temperature gradient shall be 5°C or less.
- After soldering has been completed, the device should be allowed to cool naturally for at least three minutes.
 Gradual cooling should be used as the use of forced cooling will increase the temperature gradient and result in latent failure due to mechanical stress.
- Mechanical stress or shock should not be applied during cooling.
- * Soldering a device without preheating can cause excessive thermal shock and stress which can result in damage to the device.

PACKAGE DIMENSIONS



- Y14.5M, 1982. CONTROLLING DIMENSION: MILLIMETER.
- DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.
- MAXIMUM MOLD PROTRUSION 0.15 (0.006)
- DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIN	METERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	9.80	10.00	0.386	0.393
В	3.80	4.00	0.150	0.157
С	1.35	1.75	0.054	0.068
D	0.35	0.49	0.014	0.019
F	0.40	1.25	0.016	0.049
G	1.27 BSC		0.050 BSC	
J	0.19	0.25	0.008	0.009
K	0.10	0.25	0.004	0.009
M	0 °	7°	0°	7°
Р	5.80	6.20	0.229	0.244
P	0.25	0.50	0.010	0.010

COLLECTOR, DYE #1 COLLECTOR, #1 COLLECTOR, #2 COLLECTOR, #2 COLLECTOR, #3 COLLECTOR, #3 COLLECTOR, #4 COLLECTOR, #4 BASE #4 EMITTER, #4 BASE, #3 EMITTER, #3 BASE, #2 EMITTER, #2 BASE #1 FMITTER, #1

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