❑ MN101C66D

Type	MN101C66D (under development)
ROM (×8-bit)	64 K
RAM (×8-bit)	2 K
Package	QFP084-P-1818E *Pb free, LQFP080-P-1414A *Pb free, TQFP080-P-1212D *Pb free (under plannning)
Minimum Instruction Execution Time	0.1 µs (at 4.5 V to 5.5 V, 20 MHz) 0.25 µs (at 2.7 V to 5.5 V, 8 MHz)*1 62.5 µs (at 2.0 V to 5.5 V, 32 kHz)*1, 2 *1 The lower limit for operation guarantee for flash memory built-in type is 4.5 V. *2 The lower limit for operation guarantee for EPROM built-in type is 2.3 V.
Interrupts	• RESET • Watchdog • External 0 • External 1 • External 2 • External 3*1 • External 4 (key interrupt dedicated) • Timer 0 • Timer 1 • Timer 2 • Timer 3 • Timer 6 • Time base • Timer 7 (2 systems) • Timer 8 (2 systems) • Serial 0 (2 systems) • Serial 2 • A/D conversion finish *1 LQFP080-P-1414A,TQFP080-P-1212D: Not mounted
Timer Counter	Timer counter 0 : 8-bit × 1 (square-wave/8-bit PWM output, event count, generation of remote control carrier, simple pulse width measurement) (square-wave/PWM output to large current terminal P50 possible) Clock source ····· 1/2, 1/4 of system clock frequency; 1/1, 1/4, 1/16, 1/32, 1/64 of OSC oscillation clock frequency; 1/1 of XI oscillation clock frequency; external clock input Interrupt source ····· coincidence with compare register 0 Timer counter 1 : 8-bit × 1 (square-wave output, event count, synchronous output event) Clock source ····· 1/2, 1/8 of system clock frequency; 1/1, 1/4, 1/16, 1/8192, 1/32768 of OSC oscillation clock frequency; 1/1 of XI oscillation clock frequency; external clock input Interrupt source ····· coincidence with compare register 1 Timer counter 0, 1 can be cascade-connected. Timer counter 2 : 8-bit × 1 (square-wave output, additional pulse type 10-bit PWM output, event count, synchronous output event, simple pulse width measurement) (square-wave/PWM output to large current terminal P52 possible) Clock source ····· 1/2, 1/4 of system clock frequency; 1/1, 1/4, 1/16, 1/32, 1/64 of OSC oscillation clock frequency; 1/1 of XI oscillation clock frequency; external clock input Interrupt source ····· coincidence with compare register 2 Timer counter 3 : 8-bit × 1 (square-wave output, event count, generation of remote control carrier, serial 0 baud rate timer) Clock source ····· 1/2, 1/8 of system clock frequency; 1/1, 1/4, 1/16, 1/64, 1/128 of OSC oscillation clock frequency; 1/1 of XI oscillation clock frequency; external clock input Interrupt source ····· coincidence with compare register 3 Timer counter 2, 3 can be cascade-connected. Timer counter 6 : 8-bit freerun timer Clock source ····· 1/1 of system clock frequency; 1/1, 1/4096, 1/8192 of OSC oscillation clock frequency; 1/1, 1/4096, 1/8192 of XI oscillation clock frequency Interrupt source ····· coincidence with compare register 6 Timer counter 7 : 16-bit × 1 (square-wave output, IGBT/16-bit PWM output (cycle / duty continuous variable), event count, synchronous output evevt, pulse width measurement, input capture) (square-wave/PWM output to large current terminal P51 possible) Clock source ····· 1/1, 1/2, 1/4, 1/16 of system clock frequency; 1/1, 1/2, 1/4, 1/16 of OSC oscillation clock frequency; 1/1, 1/2, 1/4, 1/16 of external clock input frequency Interrupt source ····· coincidence with compare register 7 (2 lines)

Timer Counter (Continue)

Timer counter 8: 16 bit × 1
(square-wave/16-bit PWM output [duty continuous variable], event count, pulse width measurement, inputcapture)
(square-wave/PWM output to large current terminal P53 possible)
Clock source ·····1/1, 1/2, 1/4, 1/16, 1/128 of system clock frequency;
1/1, 1/2, 1/4, 1/16, 1/128 of OSC oscillation clock frequency;
1/1, 1/2, 1/4, 1/16 of external clock input frequency
Interrupt source ·····coincidence with compare register 8 (2 lines)

Timer counters 7, 8 can be cascade-connected.
(square-wave output, PWM, input capture, pulse width measurement is possible as a 32-bit timer.)

Time base timer (one-minute count setting)
Clock source ·····1/1 of OSC oscillation clock frequency; 1/1 of XI oscillation clock frequency
Interrupt source ·····1/128, 1/256, 1/512, 1/1024, 1/8192, 1/32768 of clock source frequency

Watchdog timer
Interrupt source ·····1/65536, 1/262144, 1/1048576 of system clock frequency

Serial Interface

Serial 0 : synchronous type/UART (full-duplex) × 1
Clock source ·····1/2, 1/4 of system clock frequency; pulse output of timer counter 3;
1/2, 1/4, 1/16, 1/64 of OSC oscillation clock frequency

Serial 2 : synchronous type × 1
Clock source ·····1/2, 1/4 of system clock frequency; pulse output of timer counter 3;
1/2, 1/4, 1/16, 1/32 of OSC oscillation clock frequency

I/O Pins	**I/O**	61 (60)	• Common use • Specified pull-up resistor available • Input/output selectable (bit unit) ( ): LQFP080-P-1414A,TQFP080-P-1212D
	Input	4 (3)	• Common use • Specified pull-up resistor available ( ): LQFP080-P-1414A,TQFP080-P-1212D

A/D Inputs

10-bit × 8-ch. (with S/H)

LCD

32 segments × 4 commons (static, 1/2, 1/3, or 1/4 duty)
LCD power supply separated from VDD (usable if VLCD ≤VDD)
LCD power shunt resistance contained

Special Ports

Buzzer output, remote control carrier signal output, high-current drive port

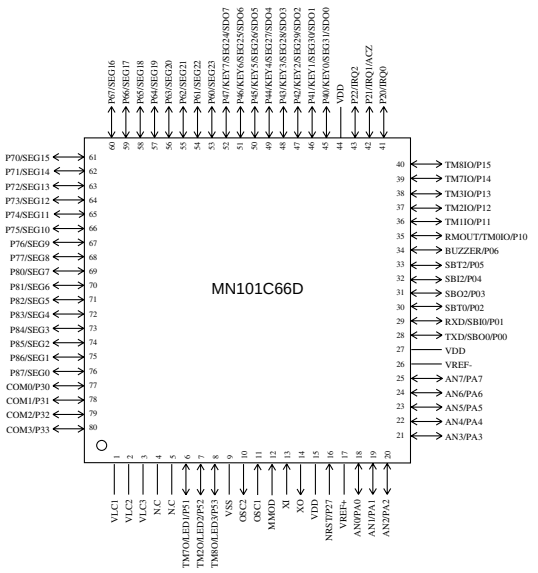
Electrical Characteristics

Supply current

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
Operating supply current	IDD1	fosc = 20 MHz, VDD = 5 V		25	60	mA
	IDD2	fosc = 8 MHz, VDD = 5 V		10	25	mA
	IDD3	fx = 32 kHz, VDD = 3 V		30	100	µA
Supply current at HALT	IDD4	fx = 32 kHz, VDD = 3 V, Ta = 25°C		4	8	µA
	IDD5	fx = 32 kHz, VDD = 3 V, Ta = –40°C to +85°C			30	µA
Supply current at STOP	IDD6	VDD = 5 V, Ta = 25°C			2	µA
	IDD7	VDD = 5 V, Ta = –40°C to +85°C			50	µA

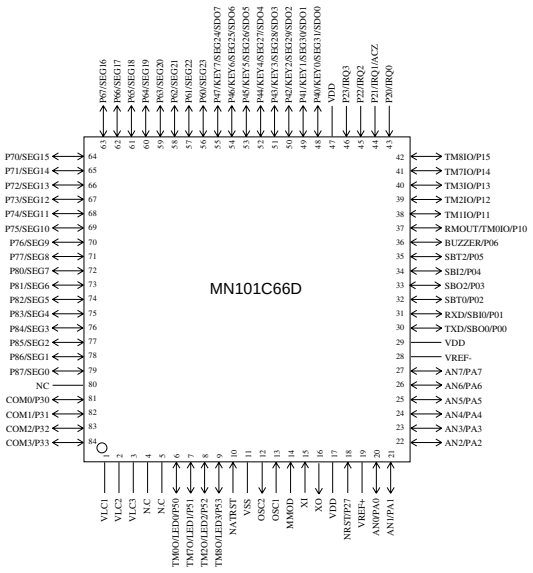
See the next page for pin assignment and support tool.

Pin Assignment



LQFP080-P-1414A *Pb free

TQFP080-P-1212D *Pb free (under plannnig)



QFP084-P-1818E *Pb free

Support Tool

In-circuit Emulator	PX-ICE101C / D + PX-PRB101C66-QFP084-P-1818E-M PX-ICE101C / D + PX-PRB101C66-LQFP080-P-1414A-M	
EPROM Built-in Type	Type	MN101CP66D(under development)
	ROM (× 8-bit)	64 K
	RAM (× 8-bit)	2 K
	Minimum instruction execution time	0.1 µs (at 4.5 V to 5.5 V, 20 MHz) 0.25 µs (at 2.7 V to 5.5 V, 8 MHz) 62.5 µs (at 2.3 V to 5.5 V, 32 kHz)
	Package	LQFP080-P-1414A *Pb free, QFP084-P-1818E *Pb free, TQFP080-P-1212D *Pb free (under planning)
Flash Memory Built-in Type	Type	MN101CF66D [ES (Engineering Sample) available]
	ROM (× 8-bit)	64 K
	RAM (× 8-bit)	2 K
	Minimum instruction execution time	0.1 µs (at 4.5 V to 5.5 V, 20 MHz) 0.25 µs (at 4.5 V to 5.5 V, 8 MHz) 62.5 µs (at 4.5 V to 5.5 V, 32 kHz)
	Package	LQFP080-P-1414A *Pb free, QFP084-P-1818E *Pb free, TQFP080-P-1212D *Pb free (under planning)

Request for your special attention and precautions in using the technical information and semiconductors described in this material

(1) An export permit needs to be obtained from the competent authorities of the Japanese Government if any of the products or technologies described in this material and controlled under the "Foreign Exchange and Foreign Trade Law" is to be exported or taken out of Japan.

(2) The technical information described in this material is limited to showing representative characteristics and applied circuit examples of the products. It does not constitute the warranting of industrial property, the granting of relative rights, or the granting of any license.

(3) The products described in this material are intended to be used for standard applications or general electronic equipment (such as office equipment, communications equipment, measuring instruments and household appliances).
Consult our sales staff in advance for information on the following applications:
- Special applications (such as for airplanes, aerospace, automobiles, traffic control equipment, combustion equipment, life support systems and safety devices) in which exceptional quality and reliability are required, or if the failure or malfunction of the products may directly jeopardize life or harm the human body.
- Any applications other than the standard applications intended.

(4) The products and product specifications described in this material are subject to change without notice for reasons of modification and/or improvement. At the final stage of your design, purchasing, or use of the products, therefore, ask for the most up-to-date Product Standards in advance to make sure that the latest specifications satisfy your requirements.

(5) When designing your equipment, comply with the guaranteed values, in particular those of maximum rating, the range of operating power supply voltage and heat radiation characteristics. Otherwise, we will not be liable for any defect which may arise later in your equipment.
Even when the products are used within the guaranteed values, redundant design is recommended, so that such equipment may not violate relevant laws or regulations because of the function of our products.

(6) When using products for which dry packing is required, observe the conditions (including shelf life and after-unpacking standby time) agreed upon when specification sheets are individually exchanged.

(7) No part of this material may be reprinted or reproduced by any means without written permission from our company.

Please read the following notes before using the datasheets

A. These materials are intended as a reference to assist customers with the selection of Panasonic semiconductor products best suited to their applications.
Due to modification or other reasons, any information contained in this material, such as available product types, technical data, and so on, is subject to change without notice.
Customers are advised to contact our semiconductor sales office and obtain the latest information before starting precise technical research and/or purchasing activities.

B. Panasonic is endeavoring to continually improve the quality and reliability of these materials but there is always the possibility that further rectifications will be required in the future. Therefore, Panasonic will not assume any liability for any damages arising from any errors etc. that may appear in this material.

C. These materials are solely intended for a customer's individual use.
Therefore, without the prior written approval of Panasonic, any other use such as reproducing, selling, or distributing this material to a third party, via the Internet or in any other way, is prohibited.

2001 MAR