

SI-3000LU Series

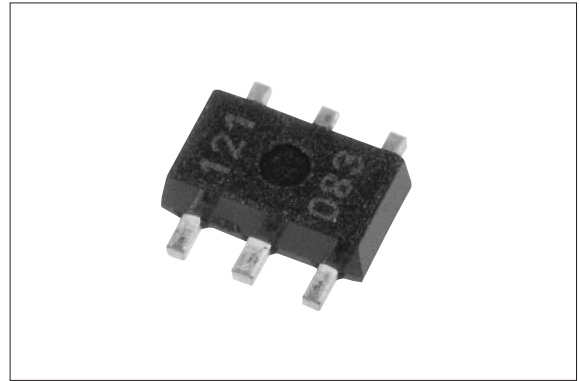
Surface-Mount, Low Current Consumption, Low Dropout Voltage Dropper Type

■Features

- Compact surface-mount package (SOT-89-5)
- Output current: 250 mA
- Low current consumption I_q (OFF) $\leq 1\mu\text{A}$ ($V_c = 0\text{ V}$)
- Low dropout voltage: $V_{DIF} \leq 0.5\text{ V}$ (at $I_o = 250\text{ mA}$)
- 5 types of output voltages (Adj, 1.8 V, 2.5 V, 3.3 V, 5.0 V) available
- Built-in dropping type overcurrent, thermal protection circuits

■Applications

- Auxiliary power supply for PC
- Battery-driven electronic equipment



■Absolute Maximum Ratings

($T_a=25^\circ\text{C}$)

Parameter	Symbol	Ratings	Unit
DC Input Voltage	V_{IN}	18	V
DC Output Current	I_o	250	mA
Power Dissipation	P_D^{*1}	0.75	W
Junction Temperature	T_j^{*2}	-40 to +135	$^\circ\text{C}$
Storage Temperature	T_{op}^{*2}	-40 to +125	$^\circ\text{C}$
Thermal Resistance (Junction to Ambient Air)	θ_{j-a}^{*1}	146	$^\circ\text{C/W}$

*1: When mounted on glass-epoxy board $40 \times 40\text{ mm}$ (copper laminate area 2%)

*2: Thermal protection circuits may operate if the junction temperature exceeds 135°C

■Recommended Operating Conditions

Parameter	Symbol	Ratings		Unit
		min.	max.	
Input Voltage	V_{IN}	*2, *3	V_o+2^{*1}	V
DC Output Current	I_o	0	250	mA
Ambient Operating Temperature	T_{op}	-20	85	$^\circ\text{C}$

*1: V_{IN} (max) and I_o (max) are restricted by the relationship $P_D = (V_{IN} - V_o) \times I_o$.

Calculate these values referring to the reference data.

*2: Refer to the dropout voltage section.

*3: For the SI-3012LU, set the input voltage to at least 2.4 V, and secure the minimum voltage as explained in Setting DC Input Voltage, Dropper Type Application Note.

■Electrical Characteristics

($T_a=25^{\circ}\text{C}$, $V_c=2\text{V}$ unless otherwise specified)

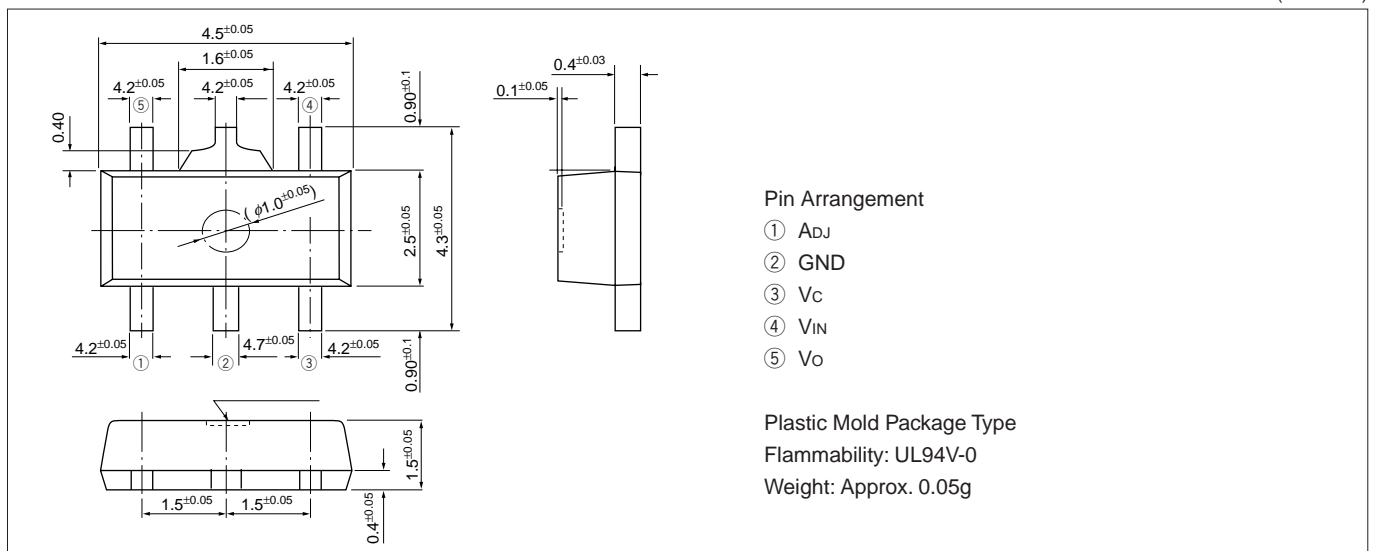
Parameter	Symbol	Ratings															Unit
		SI-3012LU			SI-3018LU ^(Under development)			SI-3025LU ^(Under development)			SI-3033LU			SI-3050LU ^(Under development)			
		min.	typ.	max.	min.	typ.	max.	min.	typ.	max.	min.	typ.	max.	min.	typ.	max.	
Output Voltage	V_o	1.210	1.250	1.290	1.764	1.800	1.836	2.450	2.500	2.550	3.234	3.300	3.366	4.900	5.000	5.100	V
	Conditions	$V_{IN}=V_o+1\text{V}$, $I_o=10\text{mA}$			$V_{IN}=3.3\text{V}$, $I_o=10\text{mA}$			$V_{IN}=3.3\text{V}$, $I_o=10\text{mA}$			$V_{IN}=5\text{V}$, $I_o=10\text{mA}$			$V_{IN}=6\text{V}$, $I_o=10\text{mA}$			
Dropout Voltage	V_{DIF}			0.3			0.5			0.3			0.3			0.3	V
	Conditions	$I_o=100\text{mA}(V_o=3.3\text{V})$			$I_o=100\text{mA}$												
	Conditions	$I_o=250\text{mA}(V_o=3.3\text{V})$			$I_o=250\text{mA}$												
Line Regulation	ΔV_{LINE}			10			10			10			10			15	mV
	Conditions	$V_{IN}=V_o+1$ to $V_o+5\text{V}$, $I_o=10\text{mA}(V_o=3.3\text{V})$			$V_{IN}=2.5$ to 5V , $I_o=10\text{mA}$			$V_{IN}=3.3$ to 5V , $I_o=10\text{mA}$			$V_{IN}=4.5$ to 8V , $I_o=10\text{mA}$			$V_{IN}=6$ to 10V , $I_o=10\text{mA}$			
Load Regulation	ΔV_{LOAD}			20			20			40			40			40	mV
	Conditions	$V_{IN}=V_o+1\text{V}$, $I_o=1$ to $250\text{mA}(V_o=3.3\text{V})$			$V_{IN}=3.3\text{V}$, $I_o=1$ to 250mA			$V_{IN}=3.3\text{V}$, $I_o=0$ to 250mA			$V_{IN}=5\text{V}$, $I_o=0$ to 250mA			$V_{IN}=6\text{V}$, $I_o=0$ to 250mA			
Temperature Coefficient of Output Voltage	$\Delta V_o/\Delta T_a$		± 0.3			± 0.2			± 0.25			± 0.3			± 0.3		mV/ $^{\circ}\text{C}$
	Conditions	$T_j=0$ to 100°C															
Ripple Rejection	R_{REJ}		55			55			55			55			55		dB
	Conditions	$V_{IN}=V_o+1\text{V}$, $f=100$ to $120\text{Hz}(V_o=3.3\text{V})$			$V_{IN}=3.3\text{V}$, $f=100$ to 120Hz			$V_{IN}=3.3\text{V}$, $f=100$ to 120Hz			$V_{IN}=5\text{V}$, $f=100$ to 120Hz			$V_{IN}=6\text{V}$, $f=100$ to 120Hz			
Quiescent Circuit Current	I_q			150			150			150			150			150	μA
	Conditions	$V_{IN}=V_o+1\text{V}$, $I_o=0\text{mA}$, $V_c=2\text{V}$, $R_2=100\text{k}\Omega$			$V_{IN}=3.3\text{V}$, $I_o=0\text{mA}$, $V_c=2\text{V}$			$V_{IN}=3.3\text{V}$, $I_o=0\text{mA}$, $V_c=2\text{V}$			$V_{IN}=5\text{V}$, $I_o=0\text{mA}$, $V_c=2\text{V}$			$V_{IN}=6\text{V}$, $I_o=0\text{mA}$, $V_c=2\text{V}$			
OFF Circuit Current	$I_q(\text{OFF})$			1			1			1			1			1	μA
	Conditions	$V_{IN}=V_o+1\text{V}$, $V_c=0\text{V}$			$V_{IN}=3.3\text{V}$, $V_c=0\text{V}$			$V_{IN}=3.3\text{V}$, $V_c=0\text{V}$			$V_{IN}=5\text{V}$, $V_c=0\text{V}$			$V_{IN}=6\text{V}$, $V_c=0\text{V}$			
Overcurrent Protection Starting Current ^{*1}	I_{S1}	260			260			260			260			260			mA
	Conditions	$V_{IN}=V_o+1\text{V}$			$V_{IN}=3.3\text{V}$			$V_{IN}=3.3\text{V}$			$V_{IN}=5\text{V}$			$V_{IN}=6\text{V}$			
Vc Pin	Control Voltage (Output ON) ^{*2}	V_c, I_H	2.0			2.0			2.0			2.0			2.0		V
	Control Voltage (Output OFF) ^{*2}	V_c, I_L			0.8			0.8			0.8			0.8		0.8	
	Control Current (Output ON)	I_c, I_H			40			40			40			40			μA
	Conditions	$V_c=2\text{V}$															
Control Current (Output OFF)	I_c, I_L		0	-5		0	-5		0	-5		0	-5		0	-5	μA
Conditions	$V_c=0\text{V}$																
Output OFF Voltage	V_o			0.5			0.5			0.5			0.5			0.5	V

*1: I_{S1} is specified as the 5% drop point of output voltage V_o on the condition that $V_{IN} = 3.3\text{V}$ (5V for SI-3033LU, 6V for SI-3050LU), and $I_o = 10\text{mA}$.

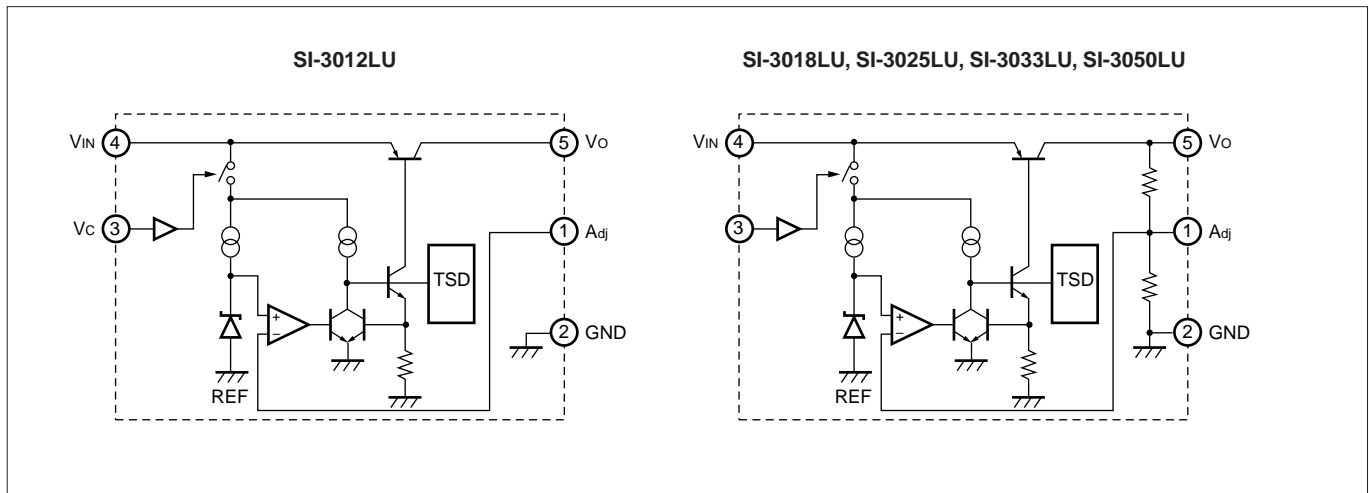
*2: Output is OFF when the output control pin (Vc pin) is open. Each input level is equivalent to that for LS-TTL. Therefore, the device can be driven directly by an LS-TTL circuit.

■External Dimensions

(Unit : mm)

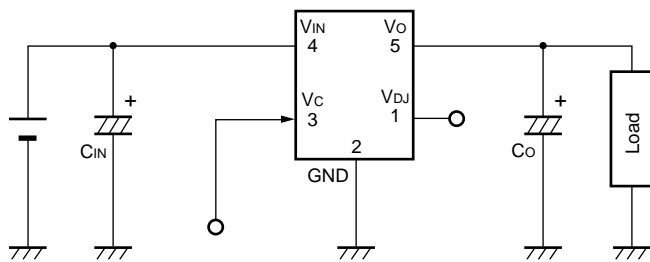


■Block Diagram



■Standard External Circuit

SI-3018LU, SI-3025LU, SI-3033LU, SI-3050LU



Co : Output capacitor (10 μ F or larger)

The SI-3000LU series can be operated on the circuit even if a low ESR ceramic capacitor is used as the output capacitor.

CIN : Input capacitor (0.1 to 10 μ F)

This capacitor is required in the case of an inductive input line or long wiring.

●Settings for SI-3012LU output voltage (recommended voltage: 1.5 V to 15 V)

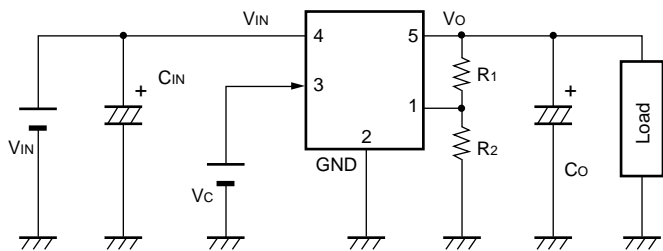
R1 and R2: Resistors for output setting

The output voltage can be set by connecting R1 and R2 as shown in the diagram on the left.

R2: 100 k Ω is recommended

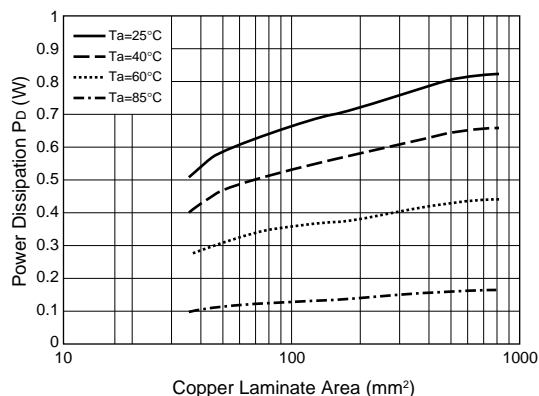
$$R1 = (V_O - V_{ADJ}) / (V_{ADJ} / R2)$$

SI-3012LU



■Reference Data

Copper Laminate Area vs Power Dissipation
Tj=100°C PWB size 40×40



- A monolithic IC is mounted. The inner frame stage is connected to the GND pin (pin 2). Therefore, enlarging the copper laminate area leading to the GND pin achieves a heat radiation effect.

- How to calculate the junction temperature

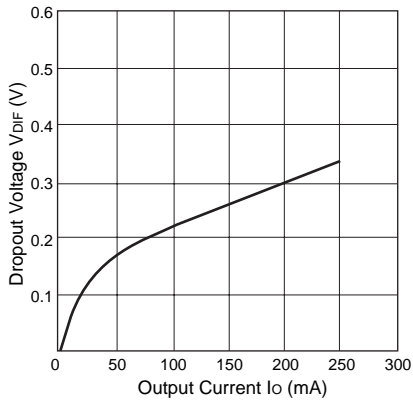
Measure the temperature (Tc) of the GND pin (pin 2) lead section using a thermistor, etc. Substitute this value in the following formula and calculate the junction temperature.

$$T_j = P_D \times \theta_{j-c} + T_c \quad (\theta_{j-c} = 5^\circ\text{C/W})$$

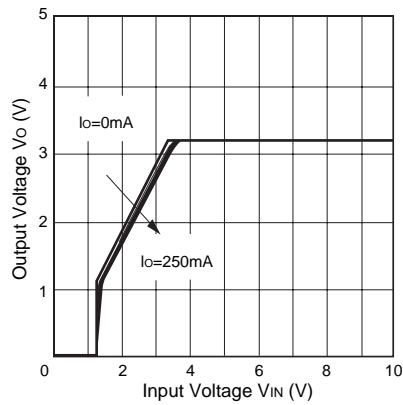
■Typical Characteristics of SI-3033LU

($T_a=25^{\circ}\text{C}$)

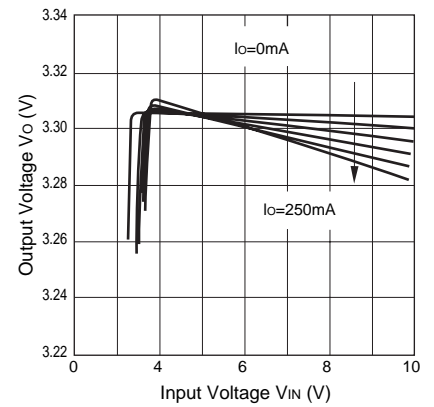
I_o vs. V_{DIF} Characteristics



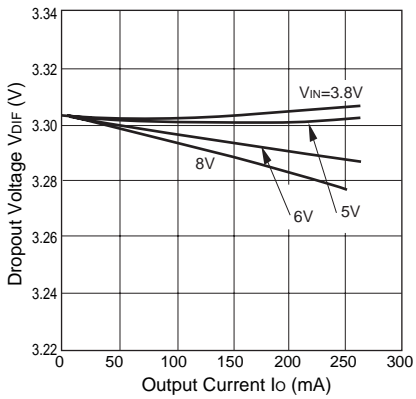
Output Voltage Characteristics



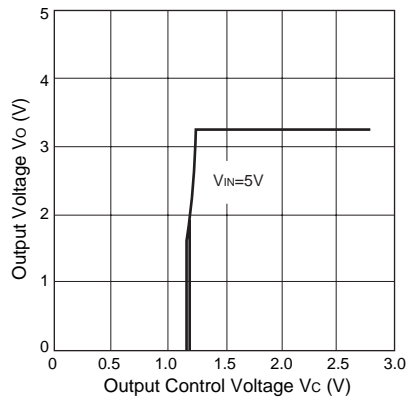
Line Regulation



Load Regulation



Output ON/OFF Control



Overcurrent Protection Characteristics

