

## GENERAL DESCRIPTION

The CMT2301 is the P-Channel logic enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and other battery powered circuits, and low in-line power loss are needed in a very small outline surface mount package.

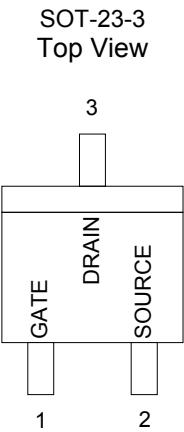
## FEATURES

- ◆ -20V/-2.3A , $R_{DS(ON)}=130\text{ m}\Omega$ @ $VGS=-4.5V$
- ◆ -20V/-1.9A , $R_{DS(ON)}=190\text{ m}\Omega$ @ $VGS=-2.5V$
- ◆ Super high density cell design for extremely low  $R_{DS(ON)}$
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOT-23-3 package design

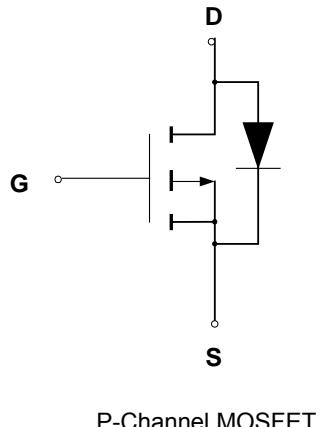
## APPLICATIONS

- ◆ Power Management in Notebook
- ◆ Portable Equipment
- ◆ Battery Powered System
- ◆ DC/DC Converter
- ◆ Load Switch
- ◆ DSC
- ◆ LCD Display inverter

## PIN CONFIGURATION



## SYMBOL



## ORDERING INFORMATION

| Part Number   | Package  |
|---------------|----------|
| CMT2301M233   | SOT-23-3 |
| CMT2301GM233* | SOT-23-3 |

\*Note: G : Suffix for Pb Free Product

**ABSOLUTE MAXIMUM RATINGS**

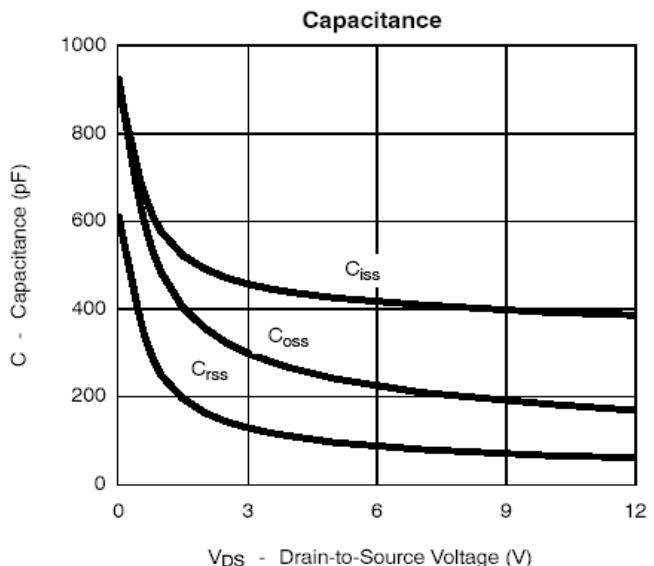
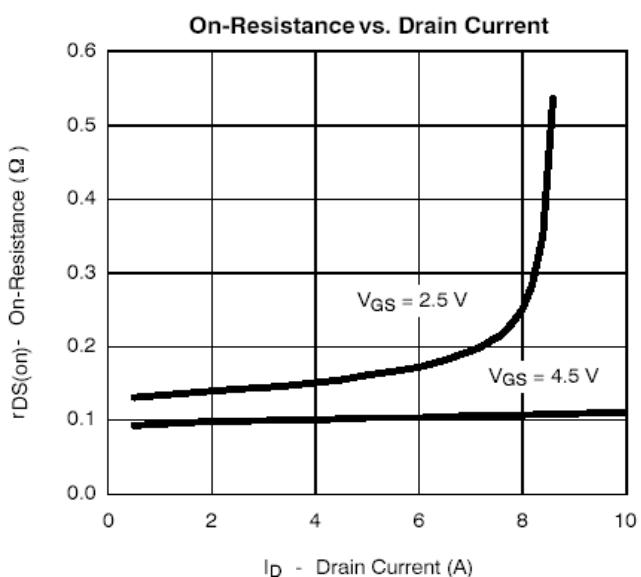
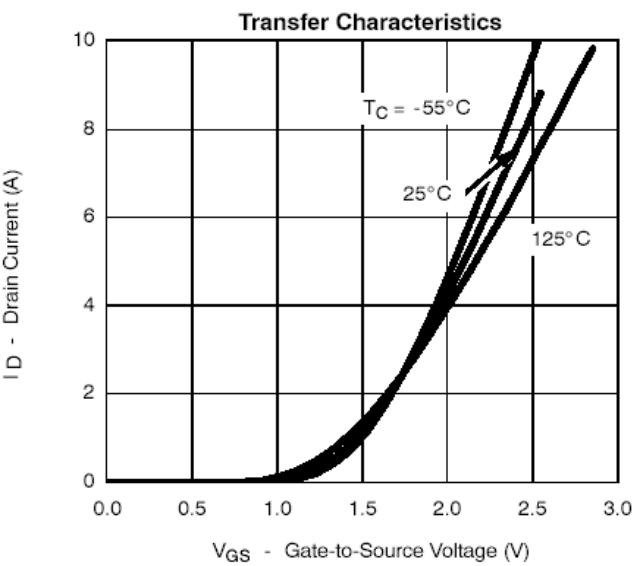
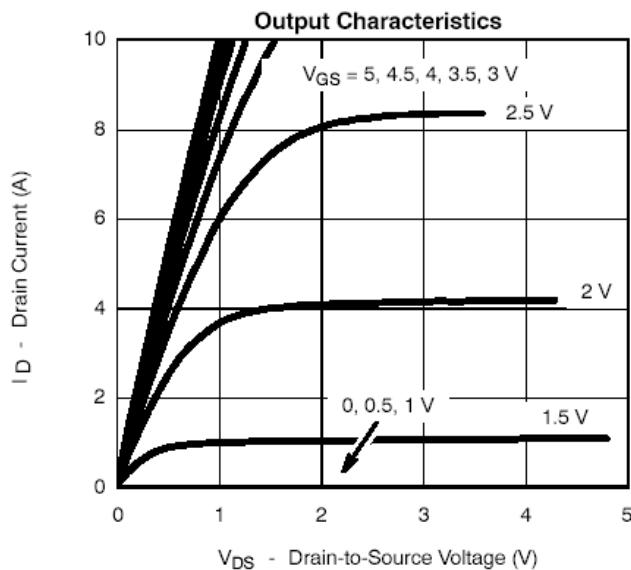
| Rating  | Symbol           | Value   | Unit |
|---|------------------|---------|------|
| Drain- to- Source Voltage                             | V <sub>DSS</sub> | -20     | V    |
| Gate-to-Source Voltage                                | V <sub>GSS</sub> | ±8      | V    |
| Continuous Drain Current( $T_J=150^{\circ}\text{C}$ ) | I <sub>D</sub>   | -2.5    | A    |
|   |                  | -1.5    |      |
| Pulsed Drain Current                                  | I <sub>DM</sub>  | -10     | A    |
| Continuous Source Current(Diode Conduction)           | I <sub>S</sub>   | -1.6    | A    |
| Power Dissipation                                     | P <sub>D</sub>   | 1.25    | W    |
|   |                  | 0.8     |      |
| Operating Junction Temperature                        | T <sub>J</sub>   | 150     | °C   |
| Storage Temperature Range                             | T <sub>STG</sub> | -55/150 | °C   |
| Thermal Resistance-Junction to Ambient                | R <sub>θJA</sub> | 120     | °C/W |

**ELECTRICAL CHARACTERISTICS**

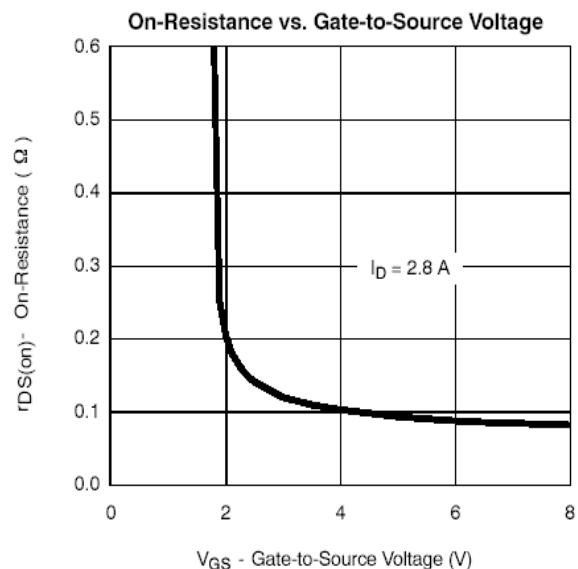
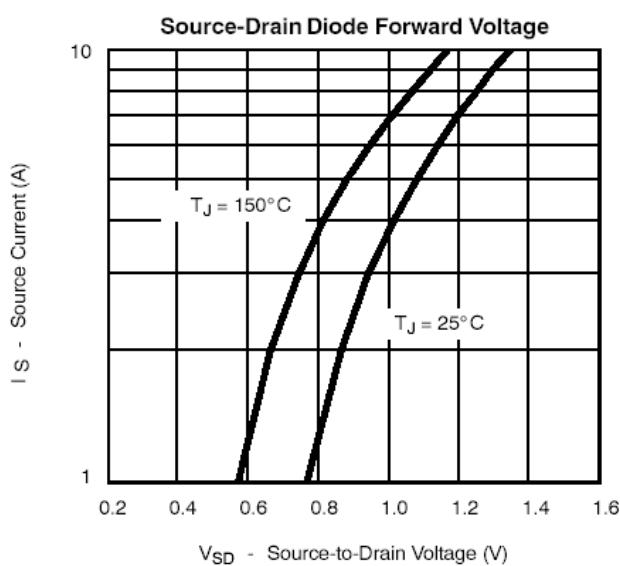
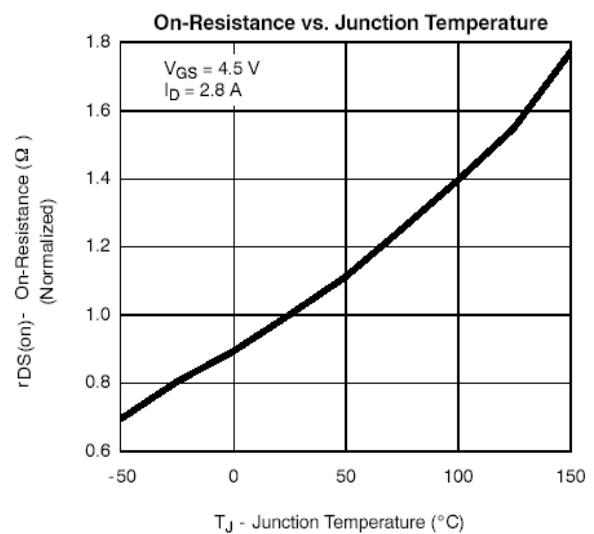
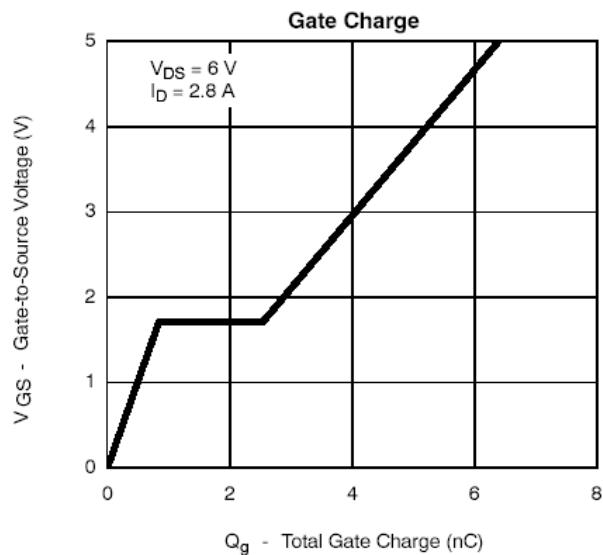
Unless otherwise specified,  $T_J = 25^{\circ}\text{C}$ .

| Characteristic   | Symbol  | CMT2301             |                |              |       |
|--|---|---------------------|----------------|--------------|-------|
|  |   | Min                 | Typ            | Max          | Units |
| <b>Static</b>  |   |                     |                |              |       |
| Drain-Source Breakdown Voltage<br>( $V_{GS} = 0 \text{ V}$ , $I_D = -250 \mu\text{A}$ )  | V <sub>(BR)DSS</sub>  | -20                 |                |              | V     |
| Gate Threshold Voltage<br>( $V_{DS} = V_{GS}$ , $I_D = -250 \mu\text{A}$ )   | V <sub>GS(th)</sub>   | -0.45               |                | -1.5         | V     |
| Gate Leakage Current<br>( $V_{DS} = 0 \text{ V}$ , $V_{GS} = \pm 8 \text{ V}$ )  | I <sub>GSS</sub>  |                     |                | ±100         | nA    |
| Zero Gate Voltage Drain Current<br>( $V_{DS} = -20 \text{ V}$ , $V_{GS} = 0 \text{ V}$ )<br>( $V_{DS} = -20 \text{ V}$ , $V_{GS} = 0 \text{ V}$ , $T_J = 55^{\circ}\text{C}$ ) | I <sub>DSS</sub>  |                     |                | -1<br>-10    | μA    |
| On-State Drain Current<br>( $V_{DS} \leq -5 \text{ V}$ , $V_{GS} = -4.5\text{V}$ )<br>( $V_{DS} \leq -5 \text{ V}$ , $V_{GS} = -2.5\text{V}$ )                                 | I <sub>D(on)</sub>  | -6<br>-3            |                |              | A     |
| Drain-Source On-Resistance<br>( $V_{GS} = -4.5 \text{ V}$ , $I_D = -2.8\text{A}$ )<br>( $V_{GS} = -2.5 \text{ V}$ , $I_D = -2.0\text{A}$ )                                     | R <sub>DS(on)</sub>   |                     | 0.105<br>0.145 | 0.13<br>0.19 | Ω     |
| Forward Transconductance ( $V_{DS} = -5 \text{ V}$ , $I_D = -2.8\text{V}$ )  | g <sub>FS</sub>   |                     | 6.5            |              | S     |
| Diode Forward Voltage ( $I_S = -1.6\text{A}$ , $V_{GS} = 0\text{V}$ )  | V <sub>SD</sub>   |                     | -0.8           | -1.2         | V     |
| <b>Dynamic</b>   |   |                     |                |              |       |
| Input Capacitance  | $(V_{DS} = -6 \text{ V}, V_{GS} = -0\text{V}, f = 1.0 \text{ MHz})$                                     | C <sub>iss</sub>    |                | 415          | pF    |
| Output Capacitance   |   | C <sub>oss</sub>    |                | 223          |       |
| Reverse Transfer Capacitance   |   | C <sub>rss</sub>    |                | 87           |       |
| Turn-On Time   | $(V_{DD} = -6 \text{ V}, R_L = 6\Omega, I_D = -1.0 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_G = 6\Omega)$ | t <sub>d(on)</sub>  |                | 13           | ns    |
| Turn-Off Time  |   | tr                  |                | 36           |       |
| Total Gate Charge  |   | t <sub>d(off)</sub> |                | 42           |       |
| Gate-Source Charge   |   | tf                  |                | 34           |       |
| Gate-Drain Charge  | $(V_{DS} = -6 \text{ V}, I_D = -2.8 \text{ A}, V_{GS} = -4.5\text{V})$                                  | Q <sub>g</sub>      |                | 5.8          | nC    |
|  |   | Q <sub>gs</sub>     |                | 0.85         |       |
|  |   | Q <sub>gd</sub>     |                | 1.7          |       |

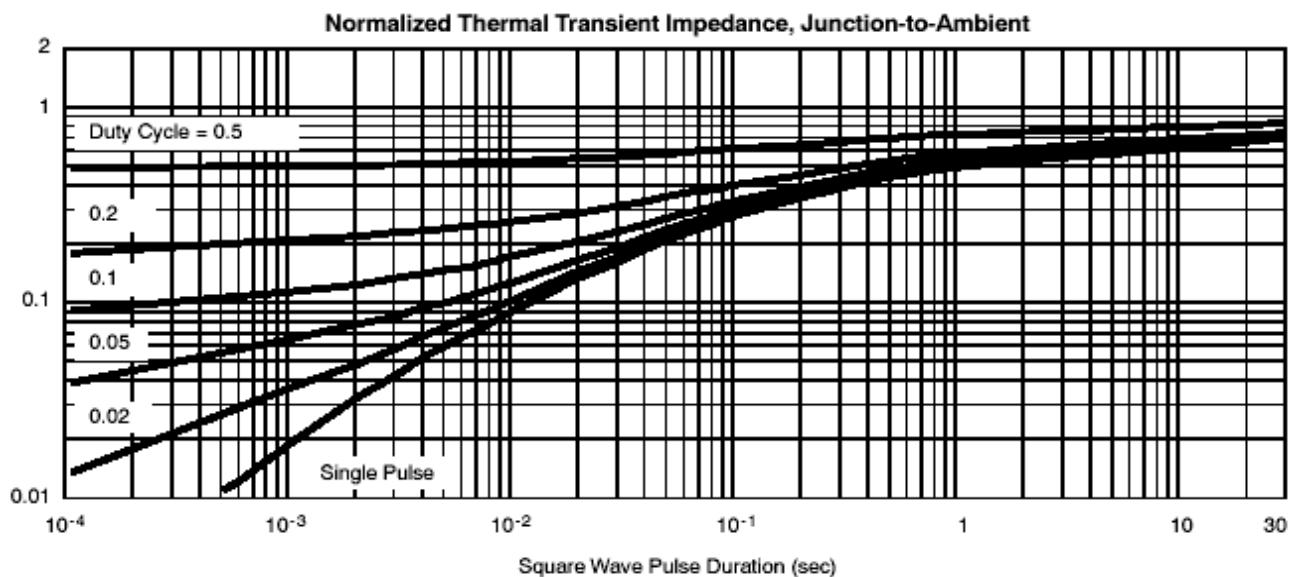
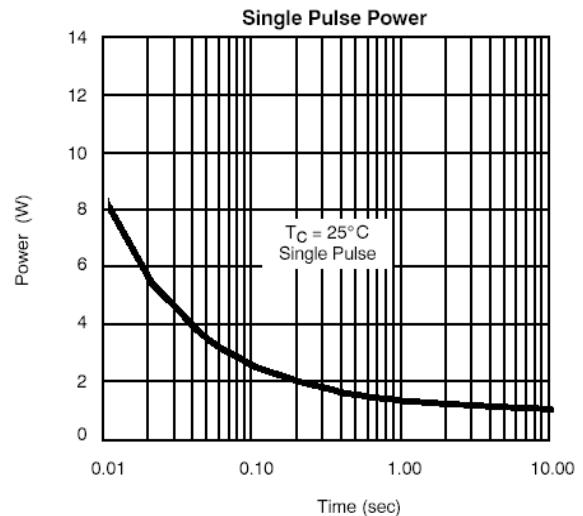
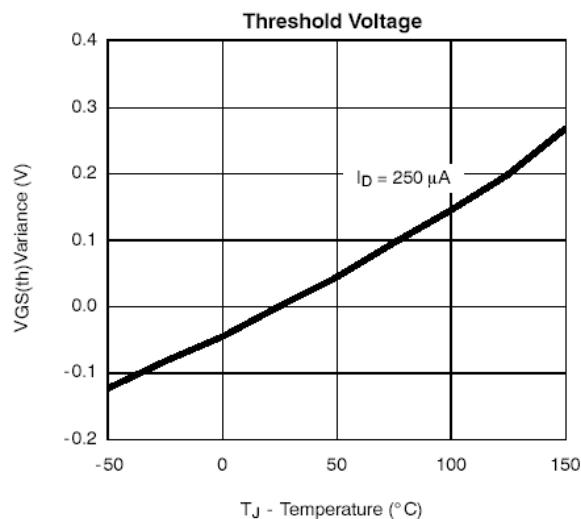
## TYPICAL CHARACTERISTICS

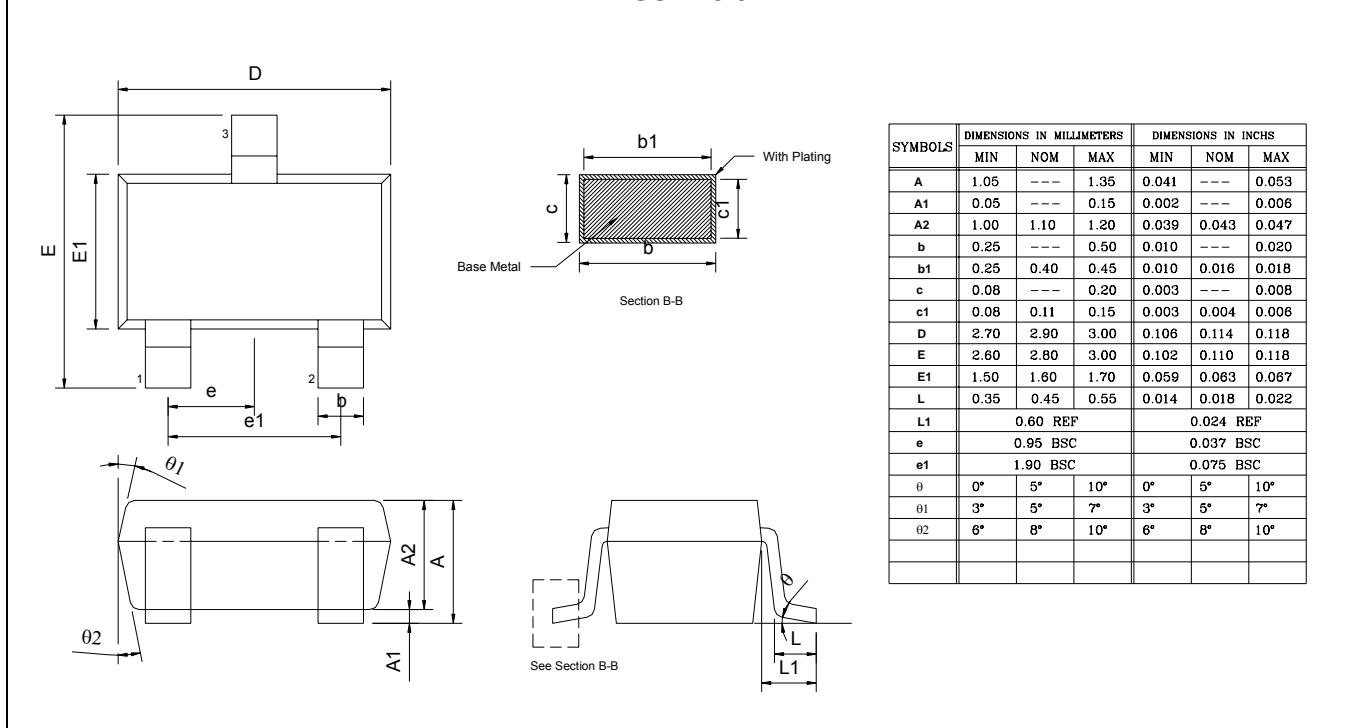


## TYPICAL CHARACTERISTICS



## TYPICAL CHARACTERISTICS



**PACKAGE DIMENSION**
**SOT-23-3**




## **IMPORTANT NOTICE**

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### **HsinChu Headquarter**

5F, No. 11, Park Avenue II,  
Science-Based Industrial Park,  
HsinChu City, Taiwan  
TEL: +886-3-567 9979  
FAX: +886-3-567 9909

### **Sales & Marketing**

11F, No. 306-3, SEC. 1, Ta Tung Road,  
Hsichih, Taipei Hsien 221, Taiwan  
TEL: +886-2-8692 1591  
FAX: +886-2-8692 1596