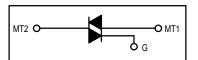
Triacs

Silicon Bidirectional Thyristors

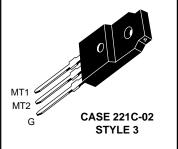
... designed primarily for full-wave ac control applications, such as solid-state relays, motor controls, heating controls and power supplies; or wherever full-wave silicon gate controlled solid-state devices are needed. Triac type thyristors switch from a blocking to a conducting state for either polarity of applied anode voltage with positive or negative gate triggering.

- Blocking Voltage to 800 Volts
- All Diffused and Glass Passivated Junctions for Greater Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation and Durability
- Gate Triggering Guaranteed in Four Modes



MAC15AFP Series

ISOLATED TRIACS THYRISTORS 15 AMPERES RMS 400 thru 800 VOLTS



MAXIMUM RATINGS (T_{.J} = 25°C unless otherwise noted.)

Rating	Symbol	Value	Unit
Repetitive Peak Off-State Voltage ⁽¹⁾ (T _J = -40 to +125°C, 1/2 Sine Wave 50 to 60 Hz, Gate Open)	VDRM		Volts
MAC15A6FP MAC15A8FP MAC15A10FP		400 600 800	
On-State RMS Current ($T_C = +80^{\circ}C$)(2) Full Cycle Sine Wave 50 to 60 Hz ($T_C = +95^{\circ}C$)	lT(RMS	15 12	Amps
Peak Nonrepetitive Surge Current (One Full Cycle, 60 Hz, T _C = +80°C) preceded and followed by rated current	ITSM	150	Amps
Peak Gate Power (T _C = +80°C, Pulse Width = 2 μs)	P _{GM}	20	Watts
Average Gate Power (T _C = +80°C, t = 8.3 ms)	P _G (AV)	0.5	Watt
Peak Gate Current	IGM	2	Amps
Peak Gate Voltage	V _{GM}	10	Volts
RMS Isolation Voltage (T _A = 25°C, Relative Humidity ≤ 20%)	V _{(ISO}	1500	Volts
Operating Junction Temperature	TJ	-40 to +125	°C
Storage Temperature Range	T _{stg}	-40 to +150	°C

^{1.} V_{DRM} for all types can be applied on a continuous basis. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.



^{2.} The case temperature reference point for all T_C measurements is a point on the center lead of the package as close as possible to the plastic body.

MAC15AFP Series

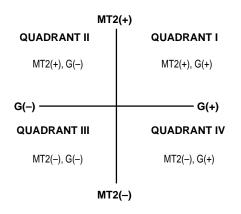
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{ heta JC}$	2	°C/W
Thermal Resistance, Case to Sink	$R_{\theta}CS$	2.2 (typ)	°C/W
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	60	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Peak Blocking Current (Either Direction) T _J = 25°C (V _D = Rated V _{DRM} , T _J = 125°C, Gate Open)	I _{DRM}	_ _	_	10 2	μA mA
Peak On-State Voltage (Either Direction) (I _{TM} = 21 A Peak; Pulse Width = 1 to 2 ms, Duty Cycle ≤ 2%)	V _{TM}	_	1.3	1.6	Volts
Gate Trigger Current (Continuous dc) (Main Terminal Voltage = 12 Vdc, R _L = 100 Ohms) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-) MT2(-), G(+)	I _{GT}			50 50 50 75	mA
Gate Trigger Voltage (Continuous dc) (Main Terminal Voltage = 12 Vdc, R _L = 100 Ohms) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-) MT2(-), G(-) MT2(-), G(+) (Main Terminal Voltage = Rated V _{DRM} , R _L = 10 k Ω , T _J = +110°C) MT2(+), G(+); MT2(-), G(-); MT2(+), G(-); MT2(-), G(+)	Vgт	 0.2	0.9 0.9 1.1 1.4	2 2 2 2.5 —	Volts
Holding Current (Either Direction) (Main Terminal Voltage = 12 Vdc, Gate Open, Initiating Current = 200 mA)	lн	_	6	40	mA
Turn-On Time (V_D = Rated V_{DRM} , I_{TM} = 17 A, I_{GT} = 120 mA, Rise Time = 0.1 μ s, Pulse Width = 2 μ s)	^t gt		1.5	_	μs
Critical Rate of Rise of Commutation Voltage (V_D = Rated V_{DRM} , I_{TM} = 21 A, Commutating di/dt = 7.6 A/ms, Gate Unenergized, T_C = 80°C)	dv/dt(c)	_	5	_	V/µs

QUADRANT DEFINITIONS



Trigger devices are recommended for gating on Triacs. They provide:

- 1. Consistent predictable turn-on points.
- 2. Simplified circuitry.
- 3. Fast turn-on time for cooler, more efficient and reliable operation.

ELECTRICAL CHARACTERISTICS of RECOMMENDED BIDIRECTIONAL SWITCHES

Usage	General		
Part Number	MBS4991	MBS4992	
Vs	6–10 V	7.5–9 V	
IS	350 μA Max	120 μA Max	
V _{S1} -V _{S2}	0.5 V Max	0.2 V Max	
Temperature Coefficient	0.02%/°C Typ		

^{1.} Ratings apply for open gate conditions. Thyristor devices shall not be tested with a constant current source for blocking capability such that the voltage applied exceeds the rated blocking voltage.

TYPICAL CHARACTERISTICS

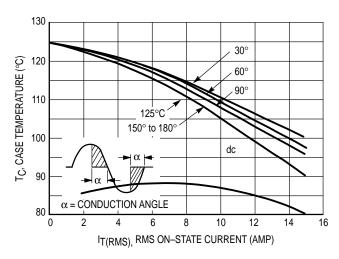


Figure 1. RMS Current Derating

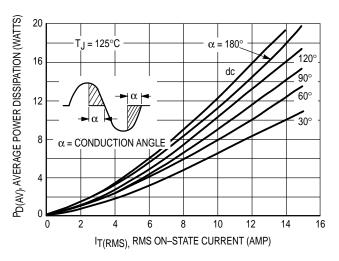


Figure 2. On-State Power Dissipation

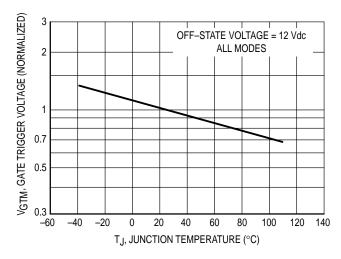


Figure 3. Typical Gate Trigger Voltage

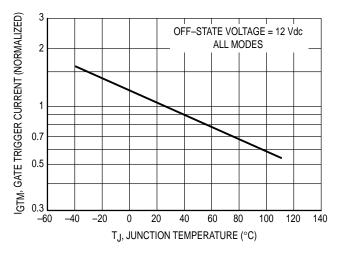


Figure 4. Typical Gate Trigger Current

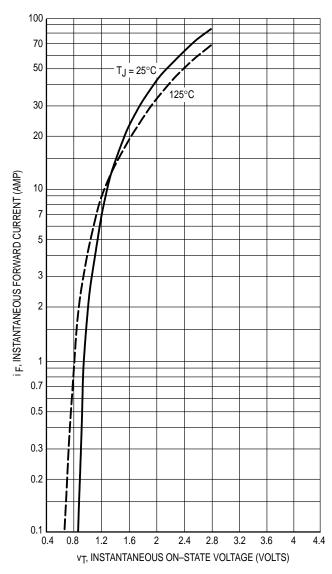
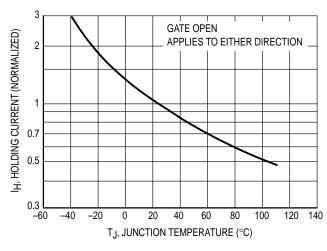


Figure 5. Maximum On-State Characteristics

MAC15AFP Series



300
200
100
70
TC = 80°C
- f = 60 Hz
SURGE IS PRECEDED AND FOLLOWED BY RATED CURRENT
2 3 5 7 10
NUMBER OF CYCLES

Figure 6. Typical Holding Current

Figure 7. Maximum Nonrepetitive Surge Current

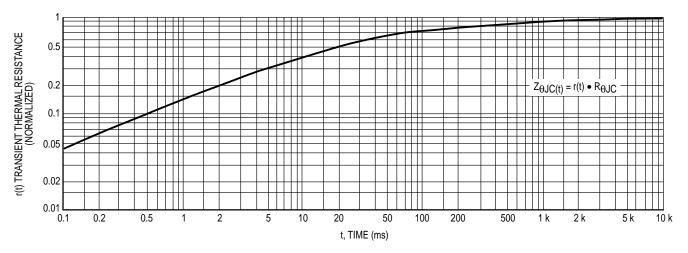
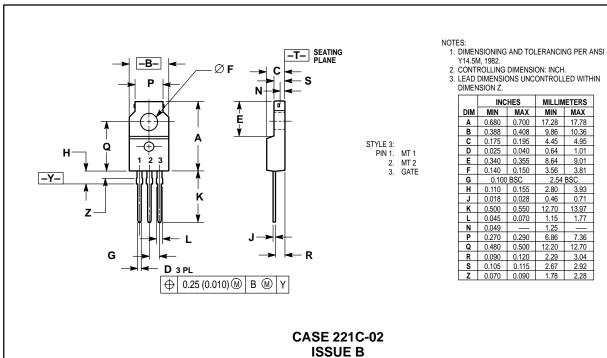


Figure 8. Thermal Response

PACKAGE DIMENSIONS



	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.680	0.700	17.28	17.78	
В	0.388	0.408	9.86	10.36	
С	0.175	0.195	4.45	4.95	
D	0.025	0.040	0.64	1.01	
E	0.340	0.355	8.64	9.01	
F	0.140	0.150	3.56	3.81	
G	0.100	BSC	2.54	BSC	
Н	0.110	0.155	2.80	3.93	
J	0.018	0.028	0.46	0.71	
K	0.500	0.550	12.70	13.97	
L	0.045	0.070	1.15	1.77	
N	0.049		1.25		
Р	0.270	0.290	6.86	7.36	
Q	0.480	0.500	12.20	12.70	
R	0.090	0.120	2.29	3.04	
S	0.105	0.115	2.67	2.92	
Z	0.070	0.090	1.78	2.28	

NOTES

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