

## Overview

The LB1867M is a 2-phase unipolar brushless motor driver. With only a few peripheral parts, lockup protection and automatic recovery can be implemented. The IC can be configured for 12 V or 24 V operation and a wide range of variations, from LOW speed to H-High speed and from 60 cm to 120 cm square using the same PCB. This makes it easy to design highly reliable fan motor installations.

## Functions and Features

- Output protection Zener diode with variable withstand voltage
Z1, Z2 pins open: VOLM $=57 \mathrm{~V}(24 \mathrm{~V}$ specification $)$
$\mathrm{Z} 1, \mathrm{Z} 2$ pins shorted: VOLM $=32 \mathrm{~V}$ (12V specification)
External Zener diode connected across Z1-VCC pins: support for fans with large drive current
- External resistor allows configuration for 12 V or 24 V .
- Direct Hall element connection possible (built-in Hall amplifier with hysteresis supports core without auxiliary electrode)
- Built-in output transistor with 1.0A output current (strengthened negative-current support for core without auxiliary electrode)
- Built-in rotation detection function: Low during rotation and High during stop
- Built-in lockup protection with automatic recovery
- Built-in thermal shutdown


## Package Dimensions

unit: mm
3111-MFP14S


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## Specifications

Absolute Maximum Ratings at $\mathbf{T a}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :--- | :---: | :---: | :---: | :---: |
| Maximum input current | ICC max | $\mathrm{t} \leq 20 \mathrm{~ms}$ | 200 | mA |
| Maximum applied output voltage | VOUT max |  | Internal | V |
| Maximum output current | IOUT max |  | 1.0 | A |
| Maximum current flowing into RD pin | IRD max |  | 10 | mA |
| Maximum RD applied voltage | VRD max |  | 30 | V |
| Allowable power dissipation | Pd max | *With specified substrate | mW |  |
| Operating temperature | Topr |  | -300 | $\mathrm{mo} \mathrm{+80}$ |
| Storage temperature | Tstg |  | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

*Printed circuit board ( $20 \times 15 \times 1.5 \mathrm{~mm}^{3}$ glass epoxy)

Allowable Operating Ranges at $\mathbf{T a}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :--- | :---: | :---: | :---: | :---: |
| Input voltage range | ICC |  | 6.0 to 50 | mA |
| Common mode input voltage range | VICM |  | 0.2 to VIN-1.5 | V |

Electrical Characteristics at $\mathbf{T a}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$, $\mathrm{Icc}=\mathbf{1 0} \mathbf{~ m A}$

| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| Output limiter withstand voltage | VOLM1 | Z1, Z2 open | 54 | 57 | 60 | V |
|  | VOLM2 | Z1, Z2 short | 31 | 33 | 35 | V |
| Output saturation voltage | Vosat 1 $2$ | $\begin{aligned} & \mathrm{IO}=0.5 \mathrm{~A} \\ & \mathrm{IO}=1.0 \mathrm{~A} \end{aligned}$ |  | $\begin{aligned} & 0.95 \\ & 0.15 \end{aligned}$ | 1.2 1.5 | $\begin{aligned} & \mathrm{V} \\ & \mathrm{~V} \end{aligned}$ |
| VIN voltage | VIN | ICC $=7.0 \mathrm{~mA}$ | 6.4 | 6.7 | 7.0 | V |
| Hall input sensitivity (at zero peak) | VHN | Including offset and hysteresis |  |  | 20 | mV |
| RD output saturation voltage | VRDsat | IRD $=5 \mathrm{~mA}$ |  | 0.1 | 0.3 | V |
| CT drain current | IC1 | $\mathrm{C}=\mathrm{GND}$ | 2.7 | 3.8 | 4.9 | $\mu \mathrm{A}$ |
| CT discharge current | IC2 | $\mathrm{C}=\mathrm{VIN}$ | 0.19 | 0.30 | 0.41 | $\mu \mathrm{A}$ |
| Comp input threshold voltage | VTH1 <br> VTH2 |  | $\begin{aligned} & 0.77 \\ & 0.42 \end{aligned}$ | $\begin{gathered} 0.8 \mathrm{VIN} \\ 0.45 \mathrm{VIN} \end{gathered}$ | $\begin{aligned} & 0.83 \\ & 0.48 \end{aligned}$ | $\begin{aligned} & \mathrm{V} \\ & \mathrm{~V} \end{aligned}$ |
| Thermal protection operating temperature | TSD | Design target value* |  | 180 |  | ${ }^{\circ} \mathrm{C}$ |
| Thermal protection circuit hysteresis | $\triangle \mathrm{TSD}$ | Design target value* |  | 40 |  | ${ }^{\circ} \mathrm{C}$ |

* Design target values are not measured.

Pd max - Ta


## LB1867M

## Block Diagram and Sample Application Circuit



## Truth Table

| IN+ | IN- | CT | OUT1 | OUT2 | RD |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $H$ | L | L | H | L | L |
| L | H | L | L | H | L |
| $H$ | L | H | H | H | H |
| L | $H$ | $H$ | $H$ | $H$ | H |

Pin Description

| Pin name | Function |
| :---: | :--- |
| IN- | Hall input + pin Hysteresis amplifier |
| $\mathrm{IN}+$ | Hall input - pin Hysteresis amplifier |
| CT | Lockup protection time setting capacitor pin (0.47 to $4.7 \mu \mathrm{~F})$ |
| Z1 | External Zener diode pin (external Zener diode to be connected between power supply and Z1) |
| Z2 | Kickback absorption voltage alteration pin (shorted to Z1: 12V operation) |
| OUT1 | Output 1 pin |
| OUT2 | Output 2 pin |
| VIN | Regulated power supply input pin (limiting resistor to be inserted between power supply and VIN) |
| GND | GND pin |
| RD | Lockup detection pin (latch type) |

## Pin Assignment



Top view

## LB1867M Sample Application Circuits

(1) 12 V supply voltage

(2) 24 V supply voltage

(3) High-Power Fan (120 mm-HH-Speed)


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