



3.3V CMOS HEX INVERTER WITH 5 VOLT TOLERANT INPUTS

IDT74LVCU04A

FEATURES:

- 0.5 MICRON CMOS Technology
- ESD > 2000V per MIL-STD-883, Method 3015; > 200V using machine model (C = 200pF, R = 0)
- Vcc = 3.3V ± 0.3V, Normal Range
- Vcc = 2.7V to 3.6V, Extended Range
- CMOS power levels (0.4μ W typ. static)
- Rail-to-Rail output swing for increased noise margin
- Inputs are 5V tolerant
- Available in SOIC, SSOP, and TSSOP packages

DRIVE FEATURES:

- High Output Drivers: ±24mA
- Reduced system switching noise

DESCRIPTION:

The LVCU04A hex inverter is built using advanced dual metal CMOS technology. This device contains six independent inverters with unbuffered outputs and performs the Boolean function $Y = \bar{A}$.

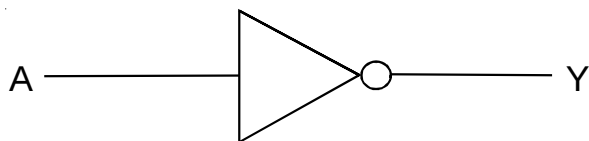
The LVCU04A has been designed with a ±24mA output driver. This driver is capable of driving a moderate to heavy load while maintaining speed performance.

Inputs can be driven from either 3.3V or 5V devices. This feature allows the use of this device as a translator in a mixed 3.3V/5V system environment.

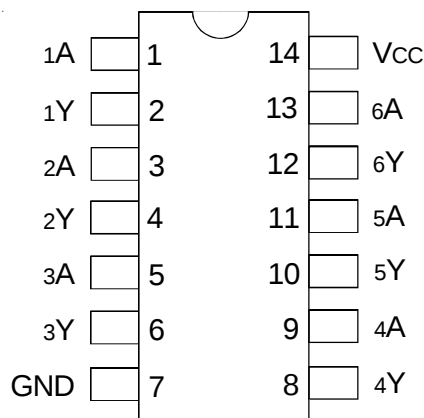
APPLICATIONS:

- 5V and 3.3V mixed voltage systems
- Data communication and telecommunication systems

FUNCTIONAL BLOCK DIAGRAM



PIN CONFIGURATION



SOIC/ SSOP/ TSSOP
TOP VIEW

PIN DESCRIPTION

Pin Names	Description
x A	Data Inputs
x Y	Data Outputs

FUNCTION TABLE (EACH INVERTER)⁽¹⁾

Inputs	Outputs
x A	x Y
H	L
L	H

NOTE:

1. H = HIGH Voltage Level
L = LOW Voltage Level

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Symbol	Description	Max	Unit
V _{TERM} ⁽²⁾	Terminal Voltage with Respect to GND	−0.5 to +6.5	V
V _{TERM} ⁽³⁾	Terminal Voltage with Respect to GND	−0.5 to V _{CC} +0.5	V
T _{STG}	Storage Temperature	−65 to +150	°C
I _{OUT}	DC Output Current	−50 to +50	mA
I _{IK} I _{OK}	Continuous Clamp Current, V _I < 0 or V _O < 0	−50	mA
I _{CC} I _{SS}	Continuous Current through each V _{CC} or GND	±100	mA

NOTES:

- Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
- V_{CC} and Input terminals.
- Output terminals only.

CAPACITANCE (T_A = +25°C, F = 1.0MHz)

Symbol	Parameter ⁽¹⁾	Conditions	Typ.	Max.	Unit
C _{IN}	Input Capacitance	V _{IN} = 0V	4.5	6	pF
C _{OUT}	Output Capacitance	V _{OUT} = 0V	5.5	8	pF
C _{I/O}	I/O Port Capacitance	V _{IN} = 0V	6.5	8	pF

NOTE:

- As applicable to the device type.

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:

Operating Condition: T_A = −40°C to +85°C

Symbol	Parameter	Test Conditions		Min.	Typ. ⁽¹⁾	Max.	Unit
V _{IH}	Input HIGH Voltage Level	V _{CC} = 1.65V		1.32	—	—	V
		V _{CC} = 2.3V		1.84	—	—	
		V _{CC} = 2.7V		2.16	—	—	
		V _{CC} = 3V		2.4	—	—	
		V _{CC} = 3.6V		2.88	—	—	
V _{IL}	Input LOW Voltage Level	V _{CC} = 1.65V		—	—	0.4	V
		V _{CC} = 2.3V		—	—	0.5	
		V _{CC} = 2.7V to 3.6V		—	—	0.65	
I _{IH} I _{IL}	Input Leakage Current	V _{CC} = 3.6V	V _I = 0 to 5.5V	—	—	±5	μA
V _{IK}	Clamp Diode Voltage	V _{CC} = 2.3V, I _{IN} = −18mA		—	−0.7	−1.2	V
V _H	Input Hysteresis	V _{CC} = 3.3V		—	100	—	mV
I _{CC} I _{CH} I _{CZ}	Quiescent Power Supply Current	V _{CC} = 3.6V, V _{IN} = GND or V _{CC}		—	—	10	μA
ΔI _{CC}	Quiescent Power Supply Current Variation	One input at V _{CC} - 0.6V, other inputs at V _{CC} or GND		—	—	500	μA

NOTE:

- Typical values are at V_{CC} = 3.3V, +25°C ambient.

OUTPUT DRIVE CHARACTERISTICS

Symbol	Parameter	Test Conditions ⁽¹⁾		Min.	Max.	Unit
VOH	Output HIGH Voltage	VCC = 1.65V to 3.6V	IOH = – 0.1mA	VCC – 0.2	—	V
		VCC = 1.65V	IOH = – 4mA	1.2	—	
		VCC = 2.3V	IOH = – 8mA	1.7	—	
		VCC = 2.7V	IOH = – 12mA	2.2	—	
		VCC = 3V		2.4	—	
		VCC = 3V	IOH = – 24mA	2.2	—	
VOL	Output LOW Voltage	VCC = 1.65V to 3.6V	IOL = 0.1mA	—	0.2	V
		VCC = 1.65V	IOL = 4mA	—	0.45	
		VCC = 2.3V	IOL = 8mA	—	0.7	
		VCC = 2.7V	IOL = 12mA	—	0.4	
		VCC = 3V	IOL = 24mA	—	0.55	

NOTE:

1. VIH and VIL must be within the min. or max. range shown in the DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE table for the appropriate VCC range. TA = – 40°C to + 85°C.

OPERATING CHARACTERISTICS, TA = 25°C

Symbol	Parameter	Test Conditions	VCC = 2.5V ± 0.2V	VCC = 3.3V ± 0.3V	Unit
			Typical	Typical	
CPD	Power Dissipation Capacitance per Inverter	CL = 0pF, f = 10MHz	—	5	pF

SWITCHING CHARACTERISTICS⁽¹⁾

Symbol	Parameter	VCC = 1.8V	VCC = 2.5V ± 0.2V		VCC = 2.7V		VCC = 3.3V ± 0.3V		Unit
		Typ.	Min.	Max.	Min.	Max.	Min.	Max.	
tPLH	Propagation Delay	12.7	1	6.7	—	4.7	1	3.8	ns
tPHL	xA to xY								
tSK(0)	Output Skew ⁽²⁾	—	—	—	—	—	—	1	ns

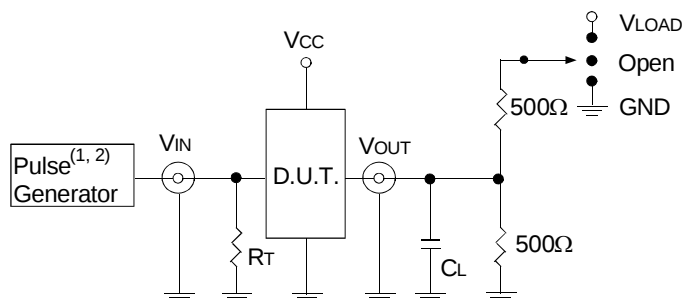
NOTES:

1. See TEST CIRCUITS AND WAVEFORMS. TA = – 40°C to + 85°C.
2. Skew between any two outputs of the same package and switching in the same direction.

TEST CIRCUITS AND WAVEFORMS

TEST CONDITIONS

Symbol	V _{CC} ⁽¹⁾ = 2.5V ± 0.2V	V _{CC} ⁽²⁾ = 3.3V ± 0.3V & 2.7V	Unit
V _{LOAD}	2 x V _{CC}	6	V
V _{IH}	V _{CC}	3	V
V _T	V _{CC} / 2	1.5	V
V _{LZ}	150	300	mV
V _{HZ}	150	300	mV
C _L	30	50	pF



Test Circuit for All Outputs

DEFINITIONS:

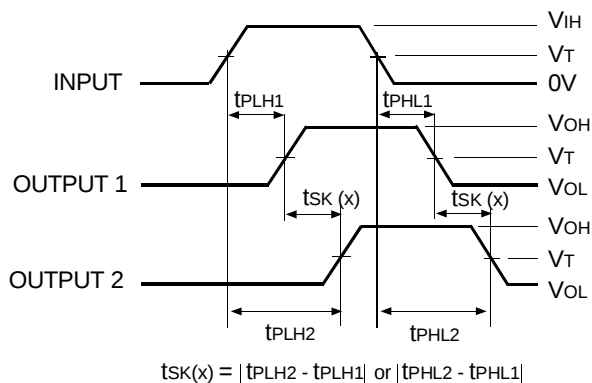
C_L = Load capacitance: includes jig and probe capacitance.
R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator.

NOTES:

1. Pulse Generator for All Pulses: Rate ≤ 10MHz; t_r ≤ 2ns; t_r ≤ 2ns.
2. Pulse Generator for All Pulses: Rate ≤ 10MHz; t_r ≤ 2.5ns; t_r ≤ 2.5ns.

SWITCH POSITION

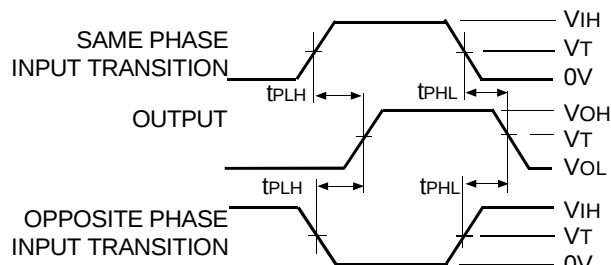
Test	Switch
Open Drain Disable Low Enable Low	V _{LOAD}
Disable High Enable High	GND
All Other Tests	Open



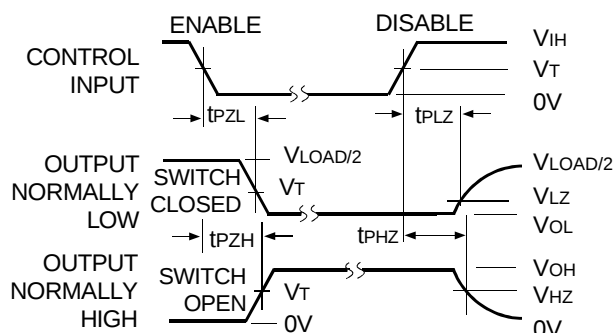
Output Skew - t_{SK}(x)

NOTES:

1. For t_{SK}(0) OUTPUT1 and OUTPUT2 are any two outputs.
2. For t_{SK}(b) OUTPUT1 and OUTPUT2 are in the same bank.



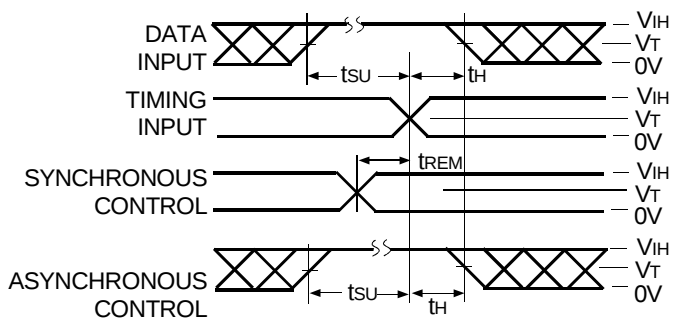
Propagation Delay



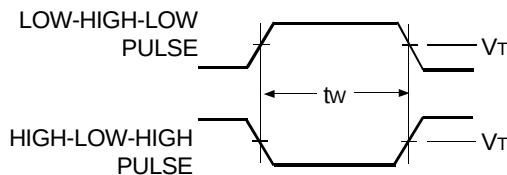
NOTE:

1. Diagram shown for input Control Enable-LOW and input Control Disable-HIGH.

Enable and Disable Times



Set-up, Hold, and Release Times



Pulse Width

ORDERING INFORMATION

IDT	XX	LVC	XXX	XX	
Temp. Range		Device Type		Package	
				DC	Small Outline IC
				PY	Shrink Small Outline Package
				PG	Thin Shrink Small Outline Package
				U04A	Hex Inverter, $\pm 24\text{mA}$
				74	-40°C to $+85^{\circ}\text{C}$



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