

POWER SCHOTTKY RECTIFIER

MAIN PRODUCT CHARACTERISTICS

I_F	1 A
V_{RRM}	40 V
V_F (max)	0.49 V
T_j (max)	150°C

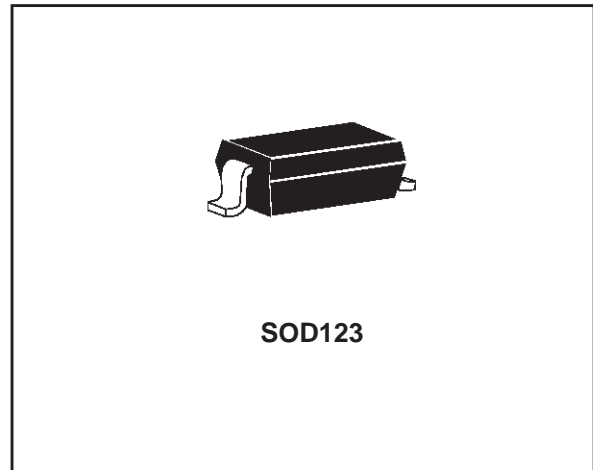
FEATURES AND BENEFITS

- VERY SMALL CONDUCTION LOSSES
- NEGLIGIBLE SWITCHING LOSSES
- EXTREMELY FAST SWITCHING

DESCRIPTION

Single Schottky rectifier suited for Switchmode Power Supplies and high frequency DC to DC converters.

Packaged in SOD123, this device is intended for use in low voltage, high frequency inverters, free wheeling and polarity protection applications. Due to the small size of the package this device fit GSM and PCMCIA requirements.



ABSOLUTE RATINGS (limiting values)

Symbol	Parameter		Value	Unit
V_{RRM}	Repetitive peak reverse voltage		40	V
I_F	Continuous forward current	$T_{amb} = 60\text{ °C}$	1	A
I_{FSM}	Surge non repetitive forward current	$t_p = 10\text{ ms}$ Sinusoidal	5.5	A
I_{RRM}	Repetitive peak reverse current	$t_p = 2\text{ }\mu\text{s square}$ $F = 1\text{ kHz}$	0.5	A
I_{RSM}	Non repetitive peak reverse current	$t_p = 100\mu\text{s square}$	1	A
T_{stg}	Storage temperature range		- 65 to + 150	°C
T_j	Maximum operating junction temperature *		150	
T_L	Maximum temperature for soldering during 10s		260	°C
dV/dt	Critical rate of rise of reverse voltage		10000	V/ μs

* $\frac{dP_{tot}}{dT_j} < \frac{1}{R_{th(j-a)}}$ Thermal runaway condition for a diode on its own heatsink.

STPS140Z

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-a)}$	Junction to ambient *	175	°C/W

* with 50 mm² copper area (e=35µm)

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Tests Conditions	Tests Conditions	Min.	Typ.	Max.	Unit	
I_R *	Reverse leakage current	$T_j = 25^\circ\text{C}$	$V_R = 5\text{V}$			10	µA
		$T_j = 25^\circ\text{C}$	$V_R = 40\text{V}$			40	µA
		$T_j = 100^\circ\text{C}$			1.5	5	mA
V_F **	Forward voltage drop	$T_j = 25^\circ\text{C}$	$I_F = 1\text{A}$			0.55	V
		$T_j = 100^\circ\text{C}$			0.45	0.51	

Pulse test : * $t_p = 5\text{ms}$, $\delta < 2\%$
 ** $t_p = 380\mu\text{s}$, $\delta < 2\%$

To evaluate the maximum conduction losses use the following equation :
 $P = 0.2 \times I_{F(AV)} + 0.3 \times I_{F(RMS)}^2$ at $T_j = 150^\circ\text{C}$

Fig. 1: Average forward power dissipation versus average forward current.

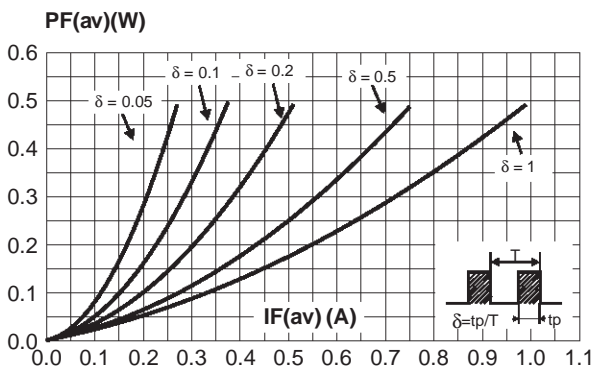


Fig. 2: Average forward current versus ambient temperature ($\delta=1$).

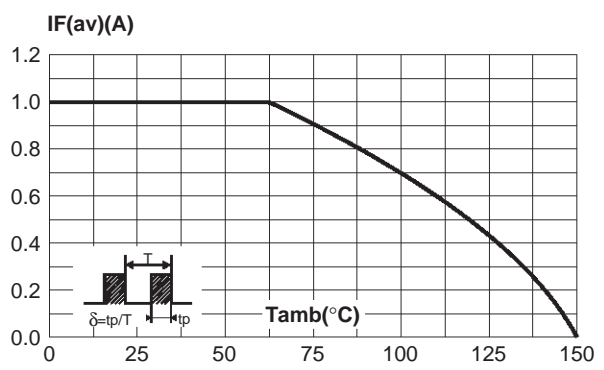


Fig. 3: Non repetitive surge peak forward current versus overload duration (maximum values).

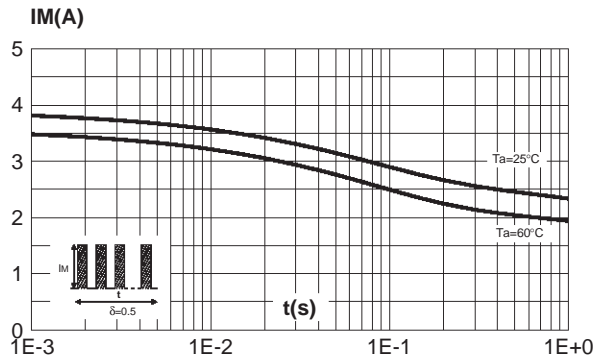


Fig. 4: Relative variation of thermal impedance junction to ambient versus pulse duration (epoxy printed circuit board FR4 with recommended pad layout t).

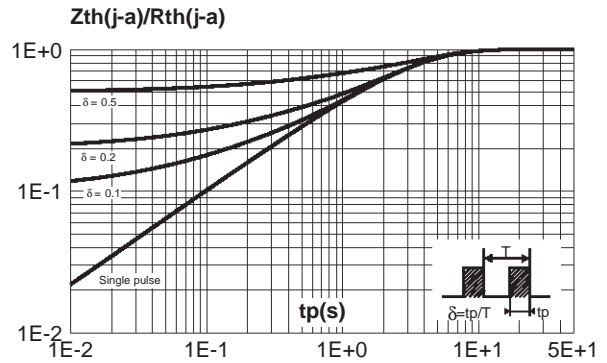


Fig. 5: Reverse leakage current versus reverse voltage applied (typical value).

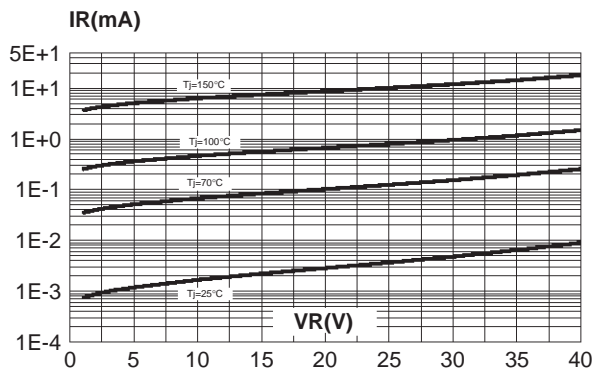


Fig. 6: Reverse leakage current versus junction temperature (typical value).

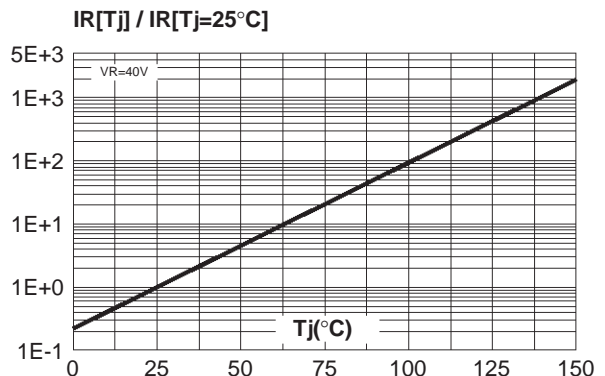


Fig. 7: Junction capacitance versus reverse voltage applied (typical value).

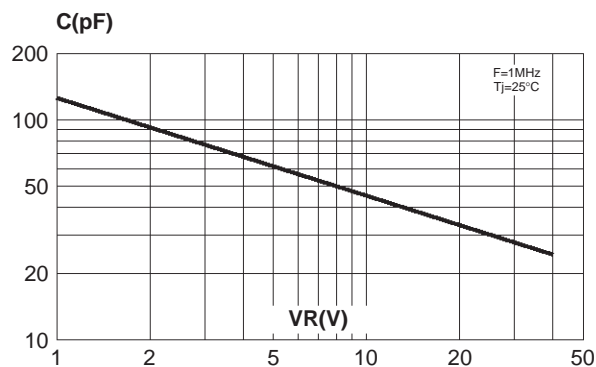


Fig. 8-1: Forward voltage drop versus forward current (high level, maximum values).

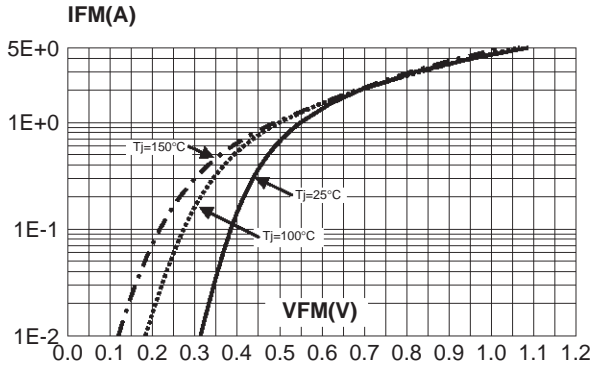


Fig. 8-2: Forward voltage drop versus forward current (low level, maximum values).

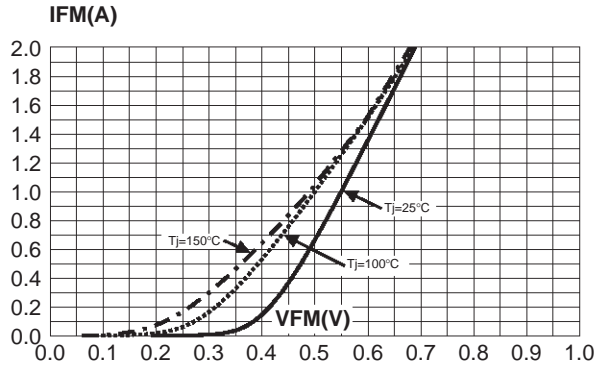
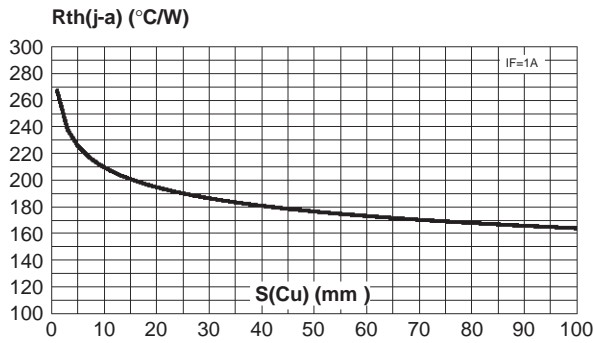
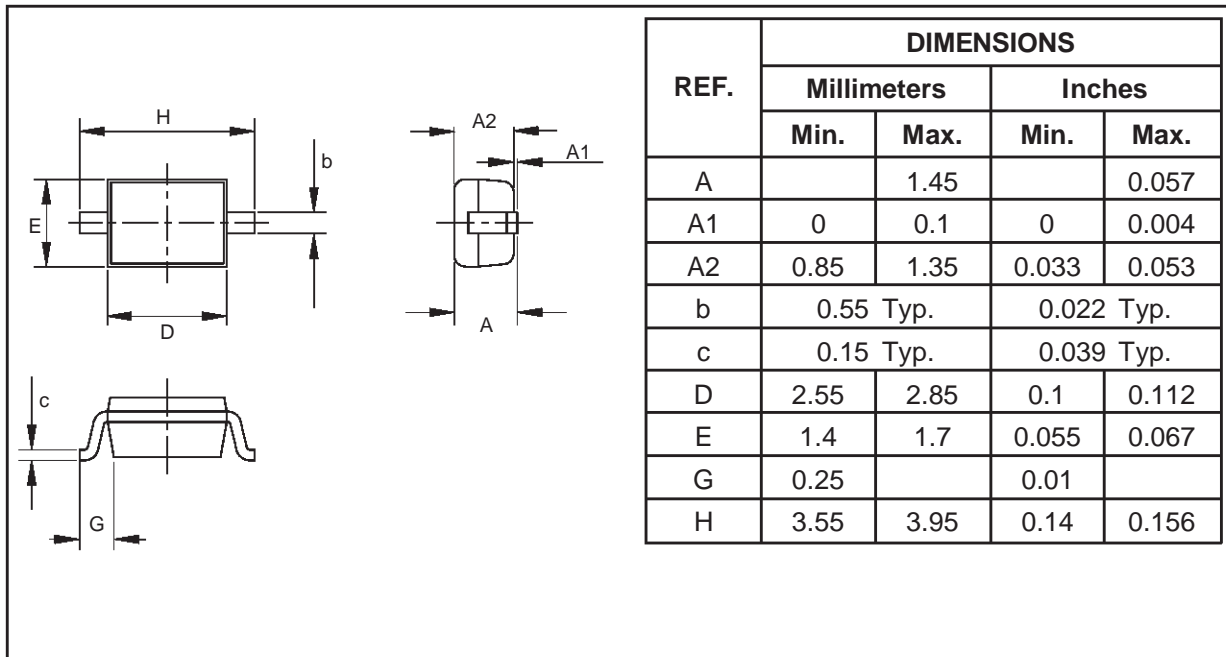


Fig. 9: Thermal resistance junction to ambient versus copper surface (epoxy printed circuit board FR4, copper thickness: 35µm).



PACKAGE MECHANICAL DATA
 SOD123 Plastic


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