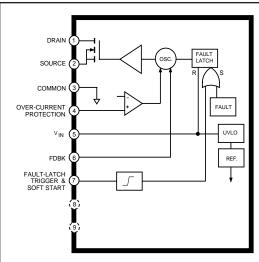
# **STR-S6513**

## OFF-LINE SWITCHING REGULATOR - WITH POWER MOSFET OUTPUT



#### ARSOLUTE MAXIMUM RATINGS

ABSOLUTE WAXIIVIOW RATINGS					
Supply Voltage, V <sub>IN</sub> <b>35 V</b>					
Drain-Source Voltage, V <sub>DS</sub> 400 V					
Continuous Drain Current, I <sub>D</sub> 10 A					
1 ms Single-Pulse Drain Current,					
Single-Pulse Avalanche Energy, E <sub>A</sub> 400 mJ					
Soft-Start/Fault-Latch Trigger Voltage,					
Feedback Input Current, I <sub>FDBK</sub> 20 mA					
Over-Current Protection Voltage,					
V <sub>OCP</sub> 3.5 V					
Insulation Voltage, V <sub>WM(RMS)</sub> 2000 V					
Package Power Diss, P <sub>D</sub> <b>See Graph</b>					
FET Junction Temperature, T <sub>J</sub> +150°C					
Internal Frame Temperature, T <sub>F</sub> <b>+125°C</b>					
Operating Temperature Range, T <sub>A</sub> 20°C to +125°C					
Storage Temperature Range,					

The STR-S6513 is specifically designed to meet the requirement for increased integration and reliability in off-line flyback converters operating in the quasi-resonant ringing choke mode. The device incorporates the primary control and drive circuit with a discrete avalanche-rated power MOSFET.

Crucial system parameters such as maximum ON time and OFF time are fixed during manufacture. Local control circuit decoupling and layout are optimized within the device.

Cycle-by-cycle current limiting, soft start, under-voltage lock-out with hysteresis, over-voltage protection, and thermal shutdown protect the device during all normal and overload conditions. Over-voltage protection, thermal shutdown, or an external fault signal are latched. The dual requirements of dielectric isolation and low transient thermal impedance and steady-state thermal resistance are satisfied in an overmolded single-in-line power package.

Proven in substantial volumes, this device and its fixed-frequency counterparts represents a significant advance in off-line SMPS reliability growth and integration. A similar off-line switching regulator, with an output rating of 600 V at 6 A, is also available.

## **FEATURES**

- Quasi-Resonant Ringing Choke Converter
- Avalanche-Rated Power MOSFET Switch
- Pulse-by-Pulse Current Limiting
- Latched Over-Voltage and Thermal Protection
- Maximum ON Time and OFF Time Set During Manufacture
- Internal Under-Voltage Lockout with Hysteresis
- Over-Molded SIP with Integral Isolated Heat Spreader

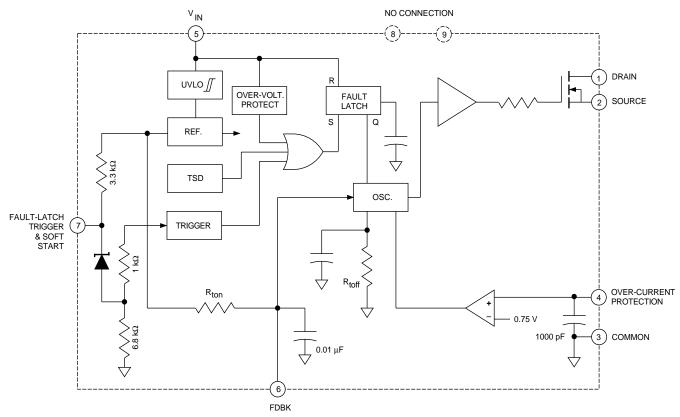
Always order by complete part number: |STR-S6513|





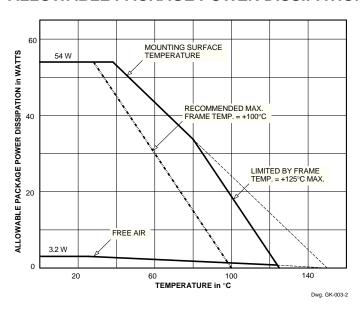
## STR-S6513 OFF-LINE SWITCHING REGULATOR

#### **FUNCTIONAL BLOCK DIAGRAM**

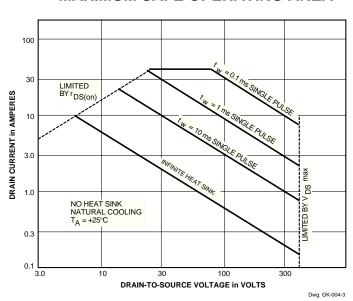


Dwg. FK-002-3

## ALLOWABLE PACKAGE POWER DISSIPATION



## **MAXIMUM SAFE OPERATING AREA**

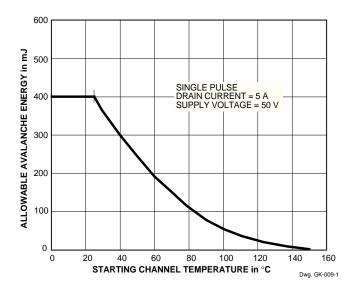






## STR-S6513 OFF-LINE SWITCHING REGULATOR

## **ALLOWABLE AVALANCHE ENERGY**



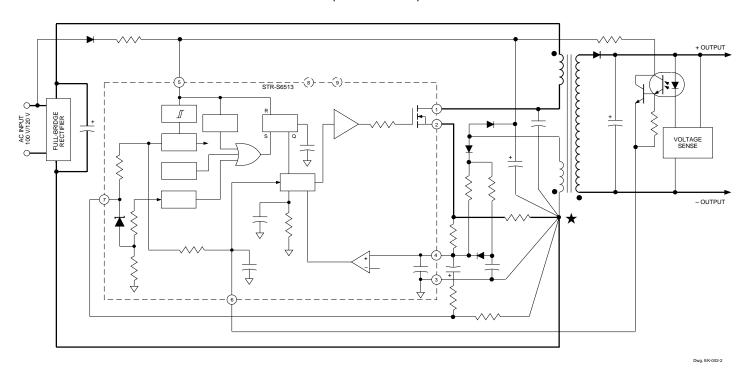
## ELECTRICAL CHARACTERISTICS at $T_A = +25^{\circ}C$ , $V_{IN} = 18$ V, voltage measurements are referenced to Common (pin 3) (unless otherwise noted).

			Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
On-State Voltage	V <sub>INT</sub>	Turn-on, increasing V <sub>IN</sub>	14.4	_	17.6	V
Under-Voltage Lockout	V <sub>INQ</sub>	Turn-off, decreasing V <sub>IN</sub>	9.0	-	11	V
Over-Voltage Threshold	V <sub>OVP(th)</sub>		26	_	31	V
FET Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> = 400 V	_	_	300	μΑ
FET ON Resistance	r <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_{D} = 5.5 \text{ A}$	_	0.45	0.58	Ω
Output Fall Time	t <sub>f</sub>	$V_{DD} = 200 \text{ V}, I_{D} = 5 \text{ A}, 10\% \text{ to } 90\% \text{ V}_{DS}$	_	_	250	ns
Maximum ON Time	t <sub>on</sub>		18	_	22	μs
Minimum OFF Time	t <sub>off</sub>		0.6	_	1.6	μs
Over-Current Threshold	V <sub>OCP(th)</sub>		700	_	800	mV
Feedback Threshold	V <sub>FDBK(th)</sub>		_	750	_	mV
Soft-Start Current	I <sub>SS</sub>	V <sub>SS</sub> = 0 V	-2.2	_	-3.4	mA
Fault-Latch Threshold Volt.	$V_{FL(th)}$		6.9	_	7.7	V
Fault-Latch Holding Current	I <sub>INH</sub>	V <sub>IN</sub> reduced to 8.5 V	_	340	400	μΑ
Fault-Latch Reset Voltage	V <sub>INQ</sub>	I <sub>IN</sub> <20 μA, V <sub>IN</sub> reduced from 31 V	6.5	_	8.5	V
Insulation RMS Voltage	V <sub>WM(RMS)</sub>	All terminals simultaneous reference metal plate against backside	2000	-	-	V
Supply Current	I <sub>IN(ON)</sub>	Operating	18	_	28	mA
	I <sub>IN(OFF)</sub>	Start up, V <sub>IN</sub> =14 V	-	_	100	μΑ
Thermal Shutdown	T <sub>J</sub>		125	150	_	°C
Thermal Resistance	$R_{\theta JM}$	FET junction to mounting surface	-	2.0	_	°C/W

NOTES: Negative current is defined as coming out of (sourcing) the specified device terminal. Typical Data is for design information only.

#### TYPICAL APPLICATION

WARNING: lethal potentials are present. See text.



#### **APPLICATIONS INFORMATION**

A WAR

**WARNING** — These devices are designed to be operated at lethal voltages and energy levels.

Circuit designs that embody these components must conform with applicable safety requirements.

Precautions must be taken to prevent accidental contact with power-line potentials.

Do not connect grounded test equipment.

The use of an isolation transformer is recommended during circuit development and breadboarding.

The power MOSFET outputs of these devices are similar to the International Rectifier type IRF740. These devices feature an excellent combination of fast switching, ruggedized device design, low on-resistance, and cost effectiveness.

Recommended mounting hardware torque:

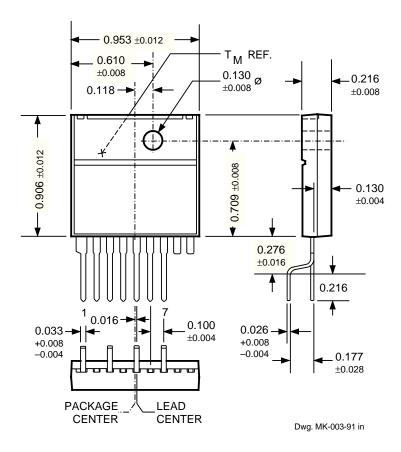
4.34 - 5.79 lbf•ft (6 - 8 kg•cm or 0.588 - 0.784 Nm).

Recommended metal-oxide-filled, alkyl-degenerated oil base, silicone grease: Dow Corning 340, or equivalent



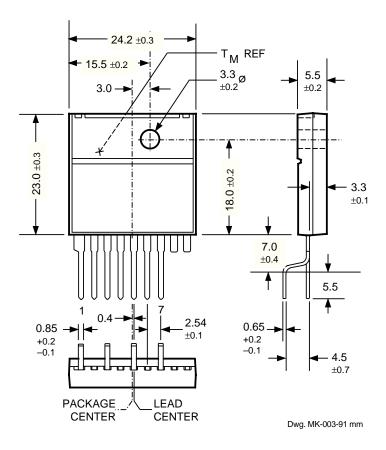


## Dimensions in Inches (for reference only)



## **Dimensions in Millimeters**

(controlling dimensions)



NOTE: Exact body and lead configuration at vendor's option within limits shown.

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