# Advance Information

# The RF Small Signal Line Silicon Lateral FET

## N-Channel Enhancement-Mode MOSFET

Designed for use in low voltage, moderate power amplifiers such as portable analog and digital cellular radios and PC RF modems.

- Performance Specifications at 5.8 V, 900 MHz: Output Power = 30 dBm Min Power Gain = 10 dB Typ Efficiency = 50% Min
- Guaranteed Ruggedness at Load VSWR = 20:1
- New Plastic Surface Mount Package
- Available in Tape and Reel Packaging.
   T1 Suffix = 1,000 Units per 8 mm, 7 inch Reel
- Device Marking = 9745

# MRF9745T1

30 dBm, 900 MHz HIGH FREQUENCY POWER TRANSISTOR LDMOS FET



CASE 449-02, STYLE 1 (PLD-1)

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	35	Vdc
Drain–Gate Voltage ( $R_{GS} = 1 M\Omega$ )	V <sub>DGO</sub>	25	Vdc
Gate-Source Voltage	V <sub>GS</sub>	±10	Vdc
Drain Current – Continuous	ΙD	2	Adc
Total Device Dissipation @ T <sub>C</sub> = 50°C Derate above 50°C	PD	10 100	W mW/°C
Storage Temperature Range	T <sub>stg</sub>	- 65 to +150	°C
Operating Temperature Range	TJ	150	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	10	°C/W

#### **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Drain–Source Leakage Current (V <sub>DS</sub> = 35 V, V <sub>GS</sub> = 0)	IDSS	_	_	10	μAdc
Gate-Source Leakage Current (VGS = 5 V, VDS = 0)	IGSS	ı	I	1	μAdc

 $NOTE - \underline{\textbf{CAUTION}}$  - MOS devices are susceptible to damage from electrostatic charge. Reasonable precautions in handling and packaging MOS devices should be observed.



### **ELECTRICAL CHARACTERISTICS – continued** ( $T_C = 25$ °C unless otherwise noted)

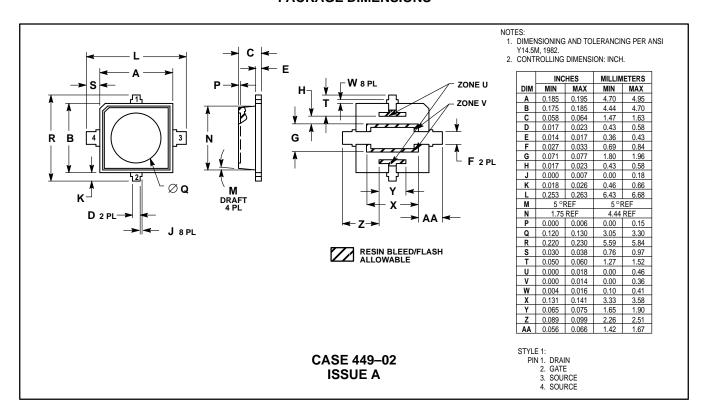
Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS				•	•
Gate Threshold Voltage ( $V_{DS} = 6 \text{ V}, I_D = 25 \mu\text{A}$ )	VGS(th)	1	2	3	Vdc
Forward Transconductance (VDS = 6 V, ID = 200 mA)	9fs	-	550	-	mmhos
Resistance Drain–Source (V <sub>GS</sub> = 4 V, I <sub>D</sub> = 100 mA)	R <sub>DS(on)</sub>	-	1	2.5	Ω
DYNAMIC CHARACTERISTICS	•			•	•
Input Capacitance (V <sub>DS</sub> = 6 V, V <sub>GS</sub> = 0, f = 1 MHz)	C <sub>iss</sub>	-	14	_	pF
Output Capacitance (VDS = 6 V, VGS = 0, f = 1 MHz)	C <sub>oss</sub>	-	11	-	pF
Feedback Capacitance (VDS = 6 V, VGS = 0, f = 1 MHz)	C <sub>rss</sub>	-	1.8	-	pF
FUNCTIONAL CHARACTERISTICS	•			•	•
Power Gain (V <sub>DD</sub> = 5.8 Vdc, P <sub>in</sub> = 20 dBm, I <sub>DQ</sub> = 150 mA, f = 900 MHz)	G <sub>ps</sub>	9.5	10	_	dB
Drain Efficiency (V <sub>DD</sub> = 5.8 Vdc, P <sub>in</sub> = 20 dBm, I <sub>DQ</sub> = 150 mA, f = 900 MHz)	ηD	50	55	-	%
Ruggedness Test (V <sub>DD</sub> = 5.8 Vdc, P <sub>in</sub> = 20 dBm, I <sub>DQ</sub> = 150 mA, f = 900 MHz, Load VSWR = 20:1, All Phase Angles at Frequency Test)	Ψ	No Degradation in Output Power after Test			

Table 1. Large Signal Impedance  $V_{DD}$  = 5.8 V,  $P_{in}$  = 20 dBm,  $I_{DQ}$  = 150 mA

f MHz	Z <sub>in</sub> Ohms	Z <sub>OL</sub> * Ohms
850	7.0 – j6.4	6.1 – j5.1
900	5.2 – j6.5	5.9 – j4.6
950	5.2 – j6.0	6.1 – j4.7

Z<sub>OL</sub>\* is the conjugate of the optimum load impedance into which the device output operates at a given output power, voltage and frequency.

#### **PACKAGE DIMENSIONS**



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