# AX+COM, INC. Preliminary Information

**MX366** 

#### **QUAD FILTER ARRAY**

#### **Features**

- Pair of Independent Lowpass Filters
- Pair of Audio Bandpass Filters (300-3000 Hz)
- Input Gain Adjustments
- Output Enable/Mute for Squeich Functions
- Low Power CMOS

#### **Applications**

- ACSB
- AMPS/TACS/N-AMPS
- Cellular Phones

# MX366P (PDIP) MX366DW 18 Pins 20-Pin SOIC

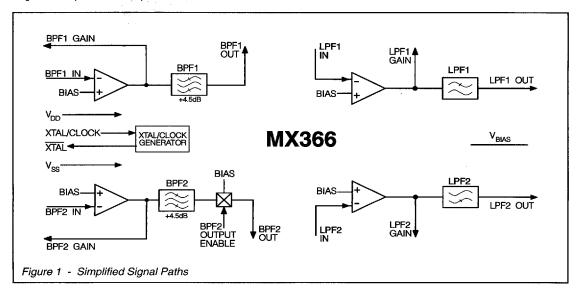
#### **DESCRIPTION**

The MX366 Quad Filter Array is comprised of 4 separate filter/gain blocks on a single IC as described below:

- 1) A pair of 10th order 3.1 kHz lowpass filters.
- A pair of 14<sup>th</sup> order channel bandpass filters (300-3000 Hz).
- Op-amps that allow external components to set input gains and pre- or de-emphasis.
- 4) A buffered low noise output with switching clock filter.
- 5) Output-enabled switching circuitry for squelch control.

This simple, comprehensive amplifier/filter combination eliminates the need for several separate ICs, and therefore saves power and space.

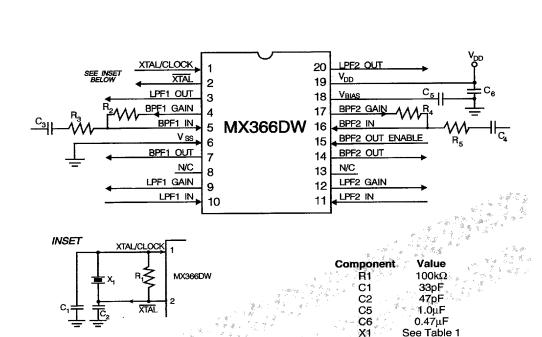
The MX366 uses CMOS switched-capacitor filter technology and requires a supply of 4.5 V to 5.5 V to facilitate battery operation.



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Notes: 1. R2, R3, C3, R4, R5 and C4 should be chosen with respect to the specific configuration used.

- 2. Xtal circuitry shown is in accordance with MX-COM's Application Note on Crystal Oscillators (page 354 of the 1991 Product Handbook).
- Operation of any MX-COM IC without a Xtal or clock may cause damage to the device. To minimize damage in the event of a Xtal/drive failure, a current limiting device (resistor fast-reaction fuse) should be installed on the power supply line (VDD).

Tolerances:  $R = \pm 10\%$ ,  $C = \pm 20\%$ 

Figure 2 - Recommended External Components

TABLE 1 - MX366 CRYSTAL FREQUENCY/FILTER RELATIONSHIP

Crystal Frequency	Bandpass Filter	Lowpass Filter		
4.433619 MHz	300 - 3000 Hz	3100 Hz		
4.40 MHz	298 - 2977 Hz	3076 Hz		
4.096 MHz	277 - 2772 Hz	2864 Hz		
4.032 MHz	272 - 2728 Hz	2819 Hz		
4.00 MHz	270 - 2706 Hz	2797 Hz		
3.579545 MHz	242 - 2422 Hz	2503 Hz		

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#### **PIN FUNCTION CHART**

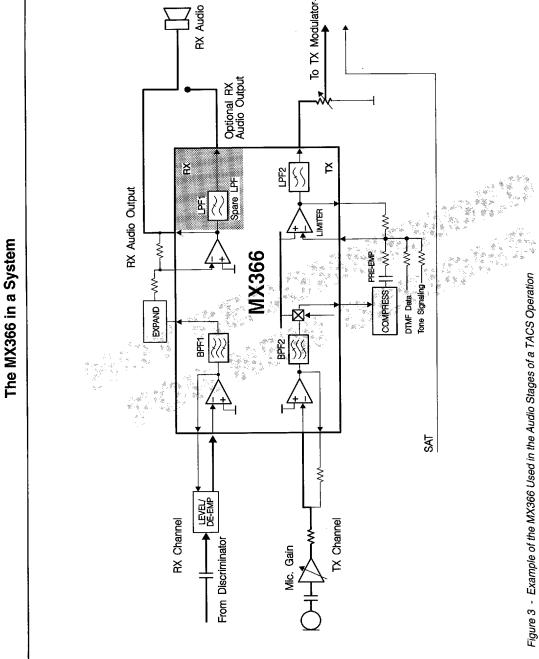
P	in .	Function					
MX366J/P	MX366DW						
1	1	Xtal/Clock: A Xtal per Table 1 or an externally derived clock is injected at this pin.					
2	2	Xtal: This is the output of the clock oscillator inverter.					
3	3	LPF1 Out: This is the output of the LPF1 filter/gain block.					
4	4	<b>BPF1 Gain:</b> This is the output of the BPF1 gain-adjusting amplifier. This output is used with BPF1 In and external components.					
5	5	BPF1 In: This is the input to the BPF1 filter/gain block.					
6	6	V <sub>ss</sub> : Negative supply (GND).					
7	7	BPF1 Out: This is the output of BPF1.					
	8	No Connect.					
8	9	LPF1 Gain: This is the output of LPF1 gain-adjusting amplifier. This output is used with LPF1 Input and external components.					
9	10	LPF1 In: This is the input to the LPF1 filter/gain block.					
10	11	LPF2 In: This is the input to the LPF2 filter/gain block.					
11	12	LPF2 Gain: This is the output of LPF2 gain-adjusting amplifier. This output is used with LPF2 Input and external components.					
	13	No Connect.					
12	14	BPF2 Out: This is the output of BPF2. It is under the control of the BPF2 Output Enable Input.					
13	15	BPF2 Output Enable: This controls the status of BPF2 Out. Logic 1 = Enable, Logic 0 = Muted. This pin has an internal $1M\Omega$ pullup resistor.					
14	16	BPF2 In: This is the input to the BPF2 gain/filter block.					
15	17	<b>BPF2 Gain</b> : This is the output of the BPF2 gain-adjusting amplifier. This output is used with BPF2 In and external components.					
16	18	<b>Bias</b> : This is the analog bias line at $V_{DD}/2$ . It should be coupled to $V_{SS}$ by a 1.0 $\mu F$ or greater capacitor.					
17	19	$\mathbf{V}_{ exttt{DD}}$ : Positive supply. A single +5 volt power supply is required. Levels and voltages within this device are dependent upon this supply.					
18	20	LPF2 Out: This is the output of LPF2.					
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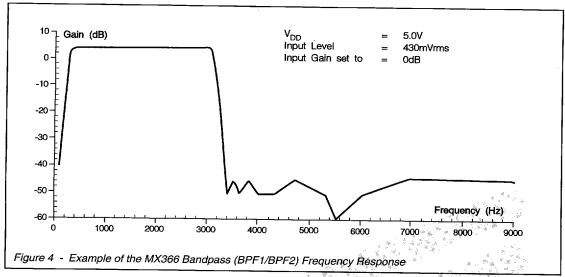
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#### **APPLICATION INFORMATION**

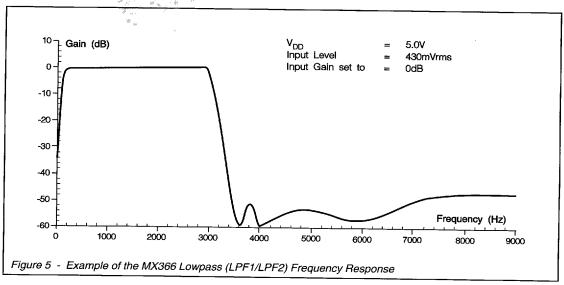
## **Bandpass Section Performance**



When using the MX366 Quad Filter Array within a cellular system, the following should be considered:

- (1) Each bandpass filter section has a frequency range of 300 Hz to 3000 Hz and a typical passband gain of 4.5 dB.
- (2) Each lowpass filter section has a cut-off frequency of 3100 Hz and a typical passband gain of 0.5 dB
- (3) BPF2 Output Enable has an enable/disable operating time as shown in "Specifications."

# **Lowpass Section Performance**



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# **Specifications**

### **Absolute Maximum Ratings**

Exceeding the maximum rating can result in device damage. Operation of the device outside the operating limits is not suggested.

Supply Voltage Input Voltage at any pin -0.3 to 7.0 V

(Ref. V<sub>ss</sub> = 0V) Output sink/source current

-0.3V to  $(V_{DD} + 0.3V)$ 

supply pins other pins Total Device Dissipation ±30mA ±20mA

@ 25°C Derating Operating Temperature Storage Ťemperature

800mW max. 10mW/°C -40°C to +85°C -40°C to +85°C

#### **Operating Limits**

All devices were measured under the following conditions unless otherwise noted.

$$V_{DD} = 5.0 \text{ V}$$

$$T_{AMB} = 25^{\circ}C$$

Clock = 4.433619 MHz

Audio Level 0dB Ref. = 775 mVrms @ 1 kHz

Characteristics	See Note	Min.	Тур.	Max.	Unit بريز
Static Values					*** v
Supply Voltage		4.5	5.0	5.5	•
Supply Current		-	5.0	₹ 8.5	mA MΩ
Input Impedance (Amplifiers)		1.0	10.0		kΩ
Input Impedance (Digital)		100	0.0	* 1.50 m	kΩ
Output Impedance (LP & BP Filters)		- 10 mg 1	2,0	** * * * * * * * * * * * * * * * * * *	** N22
On-Chip Xtal Oscillator		Fallo Ob and a			MΩ
B <sub>IN</sub>		10.0	10.0	-'84 - # + 'm #_'.	kΩ
R <sub>out</sub> Colm	42.3	' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' '	10.0	_	V/V
Inverter D.C. Voltage Gain	La Park Tab		10.0	_	MHz
Gain/Bandwidth Product			10.0		
Dynamic Values		25		_	V
Input Logic 1 Voltage	· "一根我们		-	1.5	V
Input Logic 0 Voltage					
Analog Signal Input Levels  Lowpass Filter	2 Tu 8 A W	-30.0	-	4.5	dB
Bandpass Filter	* 4 * 1	-30.0	_	-1.5	dB
Analog Signal Output Levels		00.0			
Lowpass Filter		-29.5	-	5.0	dB
Bandpass Filter	•	-26.0	-	2.5	dB
Analog Output Noise	2	-	-50.0	-	dBp
Bandpass Filter	1,3				
Passband Frequencies		300	-	3000	Hz
Passband Ripple		-	±1.0	-	dB
Low Frequency Roll-off (<200 Hz)		12	-	-	dB/oct.
High Frequency Attenuation at 3.4 kl	Ηz	-	48.0	-	dB
Passband Gain		3.5	4.5	5.5	dB
BPF2 Output Enable Time		-	8.0	-	μs
BPF2 Output Disable Time		-	20.0	-	μs
Lowpass Filter	1,3				
Cut-off Frequency (-3dB)		-	3100	-	Hz
Passband Ripple (300 to 3000 Hz)		-	±1.0	-	dB
Attenuation at 3.3 kHz		-	30.0	-	dB dB
Attenuation at 3.6 kHz		-	45.0	-	dB dB
Passband Gain		-	0.5	-	ав %
Distortion	1,4	-	2.0	-	/0
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NOTES:

- 1. Measured with Input Level -3.8 dB (500 mVrms).
- 2. Short circuit input, at any analog output and the measurement psophometrically weighted.
- 3. Op Amp gain 0 dB.
- 4. Measured in a 30 kHz bandwidth.

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