2SD2185

Silicon NPN epitaxial planer type

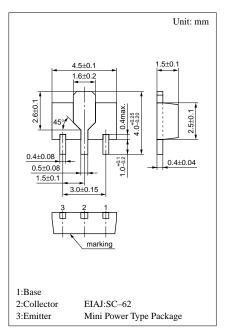
For low-frequency output amplification Complementary to 2SB1440

Features

- Low collector to emitter saturation voltage V_{CE(sat)}.
- Mini Power type package, allowing downsizing of the equipment and automatic insertion through the tape packing and the magazine packing.

Parameter	Symbol	Ratings	Unit		
Collector to base voltage	V _{CBO}	50	V		
Collector to emitter voltage	V _{CEO}	50	V		
Emitter to base voltage	V _{EBO}	5	V		
Peak collector current	I _{CP}	4	А		
Collector current	I _C	3	А		
Collector power dissipation	P_{C}^{*}	1	W		
Junction temperature	Tj	150	°C		
Storage temperature	T _{stg}	-55 ~ +150	°C		

Absolute Maximum Ratings (Ta=25°C)



Marking symbol : 1H

* Printed circuit board: Copper foil area of 1cm² or more, and the board thickness of 1.7mm for the collector portion

Electrical Characteristics (Ta=25°C)

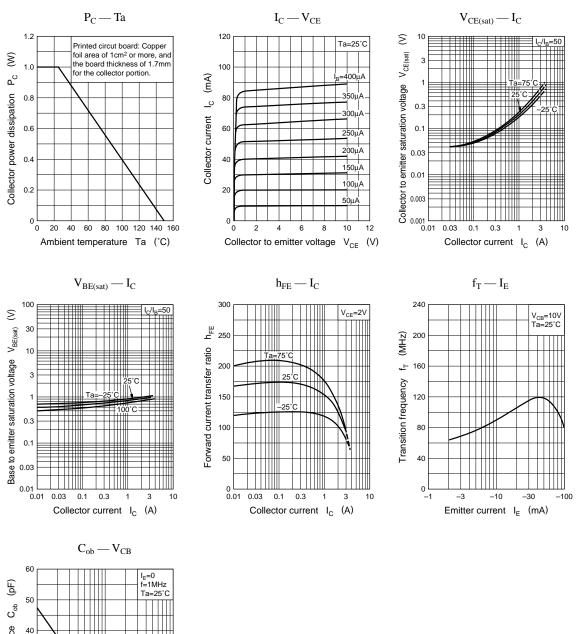
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	I _{CBO}	$V_{CB} = 20V, I_E = 0$			0.1	μA
Collector to base voltage	V _{CBO}	$I_{\rm C} = 10 \mu A, I_{\rm E} = 0$	50			v
Collector to emitter voltage	V _{CEO}	$I_{\rm C} = 1 {\rm mA}, I_{\rm B} = 0$	50			V
Emitter to base voltage	V _{EBO}	$I_{\rm E} = 10 \mu A, I_{\rm C} = 0$	5			v
Forward current transfer ratio	h _{FE1} *1	$V_{CE} = 2V, I_{C} = 200mA$	120		340	
	h _{FE2}	$V_{CE} = 2V, I_C = 1.0A$	80			
Collector to emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 1$ A, $I_{\rm B} = 50$ mA		0.15	0.3	v
Base to emitter saturation voltage	V _{BE(sat)}	$I_{\rm C} = 1$ A, $I_{\rm B} = 50$ mA		0.82	1.2	v
Transition frequency	f _T	$V_{CB} = 10V, I_E = -50mA, f = 200MHz$		110		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10V, I_E = 0, f = 1MHZ$		23	35	pF

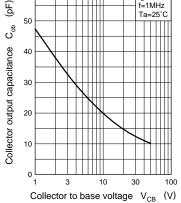
*2 Pulse measurement

*1hFE1 Rank classification

Rank	R	S		
h _{FE1}	120 ~ 240	170 ~ 340		
Marking Symbol	1HR	1HS		

Transistor





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