

One Chip AM/FM Radio with Audio Power Amplifier

Description

The integrated circuit TDA1083 includes, with exception of the FM front end, a complete AM-/FM-radio-circuit with audio power amplifier. An internal Z-diode

Features

- Large supply voltage range $V_S = 3$ to 12 V
- High AM-Sensitivity
- Limiting threshold voltage $V_i = 50 \mu V$
- Audio output power $P_0 = 0.7 \text{ W}$

stabilizes the supply voltage at $V_S \approx 13$ V, which allows with the aid of a resistor and a rectifier, the circuit to be driven by a higher external supply voltage.

- AFC-connection for VHF-tuner
- AM-FM switching without high frequency voltages

Applications

• AM-/FM- and audio-amplifier

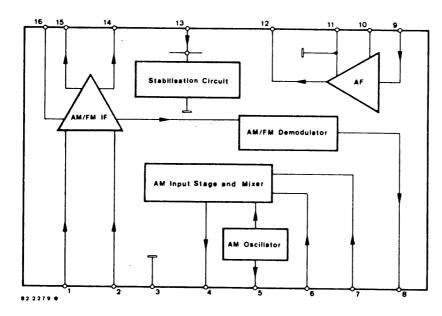


Figure 1. Block diagram and pin connections

- 1 IF-Decoupling
- 2 IF-Input
- 3 HF-Ground
- 4 AM-Mixer output
- 5 AM-Oscillator circuit
- 6 AM-Input
- 7 AM-Decoupling
- 8 Demodulator-Output
- 9 Audio-Input
- 10 Audio-Feedback
- 11 Audio-Ground
- 12 Audio-Output
- $13 + V_{\rm S}$
- 14 Demodulator circuit
- 15 Demodulator circuit
- 16 AGC/AFC-Voltage

Absolute Maximum Ratings

Reference points Pin 3 and 11, unless otherwise specified

Parameters	Symbol	Value	Unit
Supply voltage range Pin 13	V_s	3 to 12	V
Supply current when using the integrated stabilization			
circuit, $V_S = 12.5$ to 14.3 V Pin 13	I_{S}	50	mA
Power dissipation $T_{amb} = 65^{\circ}C$	P _{tot}	600	mW
Junction temperature	T_{j}	125	°C
Storage temperature range	T_{stg}	-25 to +125	°C

Thermal Resistance

Parameters	Symbol	Maximum	Unit
Junction ambient	R_{thJA}	100	K/W

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Electrical Characteristics

 $V_{s} = 9$ V, reference points Pin 3 and 11, $T_{amb} = 25$ °C, unless otherwise specified

Parameters	Test Conditions / Pin		Symbol	Min	Тур	Max	Unit
AF amplifier							
AF voltage amplification	f = 1 kHz		G_{v}		40		dB
Input impedance			Ri		150		kΩ
Output power	Figures 4 and 5		Po	300			mW
	$V_S = 5.5 \text{ V},$						
	$R_L = 8 \Omega, k = 10$						
AM-IF amplifier, $f_i = 1$ MH	$z, f_{IF} = 455 \text{ kHz, f}$	$m_{\text{mod}} = 1 \text{ kF}$	Iz, m = 0.3				
DC voltages at AM mode	$V_S = 3 V$	Pin 10	V ₁₀		1.2		V
without signal		Pin 12	V ₁₂	1.0		1.4	V
		Pin 13	V ₁₃	3.0	3.0	3.0	V
		Pin 16	V ₁₆	1.25		2.0	V
	$I_S = 42 \text{ mA}$	Pin 10	V.a		1.2		V
	$(V_S = 12.5 \text{ to} 14.3)$		V_{10}		1.2	7.2	V
	(\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	Pin 12	V ₁₂	5.9		7.2	•
		Pin 13	V_{13}^{12}	12.5	13.3	14.3	V
		Pin 16	V ₁₆	1.5	10.0	2.0	v
Regulation range	$V_{oAF}/V_{oAF} = -10$) dB	10				
	orn orn	Pin 6	ΔV_i		70		dB
AF voltage at demodulator output		Pin 8	V _{oAF}		100		mV
FM-IF amplifier, $f_{IF} = 10.7$	MHz, $\Delta f = \pm 22.5$	kHz, f _{moo}	l = 1 kHz				
DC voltages at FM mode	$V_S = 3 \text{ V}$	Pin 10	V ₁₀		1.2		V
without signal		Pin 12	V ₁₂	1.0		1.4	V
		Pin 13	V ₁₃	3.0	3.0	3.0	V
		Pin 16	V ₁₆	1.8		2.8	V
	1 42 4	D' - 10	X/		1.0		37
	$I_S = 42 \text{ mA}$ ($V_S = 12.5 \text{ to } 14.$	Pin 10	V_{10}		1.2	7.2	V V
	(VS = 12.3 to 14.	Pin 12	V ₁₂	5.9		1.2	V
		Pin 13	V ₁₂ V ₁₃	12.5	13.3	14.3	V
		Pin 16	V ₁₆	2.0	10.0	3.1	v
Limiting threshold (–3 dB)		Pin 2	V _i		50		μV
AF voltage at demodulator		Pin 8	V _{oAF}		100		mV
output			*				

Different dc voltages are developed at Pin 16 due to gain spread of AM-IF-amplifier. To determine the value of parallel resistance R_8 , at the output of the demodulator Pin 8 for $V_S=9$ V, AM mode without signal, dc voltage should be selected at Pin 16.

Table 1. Available in following voltage groups:

V ₁₆	1.4 to 1.7 V	1.7 to 1.9 V	1.9 to 2.1 V
R ₈	∞	47 kΩ	33 kΩ
Group	1	2	3

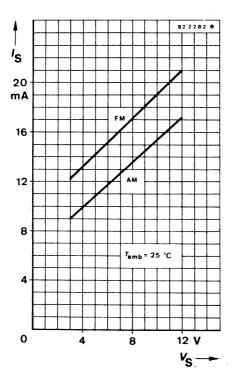


Figure 2.

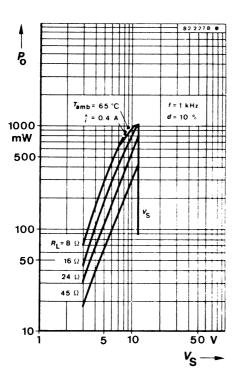


Figure 3.

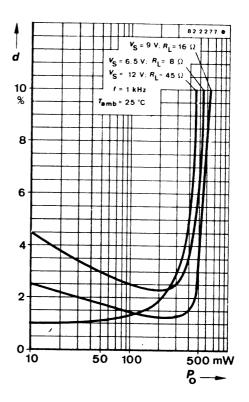


Figure 4.

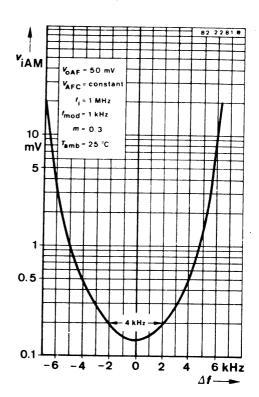


Figure 5.

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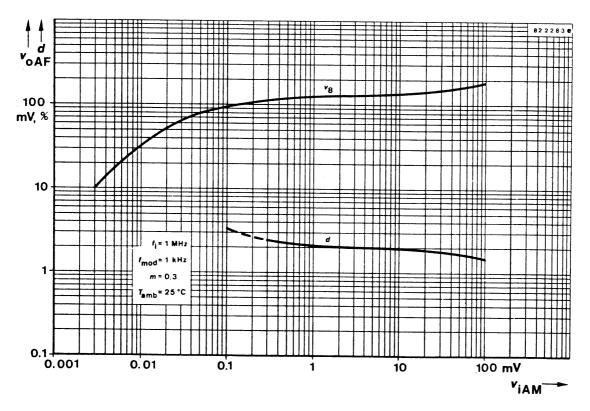


Figure 6.

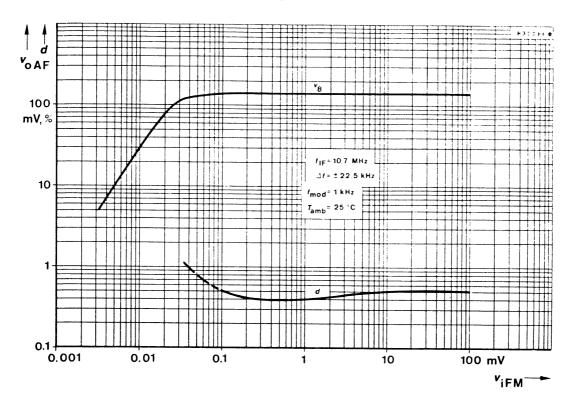


Figure 7.

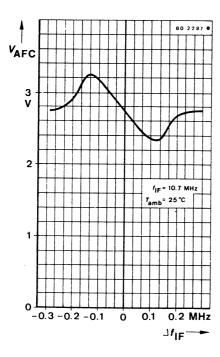


Figure 8.

Components in Figure 9

=	4	Wdg Ø 0.45 CuL, threaded core 7.5x3 material: Fi 01 U8 (Vogt GmbH)			
=	5	Wdg ∅ 0.45 CuL			
=	5	Wdg Ø 0.45 CuL, air core Ø 3.5 mm			
=	3+3	Wdg Ø 0.45 CuL, air core Ø 2.7 mm			
=	12	Wdg Ø 0.25 CuL, Pin 3−1, filter kit 154 AN(C) or 154ANS−7 A6363A0 (TOKO, Componex)			
=	2	Wdg Ø 0.25 CuL, Pin 4–6			
=	7	Wdg Ø 0.25 CuL, Pin 6–3, filter kit 154AN(C) or 154EES–7 A6392FA (TOKO, Componex)			
=	7	Wdg Ø 0.16 CuL, Pin 1–4, filter kit 154AN(C) or 154EES–7 A6391ABM (TOKO, Componex)			
=	5	5 Wdg Ø 0.16 CuL, Pin 2–6			
=	96	06 Wdg Ø 0.25 CuLs, ferrite aerial Ø 8x130 mm, type 031039–2103–606, (Draloric)			
=	6	Wdg Ø 0.25 CuLs			
=	78	Wdg Ø 0.09 CuL, Pin 3–4, filter kit RBR or RWOS–6A7609AAU (TOKO, Componex)			
=	7	Wdg Ø 0.09 CuL, Pin 2−1			
=	18	Wdg Ø 0.09 CuL, Pin 3–4, filter kit RHN(C) or RHCS–1A7607AQH (TOKO, Componex)			
=	46+100	46+100 Wdg Ø 0.09 CuL, Pin 6−2−1			
=	72+72	Wdg Ø 0.09 Cul Pin 3–4/6–1, filter kit RHN(C) or RHNS–1A7608AZP (TOKO, Componex)			
=	Ceramic filter LBF 6 (Componex) or CFU 445 H (Stettner)				
=	Ceramic filter 10.7 MF–18 (Componex) or SFE 10.7 MA (Stettner)				
=	Ferrit bead on the transistor terminal				
=	16	Wdg Ø 0.25 CuL, Ø 2 air core			
=	6	Wdg Ø 0.15 CuL, Ø 2.1x3 mm ferrit bead			
=	4.5 to 20 pF, variable capacitor type CY2–22124–RT02 (TOKO, Componex)				
=	5 to 80 pF				
=	5 to 140 pF				
=	According to gain groups ∞ , 47 k Ω or 33 k Ω				
		= 5 = 5 = 3+3 = 12 = 2 = 7 = 7 = 5 = 96 = 6 = 78 = 7 = 18 = 46+100 = 72+72 = Ceramic fi = Ferrit bead = 16 = 6 = 4.5 to 20 pr = 5 to 140 pr			

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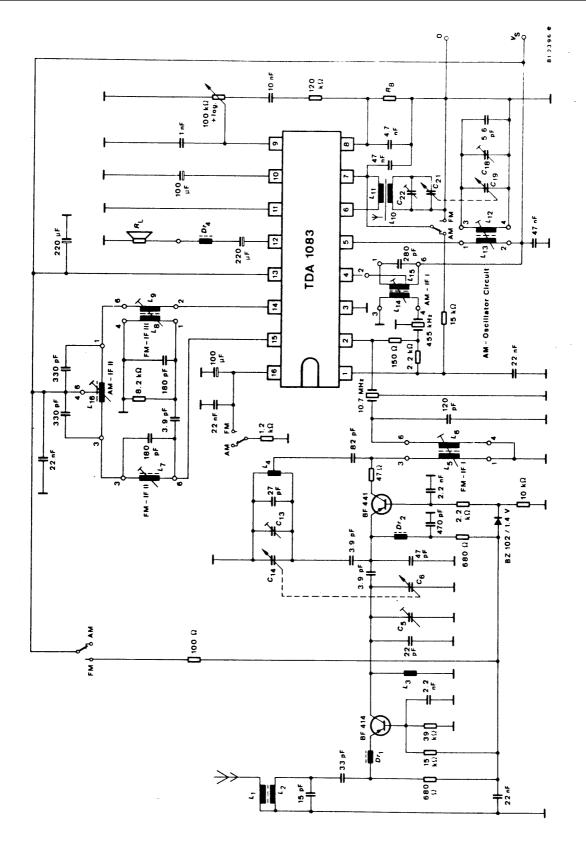


Figure 9. FM-/AM-receiver circuit

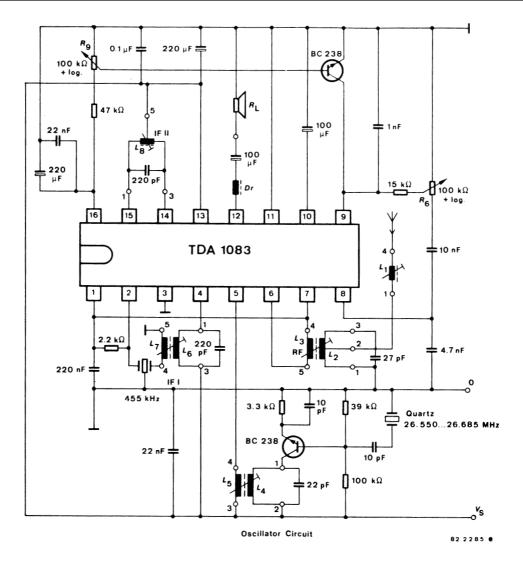
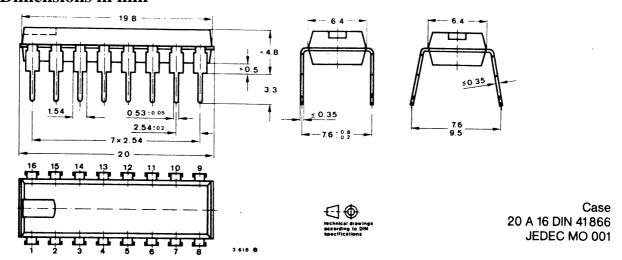


Figure 10.

Dimensions in mm



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Ozone Depleting Substances Policy Statement

It is the policy of **TEMIC Semiconductor GmbH** to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

TEMIC Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

TEMIC Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use TEMIC Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify TEMIC Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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