

# **FAN7000D**

# Low Power Amplifier

#### **Features**

- Low quiescent current
- High power supply ripple rejection
- Low voltage operation
- A few of external part required
- Built in power save switch & mute switch

### **Typical Applications**

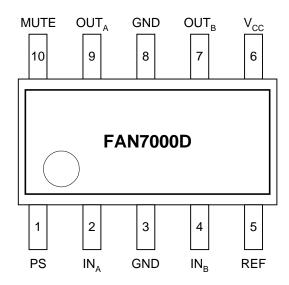
- Portable compact disk player (DISCMAN)
- Portable mini disk player (MD)
- Disc-man
- MP3 player
- CD-ROM
- Other potable compact disk media Fan motor drive

### **Description**

The FAN7000D is a monolithic integrated circuit and suitable dual amplifier for low power.



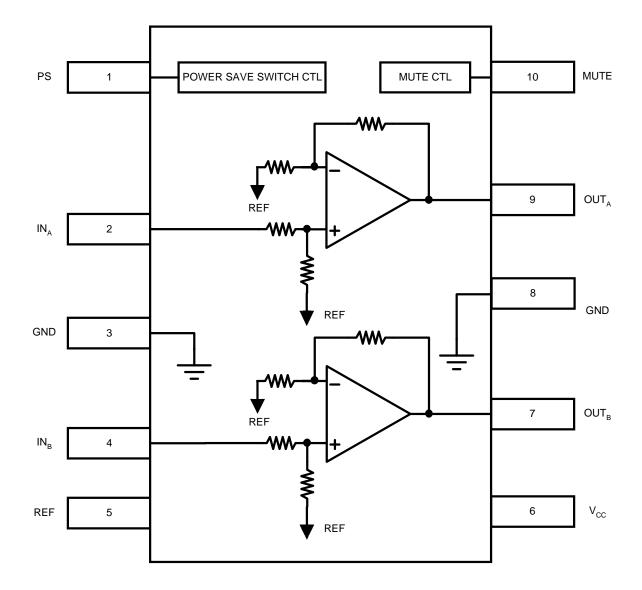
## **Pin Assignments**



### **Pin Definitions**

| Pin Number | Pin Name | Pin Function Description |
|------------|----------|--------------------------|
| 1          | PS       | Power Save Switch        |
| 2          | INA      | Signal Input A           |
| 3          | GND      | Signal Ground            |
| 4          | INB      | Signal Input B           |
| 5          | REF      | Reference Voltage        |
| 6          | Vcc      | Supply Voltage           |
| 7          | OUTB     | Signal Output B          |
| 8          | GND      | Power Ground             |
| 9          | OUTA     | Signal Output A          |
| 10         | MUTE     | Mute On Switch           |

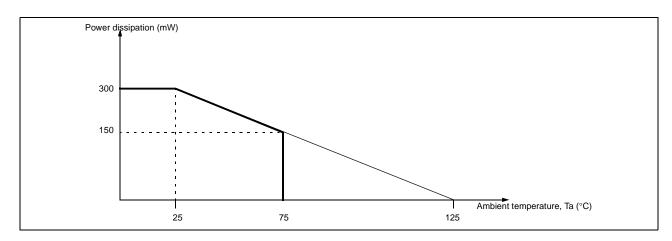
## **Internal Block Diagram**



## Absolute Maximum Ratings (Ta = 25°C)

| Parameter              | Symbol | Value              | Unit | Remark                 |
|------------------------|--------|--------------------|------|------------------------|
| Maximum Supply Voltage | Vcc    | 4.5                | V    | Maximum supply voltage |
| Power Dissipation      | PD     | 300                | mW   | Power dissipation      |
| Operating Temperature  | TOPR   | −20 <b>~ +7</b> 5  | °C   | Operating temperature  |
| Storage Temperature    | TSTG   | −55 ~ <b>+</b> 125 | °C   | Storage temperature    |
| Thermal Resistance     | Tja    | 150                | °C/W | _                      |

## **Power Dissipation Curve**



## Recommended Operating Conditions (Ta = 25°C)

| Parameter                | Parameter Symbol |     | Min. Typ. |     | Unit |  |
|--------------------------|------------------|-----|-----------|-----|------|--|
| Operating Supply Voltage | Vcc              | 1.8 | 3.0       | 4.0 | V    |  |
| Recommended Load RL      |                  | 16  | _         | 32  | Ω    |  |

## **Electrical Characteristics** (RL = $16\Omega$ , Rg = $600\Omega$ , Ta = $25^{\circ}$ C)

| Parameter                 | Symbol            | Conditions  | Min. | Тур. | Max. | Unit  |
|---------------------------|-------------------|---|------|------|------|-------|
| Quiescent Current 1       | ICC1              | Vcc = 2.4V  | -    | 5.5  | 10.0 | mA    |
| Quiescent Current 2       | ICC2              | VCC = 4.5V, Mute = GND  | -    | 1.0  | 2.0  | mA    |
| Quiescent Current 3       | ICC3              | V <sub>CC</sub> = 4.5V, PS = GND                                      | -    | -    | 1.0  | μΑ    |
| Close Loop Voltage Gain 1 | G <sub>VC1</sub>  | V <sub>CC</sub> = 2.4V, f = 1KHz,<br>V <sub>O</sub> = -10dBm          | 30   | 32   | 34   | dB    |
| Close Loop Voltage Gain 2 | G <sub>VC1</sub>  | V <sub>CC</sub> = 1.8V, f = 1KHz,<br>V <sub>O</sub> = -20dBm          | 29   | 32   | 34   | dB    |
| Channel Balance 1         | $\Delta G_{V1}$   | VCC = 2.4V, f = 1KHz,<br>VO = -10dBm                                  | -    | -    | 1.0  | dB    |
| Channel Balance 2         | $\Delta G_{V2}$   | VCC = 1.8V, f = 1KHz,<br>VO = -20dBm                                  | -    | 1    | 1.0  | dB    |
| Total Harmonic Distortion | THD               | VCC = 2.0V, f = 1KHz,<br>PO = 1mW                                     | -    | 0.5  | 1.5  | %     |
| Ripple Rejection Ratio    | RR                | VCC = 1.8V, $f = 100Hz$ ,<br>Rg = 1KΩ, $VR = -20dBm$ ,<br>BPF = 100Hz | 43   | 60   | -    | dB    |
| Crosstalk                 | СТ                | $V_{CC} = 2.4V, f = 100Hz,$<br>$Rg = 1K\Omega, V_{O} = -10dB$         | 43   | 50   | -    | dB    |
| Output Noise Voltage      | VNOISE            | $V_{CC}$ = 4.5V, Rg = 1KΩ,<br>BPF = 20Hz ~ 20KHz                      | -    | 60   | 100  | μVrms |
| Output Power              | Роит              | VCC = 3.0V, f = 1KHz,<br>THD = 10%                                    | 20   | 40   | -    | mW    |
| PS Attenuation Ratio      | ATTPS             | VCC = 1.8V, f = 100Hz,<br>PS = GND, V <sub>IN</sub> = -10dB           | -    | -    | -80  | dB    |
| MUTE attenuation ratio    | ATT <sub>MU</sub> | VCC = 1.8V, f = 100Hz,<br>MUTE = GND, V <sub>IN</sub> = -10dB         | -    | -    | -80  | dB    |
| PS ON input current       | IPSON             | VCC = 1.5V, VREF ≥ 0.85V  | -    | 0.2  | 1.0  | μΑ    |
| MUTE OFF input current    | IMOFF             | VCC = 1.5V, VREF ≥ 0.85V  | -    | 0.2  | 1.0  | μΑ    |
| PS ON high level          | VHPS              | VCC = 1.5V, VREF ≥ 0.85V  | 0.5  | 0.65 | -    | V     |
| MUTE OFF high level       | Vнмu              | VCC = 1.5V, VREF ≥ 0.85V  | 0.5  | 0.65 | -    | V     |

### **Application Information**

#### 1. PS Block

This block diagram describes the power save switch circuits.

The drive block is controlled by PS pin, which can be derived from micro controller.

It controls bias of the internal circuits of FAN7000D, so that it makes FAN7000D operate when input voltage level reaches high level.

#### 2. Mute Block

The block diagram describes the mute on switch circuits.

The drive block is controlled by MUTE pin, which can be derived from micro controller.

When the pin of mute turns on, it makes reference voltage of internal circuits approximately 0V, so that it keeps the device of FAN7000D off.

#### 3. AMP Block

This block diagram describes the AMP block with resistances, which control gain of FAN7000D.

The gain of FAN7000D is 
$$\frac{V_{OUT}}{V_{IN}} \cong 40 \cong 32[dB]$$

Output voltage of FAN7000D can be 40 times as much as input voltage, so it eliminates the number of external circuits and offers headphone input.

#### 4. Popping Noise Reduction

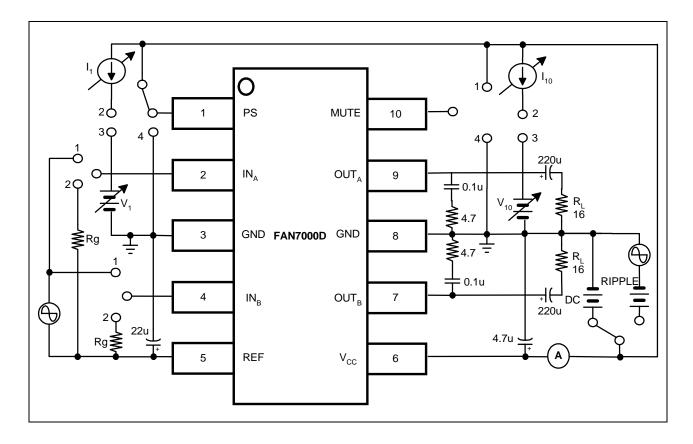
If PS pin (Pin1, Power save switch) connect the micro controller, the micro controller must follow the same sequence 1 in order to reduce popping noise on mute mode.

PS on  $\rightarrow$  Mute on  $\rightarrow$  PS off  $\rightarrow$  Mute operation  $\rightarrow$  PS on  $\rightarrow$  Mute off  $\rightarrow$  Normal operation (Sequence 1)

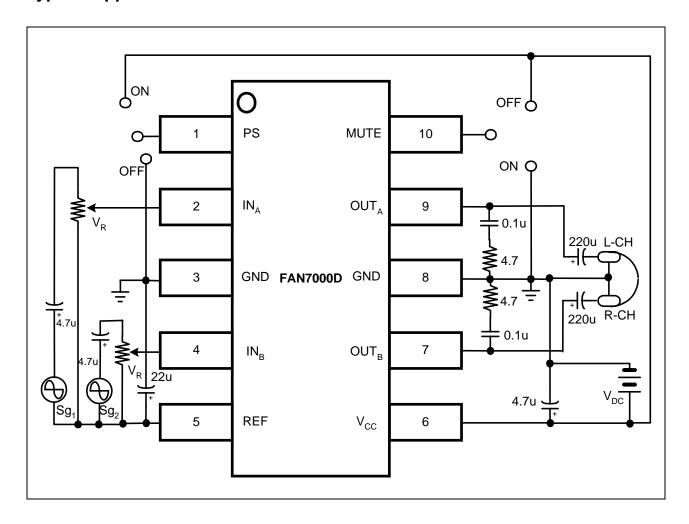
If PS connect VCC, the micro controller follow the sequence 2.

Mute on  $\rightarrow$  Mute Operation  $\rightarrow$  Mute off  $\rightarrow$  Normal operation (Sequence 2)

### **Test Circuits**

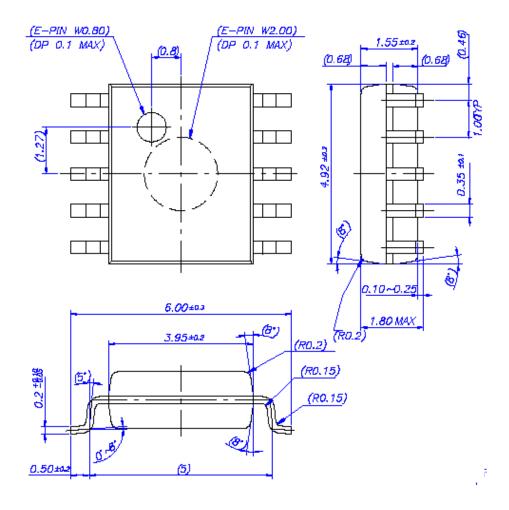


## **Typical Application Circuits**



## **Package Dimensions**

## 10-SSOP-225



### **Ordering Information**

| Device     | Package     | Operating Temp. |
|------------|-------------|-----------------|
| FAN7000D   | 10-SSOP-225 | −20°C ~ +75°C   |
| FAN7000DTF | 10-SSOP-225 | −20°C ~ +75°C   |

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