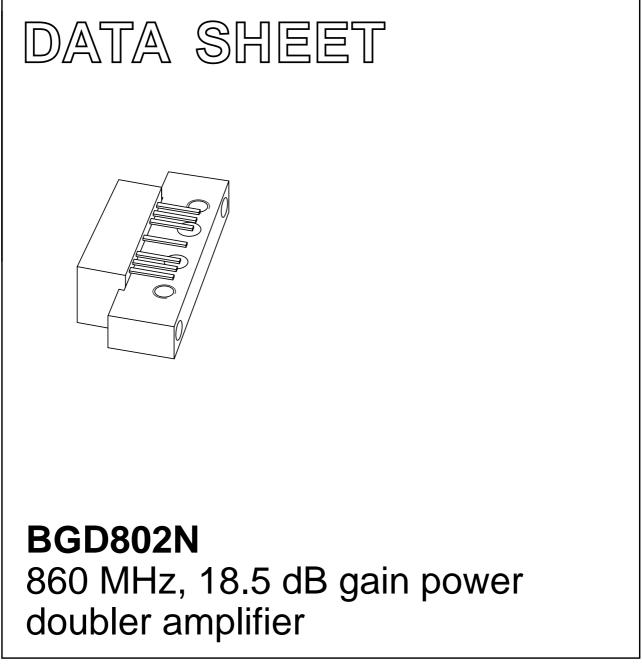
## DISCRETE SEMICONDUCTORS



Product specification Supersedes data of 1999 Mar 22 2001 Nov 2



### BGD802N

### FEATURES

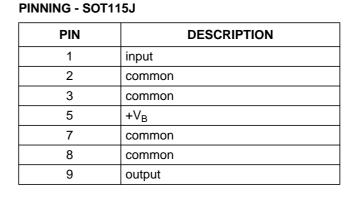
- Extremely flat gain response
- Excellent linearity
- Extremely low noise
- Excellent return loss properties
- Silicon nitride passivation
- Rugged construction
- · Gold metallization ensures excellent reliability.

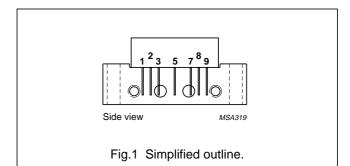
### APPLICATIONS

• CATV systems operating in the 40 to 860 MHz frequency range.

### DESCRIPTION

Hybrid amplifier module in a SOT115J package operating at a supply voltage of 24 V (DC).





### QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
G <sub>p</sub>	power gain	f = 50 MHz	18	19	dB
		f = 860 MHz	18.5	-	dB
I <sub>tot</sub>	total current consumption (DC)	V <sub>B</sub> = 24 V	_	410	mA

#### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
V <sub>B</sub>	supply voltage –		25	V
Vi	RF input voltage	-	65	dBmV
T <sub>stg</sub>	storage temperature	-40	+100	°C
T <sub>mb</sub>	operating mounting base temperature	-20	+100	°C

### BGD802N

#### CHARACTERISTICS

Table 1 Bandwidth 40 to 860 MHz;  $V_B = 24$  V;  $T_{case} = 35$  °C;  $Z_S = Z_L = 75 \Omega$ 

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
G <sub>p</sub>	power gain	f = 50 MHz	18	18.5	19	dB
		f = 860 MHz	18.5	19.5	_	dB
SL	slope cable equivalent	f = 40 to 860 MHz	0.2	0.9	2	dB
FL	flatness of frequency response	f = 40 to 860 MHz	_	±0.1	±0.25	dB
s <sub>11</sub>	input return losses	f = 40 to 80 MHz	20	32	_	dB
		f = 80 to 160 MHz	18.5	27	-	dB
		f = 160 to 320 MHz	17	24	_	dB
		f = 320 to 640 MHz	15.5	22	_	dB
		f = 640 to 860 MHz	14	20.5	-	dB
S <sub>22</sub>	output return losses	f = 40 to 80 MHz	20	33	_	dB
		f = 80 to 160 MHz	18.5	29	_	dB
		f = 160 to 860 MHz	17	22	_	dB
s <sub>21</sub>	phase response	f = 50 MHz	-45	-	+45	deg
СТВ	composite triple beat	49 channels flat; V <sub>o</sub> = 47 dBmV; measured at 859.25 MHz	-	-65	-63	dB
X <sub>mod</sub>	cross modulation	49 channels flat; $V_o = 47 \text{ dBmV}$ ; measured at 55.25 MHz	-	-64	-62	dB
CSO	composite second order distortion	49 channels flat; $V_o = 47 \text{ dBmV}$ ; measured at 860.5 MHz	-	-68	-60	dB
d <sub>2</sub>	second order distortion	note 1	_	-75	-69	dB
Vo	output voltage	d <sub>im</sub> = -60 dB; note 2	61.5	63.5	_	dBmV
F	noise figure	f = 50 MHz	-	4.5	5.5	dB
		f = 550 MHz	_	-	6	dB
		f = 650 MHz	_	-	7	dB
		f = 750 MHz	-	-	7.5	dB
		f = 860 MHz	_	6.5	9	dB
I <sub>tot</sub>	total current consumption (DC)	note 3	-	395	410	mA
				•		•

- 1.  $f_p = 55.25 \text{ MHz}; V_p = 44 \text{ dBmV};$  $f_q = 805.25 \text{ MHz}; V_q = 44 \text{ dBmV};$ measured at  $f_p + f_q = 860.5 \text{ MHz}.$
- 2. Measured according to DIN45004B:  $f_p = 851.25 \text{ MHz}; V_p = V_0;$   $f_q = 858.25 \text{ MHz}; V_q = V_o -6 \text{ dB};$   $f_r = 860.25 \text{ MHz}; V_r = V_o -6 \text{ dB};$ measured at  $f_p + f_q - f_r = 849.25 \text{ MHz}.$
- 3. The module normally operates at  $V_B = 24$  V, but is able to withstand supply transients up to 30 V.

### BGD802N

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
G <sub>p</sub>	power gain	f = 50 MHz	18	18.5	19	dB
		f = 860 MHz	18.5	19.5	-	dB
SL	slope cable equivalent	f = 40 to 860 MHz	0.2	0.9	2	dB
FL	flatness of frequency response	f = 40 to 860 MHz	-	±0.1	±0.25	dB
S <sub>11</sub>	input return losses	f = 40 to 80 MHz	20	32	—	dB
		f = 80 to 160 MHz	18.5	27	_	dB
		f = 160 to 320 MHz	17	24	-	dB
		f = 320 to 640 MHz	15.5	22	—	dB
		f = 640 to 860 MHz	14	20.5	-	dB
\$ <sub>22</sub>	output return losses	f = 40 to 80 MHz	20	33	-	dB
		f = 80 to 160 MHz	18.5	29	—	dB
		f = 160 to 860 MHz	17	22	-	dB
s <sub>21</sub>	phase response	f = 50 MHz	-45	-	+45	deg
СТВ	composite triple beat	129 channels flat; V <sub>o</sub> = 44 dBmV; measured at 859.25 MHz	-	-	-54	dB
X <sub>mod</sub>	cross modulation	129 channels flat; $V_o = 44 \text{ dBmV}$ ; measured at 55.25 MHz	-	-	-59	dB
CSO	composite second order distortion	129 channels flat; V <sub>o</sub> = 44 dBmV; measured at 860.5 MHz	-	-	-56	dB
d <sub>2</sub>	second order distortion	note 1	-	-75	-69	dB
Vo	output voltage	d <sub>im</sub> = -60 dB; note 2	61.5	63.5	-	dBmV
F	noise figure	see Table 1	-	-	-	dB
I <sub>tot</sub>	total current consumption (DC)	note 3	-	395	410	mA

### Table 2 Bandwidth 40 to 860 MHz; V\_B = 24 V; T\_{case} = 30 °C; Z\_S = Z\_L = 75 $\Omega$

- 1.  $f_p = 55.25 \text{ MHz}; V_p = 44 \text{ dBmV}; f_q = 805.25 \text{ MHz}; V_q = 44 \text{ dBmV}; measured at f_p + f_q = 860.5 \text{ MHz}.$
- 2. Measured according to DIN45004B:  $f_p = 851.25 \text{ MHz}; V_p = V_0;$   $f_q = 858.25 \text{ MHz}; V_q = V_0 - 6 \text{ dB};$   $f_r = 860.25 \text{ MHz}; V_r = V_0 - 6 \text{ dB};$ measured at  $f_p + f_q - f_r = 849.25 \text{ MHz}.$
- 3. The module normally operates at  $V_B = 24$  V, but is able to withstand supply transients up to 30 V.

### BGD802N

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
G <sub>p</sub>	power gain	f = 50 MHz	18	18.5	19	dB
		f = 750 MHz	18.5	-	-	dB
SL	slope cable equivalent	f = 40 to 750 MHz	0.2	-	2	dB
FL	flatness of frequency response	f = 40 to 750 MHz	-	-	±0.25	dB
S <sub>11</sub>	input return losses	f = 40 to 80 MHz	20	32	-	dB
		f = 80 to 160 MHz	18.5	27	-	dB
		f = 160 to 320 MHz	17	24	-	dB
		f = 320 to 640 MHz	15.5	22	-	dB
		f = 640 to 750 MHz	14	20.5	-	dB
\$ <sub>22</sub>	output return losses	f = 40 to 80 MHz	20	33	-	dB
		f = 80 to 160 MHz	18.5	29	-	dB
		f = 160 to 750 MHz	17	22	-	dB
s <sub>21</sub>	phase response	f = 50 MHz	-45	-	+45	deg
СТВ	composite triple beat	110 channels flat; V <sub>o</sub> = 44 dBmV; measured at 745.25 MHz	_	-	-59	dB
X <sub>mod</sub>	cross modulation	110 channels flat; $V_o = 44 \text{ dBmV}$ ; measured at 55.25 MHz	-	-	-60	dB
CSO	composite second order distortion	110 channels flat; V <sub>o</sub> = 44 dBmV; measured at 746.5 MHz	_	-	-60	dB
d <sub>2</sub>	second order distortion	note 1	-	-	-72	dB
Vo	output voltage	d <sub>im</sub> = -60 dB; note 2	64	-	-	dBmV
F	noise figure	see Table 1	-	-	-	dB
I <sub>tot</sub>	total current consumption (DC)	note 3	_	395	410	mA

### Table 3 Bandwidth 40 to 750 MHz; V\_B = 24 V; T\_{case} = 30 °C; Z\_S = Z\_L = 75 $\Omega$

- 1.  $f_p = 55.25 \text{ MHz}; V_p = 44 \text{ dBmV};$  $f_q = 691.25 \text{ MHz}; V_q = 44 \text{ dBmV};$ measured at  $f_p + f_q = 746.5 \text{ MHz}.$
- 2. Measured according to DIN45004B:  $f_p = 740.25 \text{ MHz}; V_p = V_0;$   $f_q = 747.25 \text{ MHz}; V_q = V_0 - 6 \text{ dB};$   $f_r = 749.25 \text{ MHz}; V_r = V_0 - 6 \text{ dB};$ measured at  $f_p + f_q - f_r = 738.25 \text{ MHz}.$
- 3. The module normally operates at  $V_B = 24$  V, but is able to withstand supply transients up to 30 V.

### BGD802N

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	
G <sub>p</sub>	power gain	f = 50 MHz	18	18.5	19	dB
		f = 650 MHz	18.5	_	-	dB
SL	slope cable equivalent	f = 40 to 650 MHz	0.2	-	2	dB
FL	flatness of frequency response	f = 40 to 650 MHz	-	-	±0.2	dB
s <sub>11</sub>	input return losses	f = 40 to 80 MHz	20	32	-	dB
		f = 80 to 160 MHz	18.5	27	-	dB
		f = 160 to 320 MHz	17	24	-	dB
		f = 320 to 650 MHz	15	22	-	dB
\$ <sub>22</sub>	output return losses	f = 40 to 80 MHz	20	33	-	dB
		f = 80 to 160 MHz	18.5	29	-	dB
		f = 160 to 650 MHz	17	22	-	dB
s <sub>21</sub>	phase response	f = 50 MHz	-45	-	+45	deg
СТВ	composite triple beat	94 channels flat; V <sub>o</sub> = 44 dBmV; measured at 649.25 MHz	-	-	-61	dB
X <sub>mod</sub>	cross modulation	94 channels flat; V <sub>o</sub> = 44 dBmV; measured at 55.25 MHz	-	-	-61	dB
CSO	composite second order distortion	94 channels flat; V <sub>o</sub> = 44 dBmV; measured at 650.5 MHz	-	-	-62	dB
d <sub>2</sub>	second order distortion	note 1	_	-	-72	dB
Vo	output voltage	d <sub>im</sub> = -60 dB; note 2	65	-	-	dBmV
F	noise figure	see Table 1	_	_	_	dB
I <sub>tot</sub>	total current consumption (DC)	note 3	_	395	410	mA

#### Table 4 Bandwidth 40 to 650 MHz; $V_B$ = 24 V; $T_{case}$ = 30 °C; $Z_S$ = $Z_L$ = 75 $\Omega$

- 1.  $f_p = 55.25 \text{ MHz}; V_p = 44 \text{ dBmV};$  $f_q = 595.25 \text{ MHz}; V_q = 44 \text{ dBmV};$ measured at  $f_p + f_q = 650.5 \text{ MHz}.$
- 2. Measured according to DIN45004B:
  - $\begin{array}{l} f_{p}=640.25 \text{ MHz}; \ V_{p}=V_{o}; \\ f_{q}=647.25 \text{ MHz}; \ V_{q}=V_{o}-6 \text{ dB}; \\ f_{r}=649.25 \text{ MHz}; \ V_{r}=V_{o}-6 \text{ dB}; \\ \text{measured at } f_{p}+f_{q}-f_{r}=638.25 \text{ MHz}. \end{array}$
- 3. The module normally operates at  $V_B$  = 24 V, but is able to withstand supply transients up to 30 V.

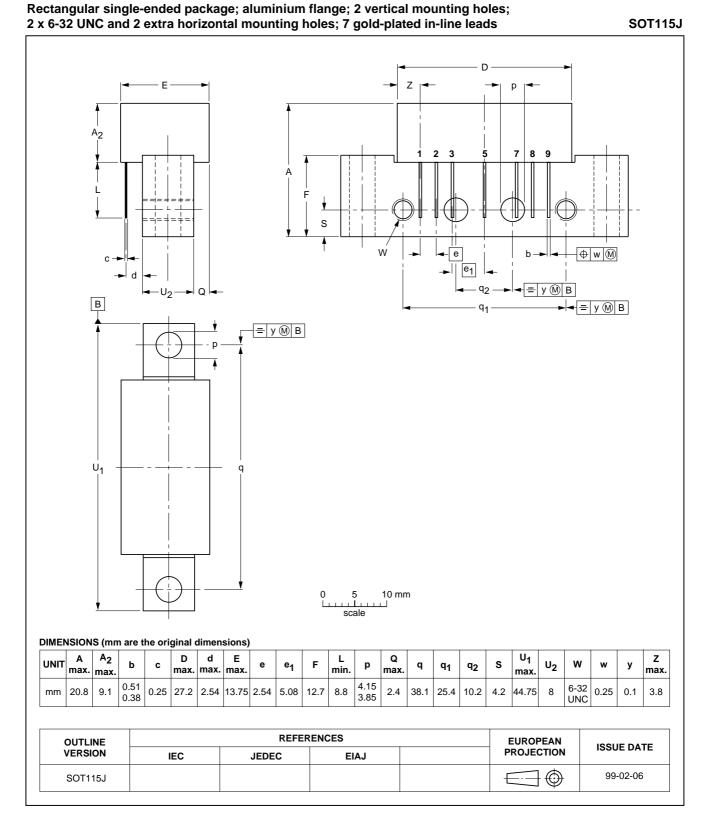
### BGD802N

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
G <sub>p</sub>	power gain	f = 50 MHz	18	18.5	19	dB
		f = 550 MHz	18.5	_	_	dB
SL	slope cable equivalent	f = 40 to 550 MHz	0.2	-	2	dB
FL	flatness of frequency response	f = 40 to 550 MHz	-	-	±0.2	dB
s <sub>11</sub>	input return losses	f = 40 to 80 MHz	20	32	_	dB
		f = 80 to 160 MHz	18.5	27	_	dB
		f = 160 to 320 MHz	17	24	_	dB
		f = 320 to 550 MHz	16	22	_	dB
\$ <sub>22</sub>	output return losses	f = 40 to 80 MHz	20	33	_	dB
		f = 80 to 160 MHz	18.5	29	_	dB
		f = 160 to 550 MHz	17	22	_	dB
s <sub>21</sub>	phase response	f = 50 MHz	-45	-	+45	deg
СТВ	composite triple beat	77 channels flat; $V_0 = 44 \text{ dBmV}$ ; measured at 547.25 MHz	-	-	-65	dB
X <sub>mod</sub>	cross modulation	77 channels flat; $V_o = 44 \text{ dBmV}$ ; measured at 55.25 MHz	-	-	-63	dB
CSO	composite second order distortion	77 channels flat; $V_o = 44 \text{ dBmV}$ ; measured at 548.5 MHz	-	-	-65	dB
d <sub>2</sub>	second order distortion	note 1	_	-	-74	dB
Vo	output voltage	d <sub>im</sub> = -60 dB; note 2	66	-	-	dBmV
F	noise figure	see Table 1	_	_	_	dB
I <sub>tot</sub>	total current consumption (DC)	note 3	_	395	410	mA

### Table 5 Bandwidth 40 to 550 MHz; $V_B$ = 24 V; $T_{case}$ = 30 °C; $Z_S$ = $Z_L$ = 75 $\Omega$

- 1.  $f_p = 55.25 \text{ MHz}; V_p = 44 \text{ dBmV};$  $f_q = 493.25 \text{ MHz}; V_q = 44 \text{ dBmV};$ measured at  $f_p + f_q = 548.5 \text{ MHz}.$
- 2. Measured according to DIN45004B:
  - $\begin{array}{l} f_{p}=540.25 \text{ MHz}; \ V_{p}=V_{o}; \\ f_{q}=547.25 \text{ MHz}; \ V_{q}=V_{o}-6 \text{ dB}; \\ f_{r}=549.25 \text{ MHz}; \ V_{r}=V_{o}-6 \text{ dB}; \\ \text{measured at } f_{p}+f_{q}-f_{r}=538.25 \text{ MHz}. \end{array}$
- 3. The module normally operates at  $V_B$  = 24 V, but is able to withstand supply transients up to 30 V.

### PACKAGE OUTLINE



**BGD802N** 

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#### DATA SHEET STATUS

DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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