6-Input 1-Output Video Switch

Monolithic IC MM1140

Outline

This is a 6-input, 1-output high performance video switch for TV/BS signal switching. It is ideal for use when multiple input circuits are needed on 1 chip.

Features

1. Built-in mute function (mute pin: input possible)

2. Crosstalk –70dB (at 4.43MHz)

3. Power supply voltage4. Frequency response5~13V10MHz

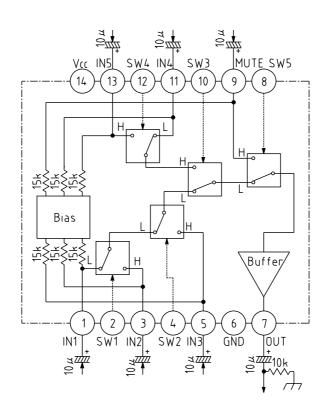
Package

SOP-14B (MM1140XF)

Applications

- 1. TV
- 2. VCR
- 3. Other video equipment

Block Diagram



Pin Description

Pin no.	Pin name	Internal equivalent circuit diagram	Pin no.	Pin name	Internal equivalent circuit diagram
1	IN1	VCC 220 \$ 15k	8	SW5	10k
		9.1k \$	9	MUTE	VCC 220 \$ 15k
2	SW1	10k			9.1k \$
3	IN2	VCC 220 \$ 15k	10	SW3	10k
		9.1k \$	11	IN4	VCC 220 \$ 15k
4	SW2	10k			9.1k \$
5	IN3	VCC 220 \$ 15k	12	SW4	10k
		9.1k \$	13	IN5	VCC 220 \$ 15k
6	GND				9.1k }
7	OUT	VCC 1.5k 50			
		1.3k 100	14	Vcc	

Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Ratings	Units	
Storage temperature	Tstg	-40~+125	°C	
Operating temperature	Topr	-20~+75	°C	
Power supply voltage	Vcc	15	V	
Allowable loss	Pd	350	mW	

Electrical Characteristics (Except where noted otherwise, Ta=25°C, Vcc=5.0V)

Item		Symbol	Measurement conditions	Min.	Тур.	Max.	Units
Operating power supply voltage	e range	Vcc		4.75	5.0	13.0	V
Consumption current		Id	Refer to Measuring Circuit		9.0	13.0	mA
Voltage gain		Gv	Refer to Measuring Circuit	-0.5	0	+0.5	dB
Frequency characteristic	;	Fc	Refer to Measuring Circuit	0	+1	dB	
Differential gain		DG	Refer to Measuring Circuit		0	±3	%
Differential phase		DP	Refer to Measuring Circuit		0	±3	deg
Crosstalk		Ст	Refer to Measuring Circuit		-70	-60	dB
Total harmonic distortion	1	THD	Refer to Measuring Circuit		0.01	0.3	%
Output offset voltage		Voff	Refer to Measuring Circuit			±30	mV
Switch input voltage L		V _{IH}	Refer to Measuring Circuit	2.1			V
		VIL	Refer to Measuring Circuit			0.7	V
Input impedance		Ri			15		kΩ
Output impedance		Ro			25		Ω

Measuring Procedures (Except where noted otherwise, Vcc=5.0V, VC1=Vcc, VC2=0V)

Item		Symbol	Switch state	Measuring Procedure				
Consumption cu	rrent	Id 1		Connect a DC ammeter to the Vcc pin and measure. Vcc is 5V and the ammeter is shorted for use in subsequent measurements.				
Voltage gain		Gv	2	Input a 2.0V _{P-P} , 100kHz sine wave to SG, and obtain Gv from the following formula given TP12 voltage as V1 and TP14 voltage as V2. Gv=20LOG (V2/V1) dB				
Frequency characteristic		Fc	2	For the above Gv measurement, given TP14 voltage for 10MHz as V3, Fc is obtained from the following formula. Fc=20LOG (V3/V2) dB				
Differential ga	in	DG	2	Input a 2.0V _{P-P} staircase wave to SG, and measure differential gain at TP14 APL=10~90%				
Differential pha	ıse	DP	2	Proceed as for DG, and measure differential phase.				
Total harmon	ic	THD	2	Input a 2.5V _{P-P} , 1kHz sine wave to SG, connect a distortion meter to TP14 and measure.				
Output offset vol	tage	Voff	3	Measure the DC voltage difference of each switch status at TP13.				
Crosstalk		Ст	9	Assume VC1=2.1V, VC2=0.7V. Input a 2.0V _{P-P} , 4.43MHz sine wave to SG, and given TP12 voltage as V4 and TP14 voltage as V5, C _T is obtained from the following formula. C _T =20LOG (V5/V4) dB				
Switch 1	Н	Vih1	4	Impress different optional DC voltages on TP6 and TP7. Gradually raise from VC3=0V. TP1 voltage when TP7 voltage is output on TP13				
input voltage	L	VIL1		is V _{IH} 1. Gradually lower from VC3=Vcc. TP1 voltage when TP6 voltage is output on TP13 is V _{IL} 1.				
Switch 2	н	V _{IH} 2	5	Impress different optional DC voltages on TP6 and TP8. Gradually raise from VC3=0V. TP2 voltage when TP8 voltage is output on TP13				
input voltage	L	VIL2	·	is V _{IH} 2. Gradually lower from VC3=V _{CC} . TP2 voltage when TP6 voltage is output on TP13 is V _{II} 2.				
Switch 3	Н	V _{IH} 3	6	Impress different optional DC voltages on TP6 and TP9. Gradually raise from VC3=0V. TP3 voltage when TP9 voltage is output on TP13				
input voltage	L	VIL3	O	is Vih3. Gradually lower from VC3=Vcc. TP3 voltage when TP6 voltage is output on TP13 is Vil3.				
Switch 4	н	V _{IH} 4	7	Impress different optional DC voltages on TP9 and TP10. Gradually raise from VC3=0V. TP4 voltage when TP10 voltage is output on				
input voltage	L	VIL4	′	TP13 is V _{II} 4. Gradually lower from VC3=Vcc. TP4 voltage when TP9 voltage is output on TP13 is V _{II} 4.				
Switch 5	Н	V _{IH} 5	8	Impress different optional DC voltages on TP6 and TP11. Gradually raise from VC3=0V. TP5 voltage when TP11 voltage is output on				
input voltage	L	VII.5		TP13 is V _{IH} 5. Gradually lower from VC3=Vcc. TP5 voltage when TP6 voltage is output on TP13 is V _{IL} 5.				

Switch Conditions Table

	SW										
Conditions		Con	trol switc	hing		Input switching					
	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11
1	0	0	0	0	0	0	0	0	0	0	0
2	0	0	0	0	0	1	0	0	0	0	0
	1	0	0	0	0	0	1	0	0	0	0
	1	1	0	0	0	0	0	1	0	0	0
	1	1	1	0	0	0	0	0	1	0	0
	1	1	1	1	0	0	0	0	0	1	0
	1	1	1	1	1	0	0	0	0	0	1
3		С	onditions	2		0	0	0	0	0	0
4	2	0	0	0	0	0	0	0	0	0	0
5	0	2	0	0	0	0	0	0	0	0	0
6	0	0	2	0	0	0	0	0	0	0	0
7	0	0	1	2	0	0	0	0	0	0	0
8	0	0	0	0	2	0	0	0	0	0	0
9	Combination of all control switching and input switching when no signal is output to TP14.										

Control Input-Output Table

		OUT			
1	2	3	4	5	001
L	L	L	_	L	IN1
Н	L	L	_	L	IN2
_	Н	L	_	L	IN3
_	_	Н	L	L	IN4
_	_	Н	Н	L	IN5
_	_	_	_	Н	MUTE

Measuring Circuit

