# Advance Information

# The MRFIC Line **Balanced Transmit Mixer**

The MRFIC0931 is a balanced Gilbert cell mixer with LO buffer amplifier intended for transmit upmixer application. The device is usable for Industrial, Scientific and Medical (ISM), Cellular and PCS applications and is packaged in a low–cost surface mount package.

- Usable 500-2000 MHz
- High Output Power @ 1 dB Gain Compression, 3.6 Volts
   0.9 dBm (Typ) @ 900 MHz
   2 dBm (Typ) @ 1800 MHz
- 2.7-4.8 Volts Operation
- Balanced Design for Good LO Rejection
- 47 mA (Max) Current @ 4.5 Volts 45 mA (Max) Current @ 3.6 Volts
- Low Cost Surface Mount Package
- Order MRFIC0931R2 for Tape and Reel.
   R2 suffix = 2,500 Units per 12 mm, 13 inch Reel.
- Device Marking = M0931

# MRFIC0931

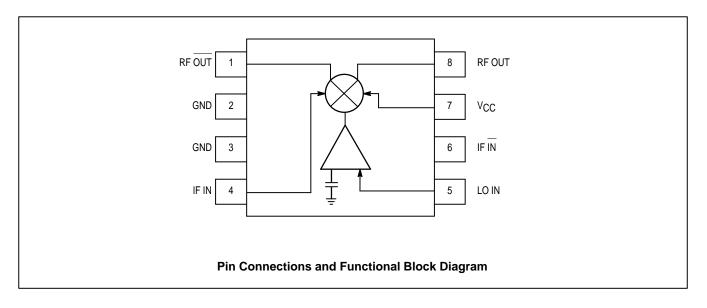
BALANCED TRANSMIT
MIXER SILICON
MONOLITHIC
INTEGRATED CIRCUIT



CASE 751-05 (SO-8)

#### **ABSOLUTE MAXIMUM RATINGS**

Ratings	Symbol	Value	Unit
Supply Voltage	VCC	5	Vdc
Input Power, IF and LO	P <sub>IF</sub> , P <sub>LO</sub>	+10	dBm
Operating Ambient Temperature	TA	- 35 to + 85	°C
Storage Temperature	T <sub>stg</sub>	- 65 to +150	°C







## **RECOMMENDED OPERATING CONDITIONS**

Parameter	Symbol	Value	Unit
Supply Voltages	Vcc	2.7 to 4.5	Vdc
RF Frequency Range	fRF	500 to 2000	MHz
IF Frequency Range	fIF	0 to 250	MHz

## **ELECTRICAL CHARACTERISTICS** ( $T_A = 25$ °C, measured in circuit shown in Figure 1 or 2 as frequency indicates)

Parameter	Min	Тур	Max	Unit
RF Output Power (flF = 88 MHz, fLO = 815 MHz, PLO = $-10$ dBm, PlF = $-20$ dBm, VCC = $4.5$ V)	2	4	8	dBm
LO Feed Through (flF = 88 MHz, fLO = 815 MHz, PLO = –10 dBm, PlF = –20 dBm, $V_{CC}$ = 4.5 V)	_	_	-16	dBc
Undesired Sideband Output (f $_{IF}$ = 88 MHz, f $_{LO}$ = 815 MHz, P $_{LO}$ = -10 dBm, P $_{IF}$ = -20 dBm, V $_{CC}$ = 4.5 V)	_	_	- 25	dBc
Small Sigal Conversion Gain (f $_{IF}$ = 100 MHz, P $_{IF}$ = -25 dBm, V $_{CC}$ = 3.6 V) 900 MHz (P $_{LO}$ = -10 dBm) 1800 MHz (P $_{LO}$ = -5 dBm)	_	25 20		dB dB
Power Output at 1 dB Gain Compression (f $_{IF}$ = 100 MHz, P $_{IF}$ = -25 dBm, V $_{CC}$ = 3.6 V) 900 MHz (P $_{LO}$ = -10 dBm) 1800 MHz (P $_{LO}$ = -5 dBm)		0.9 -2	_ _	dBm dBm
Supply Current  V <sub>CC</sub> = 4.5 V  V <sub>CC</sub> = 3.6 V		38 30	47 45	mA mA

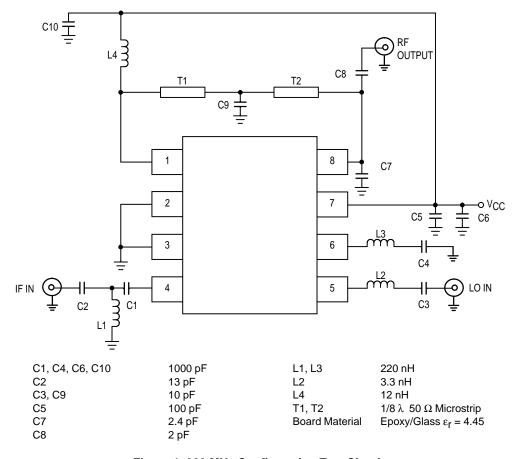
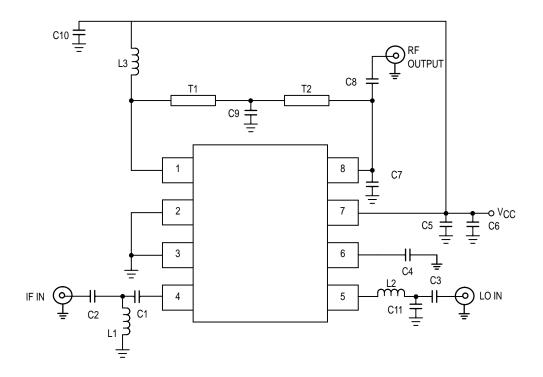


Figure 1. 903 MHz Configuration Test Circuit



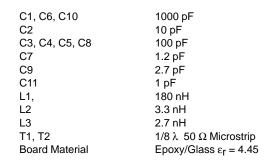
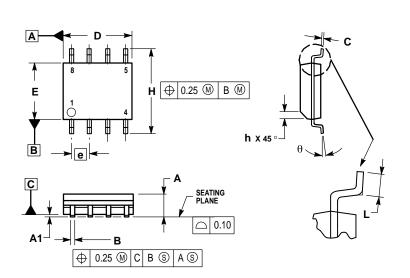


Figure 2. 1800 MHz Applications Circuit

#### PACKAGE DIMENSIONS



#### NOTES

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994
- DIMENSIONS ARE IN MILLIMETERS.
- DIMENSION D AND E DO NOT INCLUDE MOLD PROTRUSION
- MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.
- DIMENSION B DOES NOT INCLUDE MOLD PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 TOTAL IN EXCESS OF THE B DIMENSION AT MAXIMUM MATERIAL CONDITION

	MILLIMETERS		
DIM	MIN	MAX	
Α	1.35	1.75	
A1	0.10	0.25	
В	0.35	0.49	
С	0.18	0.25	
D	4.80	5.00	
E	3.80	4.00	
е	1.27 BSC		
Н	5.80	6.20	
h	0.25	0.50	
L	0.40	1.25	
Α	N٥	70	

**CASE 751-05 ISSUE S** 

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