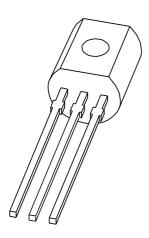
## **DISCRETE SEMICONDUCTORS**

# DATA SHEET



# **PSS9014C**NPN general purpose transistor

Product specification Supersedes data of 2002 Sep 20 2004 Aug 10





# NPN general purpose transistor

**PSS9014C** 

#### **FEATURES**

- High power dissipation: 500 mW
- · Low collector capacitance
- Low collector-emitter saturation voltage
- · High current capability.

#### **APPLICATIONS**

• General purpose switching and amplification.

#### **DESCRIPTION**

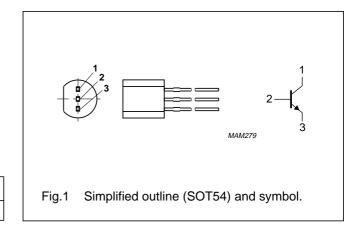
NPN low  $V_{\text{CEsat}}$  transistor in a SOT54 (TO-92) plastic package.

#### **MARKING**

TYPE NUMBER	MARKING CODE
PSS9014C	S9014C

#### **PINNING**

PIN	DESCRIPTION
1	collector
2	base
3	emitter



#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	50	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	45	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	5	V
I <sub>C</sub>	collector current (DC)		_	100	mA
I <sub>CM</sub>	peak collector current		_	200	mA
I <sub>BM</sub>	peak base current		_	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	_	500	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

1. Device mounted on a printed-circuit board; single sided copper; tinplated; standard footprint.

# NPN general purpose transistor

PSS9014C

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to	in free air; note 1	250	K/W
	ambient			

#### Note

1. Device mounted on a printed-circuit board; single sided copper; tinplated; standard footprint.

#### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

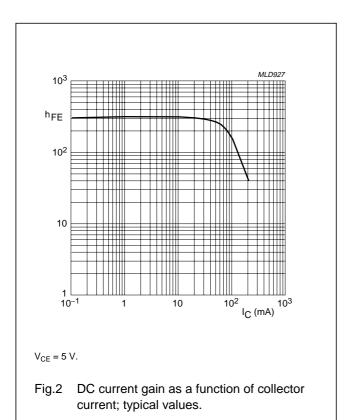
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0	_	_	15	nA
		V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0; T <sub>amb</sub> = 150 °C	_	_	5	μΑ
I <sub>CEO</sub>	collector-emitter cut-off current	$V_{CE} = 30 \text{ V}; I_{B} = 0$	_	_	100	nA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0$	_	_	100	nA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = 1 mA; V <sub>CE</sub> = 5 V	200	300	600	
		I <sub>C</sub> = 2 mA; V <sub>CE</sub> = 5 V	200	300	450	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 100 \text{ mA}; I_B = 5 \text{ mA}; \text{ note 1}$	_	200	300	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = 100 \text{ mA}$ ; $I_B = 0.5 \text{ mA}$ ; note 1	_	815	850	mV
V <sub>BEon</sub>	base-emitter turn-on voltage	I <sub>C</sub> = 2 mA; V <sub>CE</sub> = 5 V	580	650	700	mV
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 50 mA; V <sub>CE</sub> = 10 V; f = 100 MHz	100	220	_	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = i_e = 0; f = 1 \text{ MHz}$	_	1.6	1.75	pF
F	noise figure	$V_{CE} = 5 \text{ V; } I_{C} = 0.2 \text{ mA;}$ $R_{S} = 2 \text{ k}\Omega; f = 1 \text{ kHz; } B = 200 \text{ Hz}$	_	_	10	dB

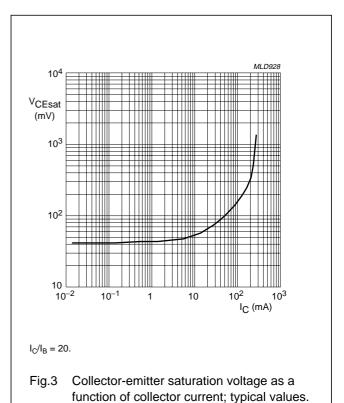
#### Note

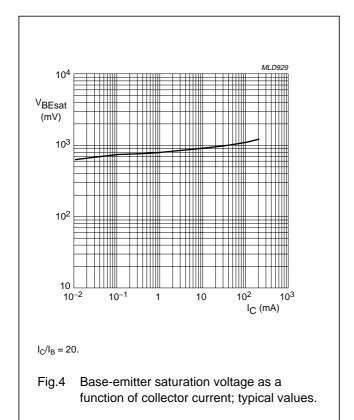
1. Pulse test:  $t_p \le 300 \ \mu s; \ \delta \le 0.02$ .

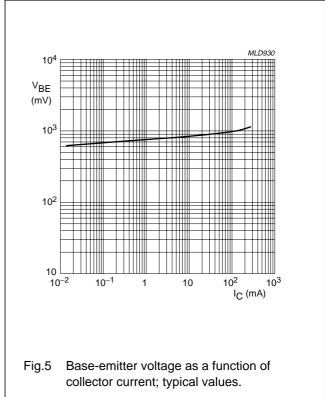
# NPN general purpose transistor

PSS9014C









2004 Aug 10

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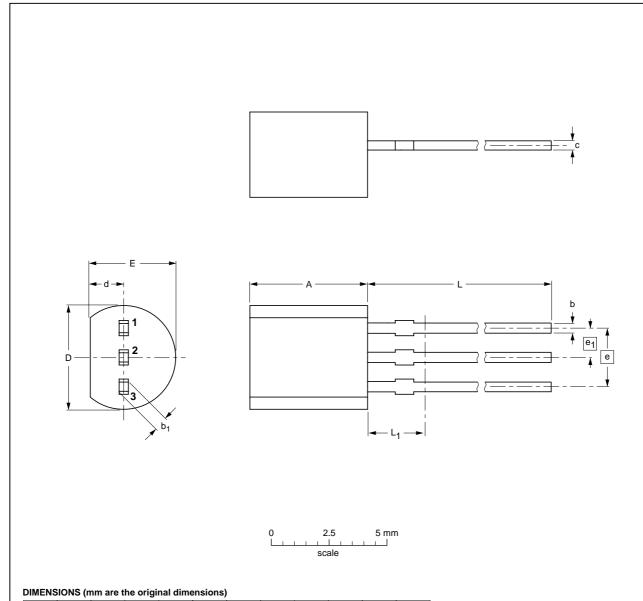
# NPN general purpose transistor

PSS9014C

#### **PACKAGE OUTLINE**

#### Plastic single-ended leaded (through hole) package; 3 leads

SOT54



UNIT	A	b	b <sub>1</sub>	С	D	d	E	е	e <sub>1</sub>	L	L <sub>1</sub> <sup>(1)</sup> max.	
mm	5.2 5.0	0.48 0.40	0.66 0.55	0.45 0.38	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5	

#### Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC JEITA PROJECTION		PROJECTION	ISSUE DATE	
SOT54		TO-92	SC-43A			<del>97-02-28</del> 04-06-28

### NPN general purpose transistor

PSS9014C

#### **DATA SHEET STATUS**

LEVEL	DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)(3)</sup>	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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SCA76

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