

FDD6030L

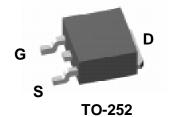
N-Channel Logic Level Enhancement Mode Field Effect Transistor

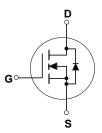
General Description

These N-Channel logic level enhancement mode power field effect transistors are produced using Fairchild's proprietary, high cell density, DMOS technology. This very high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage applications such as DC/DC converters and high efficiency switching circuits where fast switching, low in-line power loss, and resistance to transients are needed.

Features

- 50 A, 30 V. $R_{DS(ON)} = 0.0135 \ \Omega \ @V_{GS} = 10 \ V$ $R_{DS(ON)} = 0.0200 \ \Omega \ @V_{GS} = 4.5 \ V.$
- Low gate charge.
- · Fast switching speed.
- Low Crss.





Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units	
V _{DSS}	Drain-Source Voltage		30	V	
V _{GSS}	Gate-Source Voltage		±20	V	
I _D	Maximum Drain Current -Continuous	(Note 1)	50	A	
		(Note 1a)	12		
	Maximum Drain Current -Pulsed		150		
P _D	Maximum Power Dissipation @ T _C = 25°C	(Note 1)	60	W	
	$T_A = 25$ °C	(Note 1a)	3.2		
	$T_A = 25^{\circ}C$	(Note 1b)	1.3		
T _J , T _{stg}	Operating and Storage Junction Temperature	Range	-55 to +150	°C	

Thermal Characteristics

$R_{ heta$ JC	Thermal Resistance, Junction-to- Case	(Note 1)	2.1	°C/W
$R_{\theta^{JA}}$	Thermal Resistance, Junction-to- Ambient	(Note 1a)	39	°C/W
		(Note 1b)	96	°C/W

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity
FDD6030L	FDD6030L	13"	16mm	2500

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
OFF CH	ARACTERISTICS		•			
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 24 V, V _{GS} = 0 V			10	μA
I _{GSSF}	Gate-Body Leakage, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage, Reverse	$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
ON CHA	ARACTERISTICS (Note 2)		,	1		ı
1/	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1		3	V
$V_{GS(TH)}$	Cate Thiconola Tollage					
R _{DS(ON)}	Static Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, I_D = 12 \text{ A}$ $V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$			0.0135 0.0200	Ω
R _{DS(ON)}	Static Drain-Source	$V_{GS} = 10 \text{ V}, I_{D} = 12 \text{ A}$ $V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$	RATING	s		Ω
R _{DS(ON)}	Static Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, I_{D} = 12 \text{ A}$ $V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$ ERISTICS AND MAXIMUM	RATING	S		Ω

Notes:

^{1.} $R_{\theta,JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the drain tab. $R_{\theta,JC}$ is guaranteed by design while $R_{\theta,CA}$ is determined by the user's board design.



Scale 1 : 1 on letter size paper

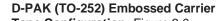
2. Pulse Test: Pulse Width $\leq\!300\,\mu\text{s},$ Duty Cycle $\leq\!2.0\%$

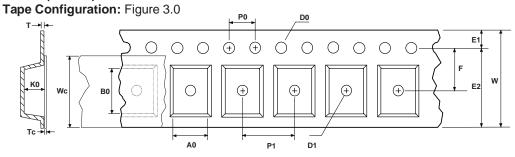
TO-252 Tape and Reel Data and Package Dimensions FAIRCHILD SEMICONDUCTOR TM D-PAK (TO-252) Packaging Configuration: Figure 1.0 Packaging Description: To-252 parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 2500 units per 13' or 330cm diameter reel. The reels are dark blue in color and is made of polystyrene plastic (anti-static coated). This and some other options are further described in the Packaging Information table. Antistatic Cover Tape ESD Label These full reels are individually barcode labeled and placed inside a standard intermediate box (illustrated in figure 1.0) made of recyclable corrugated brown paper. One box contains two reels maximum. And these boxes are placed inside a barcode labeled shipping box which comes in different sizes depending on the number of parts shipped. Static Dissipative **Embossed Carrier Tape** F63TNR Label D-PAK (TO-252) Packaging Information Packaging Option D-PAK (TO-252) Unit Orientation Packaging type TNR Qty per Reel/Tube/Bag 2.500 Reel Size 13" Dia Box Dimension (mm) 359x359x57 5,000 Max qty per Box 359mm x 359mm x 57mm Weight per unit (gm) 0.300 Standard Intermediate box Weight per Reel(kg) 1.200 **ESD Label** F63TNR Label sample F63TNR Label D/C1: Z9942 D/C2: SPEC REV: CPN: QTY1: QTY2: TO-252 (D-PAK) Tape Leader and **Trailer Configuration:** Figure 2.0 \bigcirc \bigcirc \bigcirc \bigcirc \bigcirc 0 0 0 0 Components Trailer Tape 640mm minimum or 1680mm minimum or

80 empty pockets

210 empty pockets







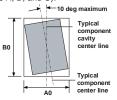
User Direction of Feed

					Dim	ensions	are in mi	llimeter						
Pkg type	A0	В0	w	D0	D1	E1	E2	F	P1	P0	K0	т	Wc	Тс
TO252 (24mm)	6.90 +/-0.10	10.50 +/-0.10	16.0 +/-0.3	1.55 +/-0.05	1.5 +/-0.10	1.75 +/-0.10	14.25 min	7.50 +/-0.10	8.0 +/-0.1	4.0 +/-0.1	2.65 +/-0.10	0.30 +/-0.05	13.0 +/-0.3	0.06 +/-0.02

Notes: A0, B0, and K0 dimensions are determined with respect to the EIA/Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C).



Sketch A (Side or Front Sectional View)
Component Rotation



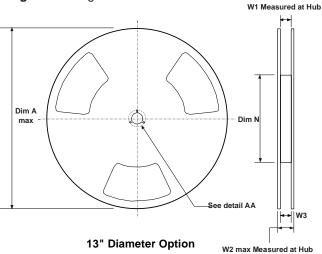
Sketch B (Top View)
Component Rotation

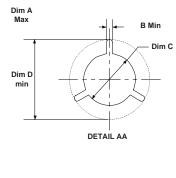


Sketch C (Top View)

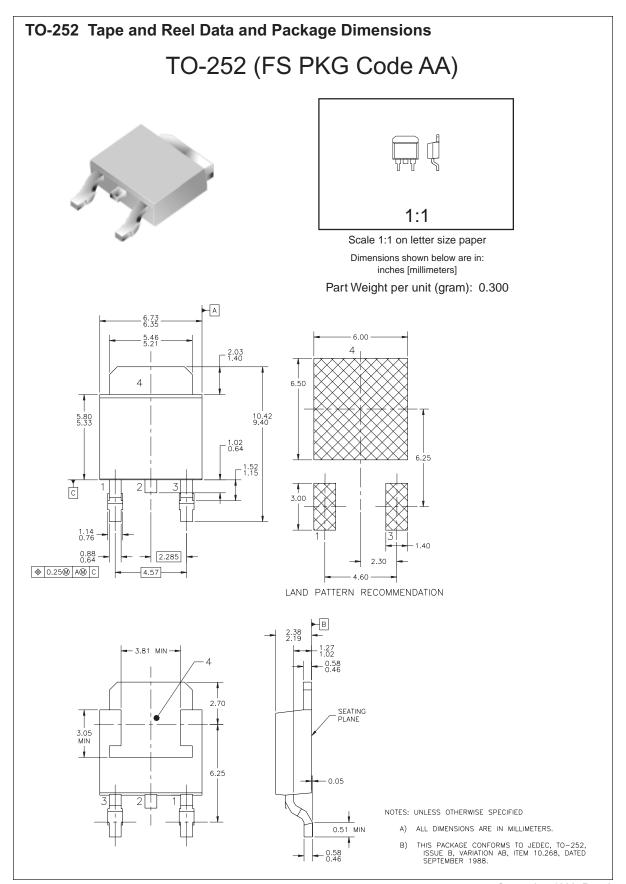
Component lateral movement

D-PAK (TO-252) Reel Configuration: Figure 4.0





Dimensions are in inches and millimeters									
Tape Size	Reel Option	Dim A	Dim B	Dim C	Dim D	Dim N	Dim W1	Dim W2	Dim W3 (LSL-USL)
164mm	13" Dia	13.00 330	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	4.00 100	0.646 +0.078/-0.000 16.4 +2/0	0.882 22.4	0.626 - 0.764 15.9 - 19.4



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 $\begin{array}{lll} \mathsf{FAST}^{\circledast} & \mathsf{Quiet}\,\mathsf{Series^{\mathsf{TM}}} \\ \mathsf{FASTr^{\mathsf{TM}}} & \mathsf{SuperSOT^{\mathsf{TM}}\text{-}3} \\ \mathsf{GTO^{\mathsf{TM}}} & \mathsf{SuperSOT^{\mathsf{TM}}\text{-}6} \\ \mathsf{HiSeC^{\mathsf{TM}}} & \mathsf{SuperSOT^{\mathsf{TM}}\text{-}8} \\ \end{array}$

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