



# 4CH Bridge (BTL) Driver for CD-R

#### Overview

The LA6564H is a 4-channel BTL driver developed for CD-ROM/R/RW and DVD-ROM actuator.

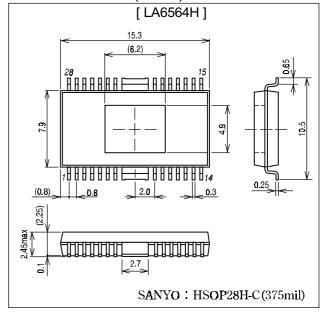
#### **Functions**

- Three power supply systems (with a separate preamplifier stage).
- Four bridge-connection (BTL) power amps built-in.
- IO max : 1A.
- Mute circuit (output ON/OFF) built-in. With three systems (2-1-1).
- Provides output voltage setting pin (for 4CH only).

### **Package Dimensions**

unit: mm

3234A-HSOP28H-C (375mil)



### **Specifications**

**Maximum Ratings** at  $Ta = 25^{\circ}C$ 

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage 1	V <sub>CC</sub> max	*1	14	V
Supply voltage 2	VS max	*1	14	V
Allowable power dissipation		Independent IC	0.82	W
	Pd max	A specified substrate (114.3mm×76.1mm×1.6mm/glass epoxy)	2	W
Maximum input voltage	VINB		13	V
Mute pin voltage	VMUTE		13	V
Maximum output current	I <sub>O</sub> max	Each output	1	Α
Operating temperature	Topr		-30 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

<sup>\*1</sup> Note : VCC≥VS\*

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#### **Recommended Operating Conditions** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage 1	V <sub>CC</sub>	V <sub>CC</sub> *1	4 to 13.5	٧
Supply voltage 2	VS	VS 1,2,3 *1	4 to 13.5	V

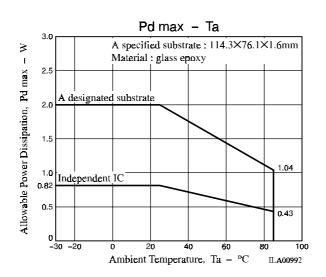
<sup>\*1</sup> Note : V<sub>C</sub>C≥VS\*

#### **Electrical Characteristics** at Ta = 25°C, V<sub>CC</sub> = 12V, VS1 = VS2 = 5V, VS3 = 12V, VREF = 1.65V, unless otherwise specified

Б	0 1 1	O maked	Ratings			11-24	
Parameter	Symbol	Conditions	min	typ	max	Unit	
[All Blocks]							
V <sub>CC</sub> no-load current drain	I <sub>CC</sub> -ON	V <sub>CC</sub> current, all outputs ON (all MUTE : H)		20	30	mA	
No-load current drain OFF	I <sub>CC</sub> -OFF	Total current of V <sub>CC</sub> and VS1 through 3 (All MUTE : L)			0.5	mA	
[Output AMP Block]							
Output offset voltage	VOFF	Between + and - outputs of each CH	-50		50	mV	
Output voltage 1	V <sub>O</sub> 1	$R_L$ = $8\Omega$ , voltage between outputs of CH1 through CH3 *1	4	4.5		٧	
Output voltage 2	V <sub>O</sub> 2	$R_L$ = 16 $\Omega$ , voltage between outputs of CH4 *1	10.5	11		٧	
Closed-circuit voltage gain 1	VG1	CH1,2,3, input and output gain	10	12	14	dB	
Closed-circuit voltage gain 2	VG2	CH4, input and output gain	16	18	20	В	
Input voltage range	V <sub>IN</sub>	Each input pin	0		VS*	٧	
Slew rate	SR	Independent AMP. Doubled when between outputs.		0.5		V/μs	
[MUTE Block]							
MUTE ON voltage	VMUTE-ON	MUTE *2	2			V	
MUTE OFF voltage	VMUTE-OFF	MUTE *2			0.5	V	
MUTE pin inrush current	I-MUTE	Inrush current of each MUTE pin		25	50	μА	
[AREF AMP Block]							
VREF-IN input voltage range			1		V <sub>CC</sub> -1.5	V	
[Voltage limiter block] [Setting the limit value of CH4 output voltage]							
V <sub>O</sub> -SET input and output gain	G-V <sub>O</sub> SET	*1	11	12	13	dB	
V <sub>O</sub> -SET input current	I-V <sub>O</sub> SET	V <sub>O</sub> -SET : current at 3.3 V			1	μА	

<sup>\*1.</sup> Output saturated.

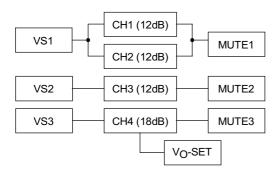
<sup>\*2.</sup> MUTE output ON with HI and OFF with LOW (High impedance with AMP output OFF). MUTE operates independently for each CH (Refer to "Relationship of MUTE and output" described later).



1) Relation ship of MUTE and output

	Output           CH1         CH2         CH3         CH4			
Each MUTE	MUTE1		MUTE2	MUTE3
Н	ON			
L	OFF			

- \*1 The output becomes HI impedance when it is OFF.
- \*2 MUTE operates independently for each CH (Refer to the following description). All MUTES enter the STBY mode when they are L (output OFF), turning OFF all the circuits including the output AMP.
- 2) Relationship between each CH and V\*, MUTE



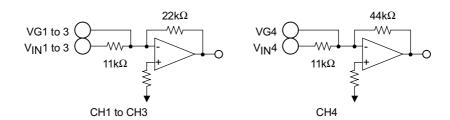
#### 3) Vo-SET

V<sub>O</sub>-SET operates for CH4. V<sub>O</sub>-SET is correlated to CH4 output by 12dB. For example, the output is 4V when V<sub>O</sub>-SET is 1V.

#### 4) Gain set (V<sub>IN</sub>\* and VG\*)

Gain of each CH can be equivalently represented as follows:

- CH1 to CH3: 12dB, CH4: 18dB when only V<sub>IN</sub> pin is used. The similar gain is obtained also when a 11k resistor is used for the VG\* pin and the input is provided from its resistor end.
- The input/output gain is determined from the resistance ratio as shown in the figure below. To set the gain with the VG pin, the input-output gain has a slight temperature characteristic depending on the difference in temperature characteristic between internal and external resistances.

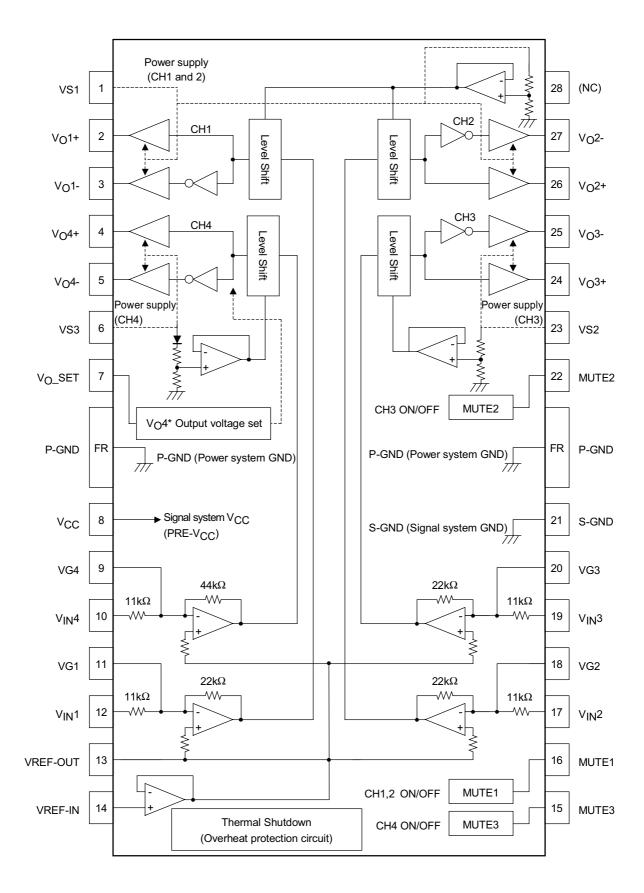


# **Pin Functions**

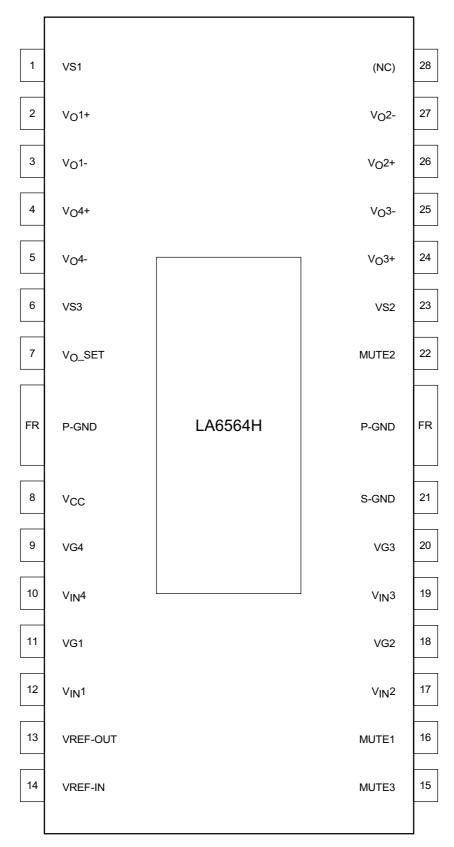
Pin No.	Pin Name	Description (Functions)
1	VS1	Output stage power supply for channel 1 and 2
2	V <sub>O</sub> 1+	Output pin (+) for channel 1, non-inverted output for channel 1 input
3	V <sub>O</sub> 1-	Output pin (-) for channel 1, inverted output for channel 1 input
4	V <sub>O</sub> 4+	Output pin (+) for channel 4, non-inverted output for channel 4 input
5	V <sub>O</sub> 4-	Output pin (-) for channel 4, inverted output for channel 4 input
6	VS3	Output stage power supply for channel 4
7	V <sub>O</sub> _SET	Pin to adjust channel 4 output voltage
8	Vcc	Power supply for preamplifier stage signal system
9	VG4	Input pin for channel 4 (for gain adjustment)
10	V <sub>IN</sub> 4	Input pin for channel 4
11	VG1	Input pin for channel 1 (for gain adjustment)
12	V <sub>IN</sub> 1	Input pin for channel 1
13	VREF-OUT	VREF-AMP output
14	VREF-IN	Reference voltage input pin
15	MUTE3	ON/OFF for channel 4 output
16	MUTE1	ON/OFF for channel 1 and 2 outputs
17	V <sub>IN</sub> 2	Input pin for channel 2
18	VG2	Input pin for channel 2 (for gain adjustment)
19	V <sub>IN</sub> 3	Input pin for channel 3
20	VG3	Input pin for channel 3 (for gain adjustment)
21	S-GND	Signal system GND
22	MUTE2	ON/OFF for channel 3 output
23	VS2	Output stage power supply for channel 3
24	V <sub>O</sub> 3+	Output pin (+) for channel 3, non-inverted output for channel 3 input
25	V <sub>O</sub> 3-	Output pin (-) for channel 3, inverted output for channel 3 input
26	V <sub>O</sub> 2+	Output pin (+) for channel 2, non-inverted output for channel 2 input
27	V <sub>O</sub> 2-	Output pin (-) for channel 2, inverted output for channel 2 input
28	(NC)	Do not use

<sup>\*</sup> Center frame (FR) becomes GND for the power system (P-GND). Set this to the minimum potential together with S-GND.

### **Block Diagram**



## **Pin Assignment**



Top view

# **Pin Description**

Name	Pin Name	Pin No.	Equivalent Circuit Diagram	Description
Input	V <sub>IN</sub> 1 V <sub>IN</sub> 2 V <sub>IN</sub> 3 V <sub>IN</sub> 4 VG1 VG2 VG3 VG4	12 17 19 10 11 18 20 9	VG* 11kΩ VCC VREF-OUT S-GND	Input pins
Output	V <sub>O</sub> 1+ V <sub>O</sub> 1- V <sub>O</sub> 2+ V <sub>O</sub> 2- V <sub>O</sub> 3+ V <sub>O</sub> 3- V <sub>O</sub> 4+ V <sub>O</sub> 4-	2 3 26 27 24 25 4 5	Vcc* Vo* P-GND	Output pins
MUTE	MUTE1 MUTE2 MUTE3	16 22 15	MUTE1,2	Switch for each channel output MUTE: H output ON MUTE: L output OFF

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