HSU119

Silicon Epitaxial Planar Diode for High Speed Switching

HITACHI

ADE-208-444(Z) Rev 0 Feb. 1997

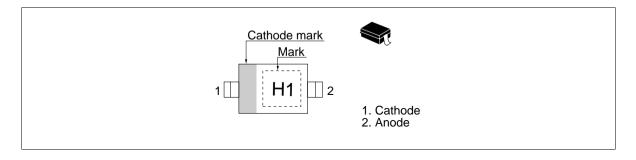
Features

- Low capacitance. (C = 2.0 pF max)
- Short reverse recovery time. $(t_{rr} = 3.0 \text{ns max})$
- Ultra small Resin Package (URP) is suitablefor high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HSU119	H1	URP

Outline





HSU119

Absolute Maximum Ratings (Ta = 25°C)

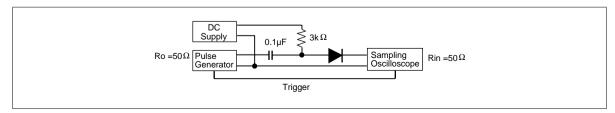
Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM}	85	V
Reverse voltage	V_R	80	V
Average forward current	Io	100	mA
Peak rectified current	I _{FM}	300	mA
Non-Repetitive peak forward surge current	I _{FSM} *1	4	A
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Note: 1. Within 1μ s forward surge current.

Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V_{F1}	_	_	0.8	V	I _F = 10 mA
	V_{F2}	_	_	1.2		I _F = 100 mA
Reverse current	I _R	_	_	0.1	μΑ	V _R = 80V
Capacitance	С	_	_	2.0	pF	$V_R = 0V, f = 1 MHz$
Reverse recovery time*1	t _{rr}	_	_	3.0	ns	$I_F = 10 \text{ mA}, V_R = 6V R_L = 50\Omega$

Note: 1. Reverse recovery time test circuit



Main Characteristic

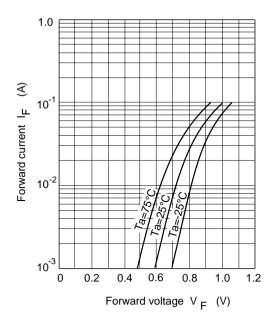


Fig.1 Forward current Vs. Forward voltage

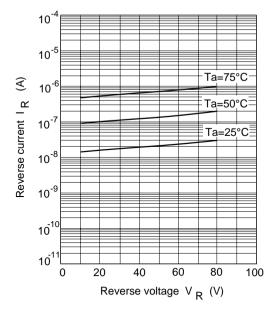


Fig.2 Reverse current Vs. Reverse voltage

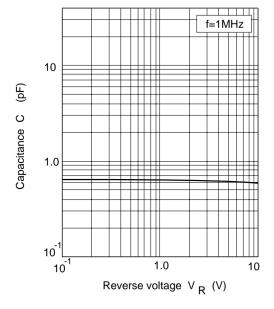
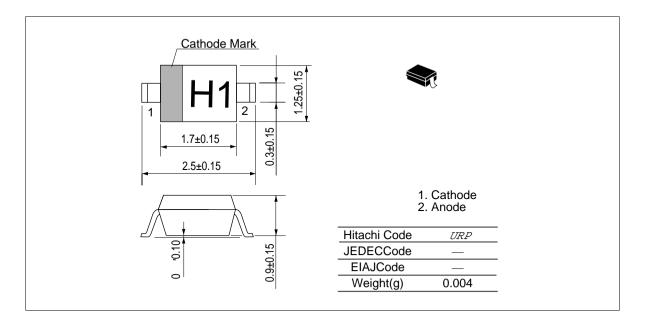


Fig.3 Capacitance Vs. Reverse voltage

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Package Dimensions

Unit: mm



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