

6-Pin DIP Optoisolators for Power Supply Applications (No Base Connection)

The MOC8106, MOC8107 and MOC8108 devices consist of a gallium arsenide LED optically coupled to a silicon phototransistor in a dual-in-line package.

- Closely Matched Current Transfer Ratio (CTR) Minimizes Unit-to-Unit Variation
- Narrow (CTR) Windows that translate to a Narrow and Predictable Open Loop Gain Window
- Very Low Coupled Capacitance along with No Chip to Pin 6 Base Connection for Minimum Noise Susceptibility
- *To order devices that are tested and marked per VDE 0884 requirements, the suffix "V" must be included at end of part number. VDE 0884 is a test option.*

Applications

- Switchmode Power Supplies (Feedback Control)
- AC Line/Digital Logic Isolation
- Interfacing and coupling systems of different potentials and impedances

MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
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INPUT LED

Forward Current — Continuous	I_F	60	mA
Forward Current — Peak (PW = 100 μs , 120 pps)	$I_F(\text{pk})$	1	A
Reverse Voltage	V_R	6	Volts
LED Power Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	120 1.41	mW mW/ $^\circ\text{C}$

OUTPUT TRANSISTOR

Collector-Emitter Voltage	V_{CEO}	70	Volts
Emitter-Collector Voltage	V_{ECO}	7	Volts
Collector Current — Continuous	I_C	150	mA
Detector Power Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	150 1.76	mW mW/ $^\circ\text{C}$

TOTAL DEVICE

Input-Output Isolation Voltage ⁽¹⁾ (f = 60 Hz, t = 1 sec.)	V_{ISO}	7500	Vac(pk)
Total Device Power Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	250 2.94	mW mW/ $^\circ\text{C}$
Ambient Operating Temperature Range ⁽²⁾	T_A	-55 to +100	$^\circ\text{C}$
Storage Temperature Range ⁽²⁾	T_{stg}	-55 to +150	$^\circ\text{C}$
Lead Soldering Temperature (1/16" from case, 10 sec. duration)	T_L	260	$^\circ\text{C}$

1. Input-Output Isolation Voltage, V_{ISO} , is an internal device dielectric breakdown rating. For this test, Pins 1 and 2 are common, and Pins 4 and 5 are common.
 2. Refer to Quality and Reliability Section in Opto Data Book for information on test conditions.
- GlobalOptoisolator is a trademark of Motorola, Inc.

MOC8106

[CTR = 50–150%]

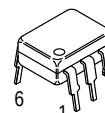
MOC8107

[CTR = 100–300%]

MOC8108

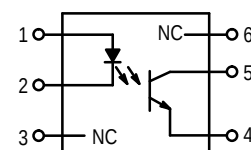
[CTR = 250–600%]

STYLE 3 PLASTIC



STANDARD THRU HOLE
CASE 730A-04

SCHEMATIC



- PIN 1. ANODE
2. CATHODE
3. NO CONNECTION
4. EMITTER
5. COLLECTOR
6. NO CONNECTION



MOC8106 MOC8107 MOC8108

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)⁽¹⁾

Characteristic	Symbol	Min	Typ ⁽¹⁾	Max	Unit
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INPUT LED

Forward Voltage ($I_F = 10\text{ mA}$)	V_F	1.0	1.15	1.5	Volts
Reverse Leakage Current ($V_R = 5.0\text{ V}$)	I_R	—	0.05	10	μA
Capacitance	C	—	18	—	pF

OUTPUT TRANSISTOR

Collector–Emitter Dark Current ($V_{CE} = 10\text{ V}$, $T_A = 25^\circ\text{C}$)	I_{CEO1}	—	1.0	50	nA
	I_{CEO2}	—	1.0	—	μA
Collector–Emitter Breakdown Voltage ($I_C = 1.0\text{ mA}$)	$V_{(BR)CEO}$	70	100	—	Volts
Emitter–Collector Breakdown Voltage ($I_E = 100\text{ }\mu\text{A}$)	$V_{(BR)ECO}$	7.0	7.8	—	Volts
Collector–Emitter Capacitance ($f = 1.0\text{ MHz}$, $V_{CE} = 0$)	C_{CE}	—	7.0	—	pF

COUPLED

Output Collector Current ($I_F = 10\text{ mA}$, $V_{CE} = 10\text{ V}$)	MOC8106 MOC8107 MOC8108	$I_C\text{ (CTR)}^{(2)}$	5.0 (50) 10 (100) 25 (250)	— — —	15 (150) 30 (300) 60 (600)	$\text{mA } (\%)$
Collector–Emitter Saturation Voltage ($I_C = 500\text{ }\mu\text{A}$, $I_F = 5.0\text{ mA}$)		$V_{CE(sat)}$	—	0.15	0.4	Volts
Turn–On Time ($I_C = 2.0\text{ mA}$, $V_{CC} = 10\text{ V}$, $R_L = 100\text{ }\Omega$) ⁽³⁾		t_{on}	—	7.5	—	μs
Turn–Off Time ($I_C = 2.0\text{ mA}$, $V_{CC} = 10\text{ V}$, $R_L = 100\text{ }\Omega$) ⁽³⁾		t_{off}	—	5.7	—	μs
Rise Time ($I_C = 2.0\text{ mA}$, $V_{CC} = 10\text{ V}$, $R_L = 100\text{ }\Omega$) ⁽³⁾		t_r	—	3.2	—	μs
Fall Time ($I_C = 2.0\text{ mA}$, $V_{CC} = 10\text{ V}$, $R_L = 100\text{ }\Omega$) ⁽³⁾		t_f	—	4.7	—	μs
Isolation Voltage ($f = 60\text{ Hz}$, $t = 1.0\text{ sec.}$) ⁽⁴⁾		V_{ISO}	7500	—	—	Vac(pk)
Isolation Resistance ($V_{I-O} = 500\text{ V}$) ⁽⁴⁾		R_{ISO}	10^{11}	—	—	Ω
Isolation Capacitance ($V_{I-O} = 0$, $f = 1.0\text{ MHz}$) ⁽⁴⁾		C_{ISO}	—	0.2	—	pF

1. Always design to the specified minimum/maximum electrical limits (where applicable).
2. Current Transfer Ratio (CTR) = $I_C/I_F \times 100\%$.
3. For test circuit setup and waveforms, refer to Figure 7.
4. For this test, Pins 1 and 2 are common, and Pins 4 and 5 are common.

TYPICAL CHARACTERISTICS

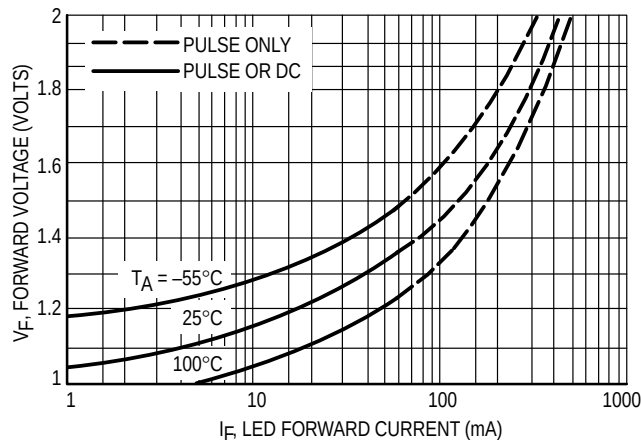


Figure 1. LED Forward Voltage versus Forward Current

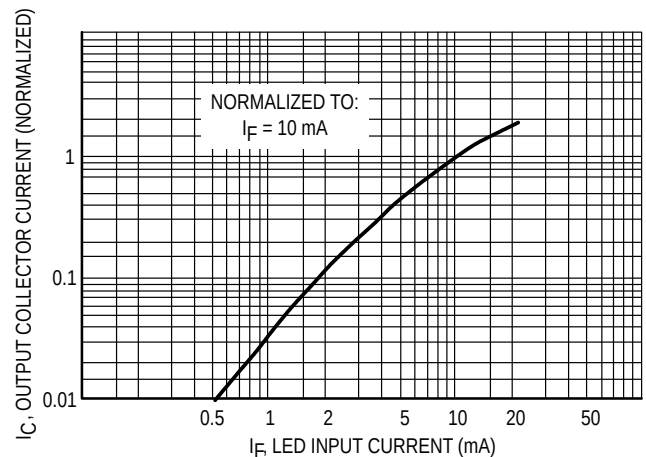


Figure 2. Output Current versus Input Current

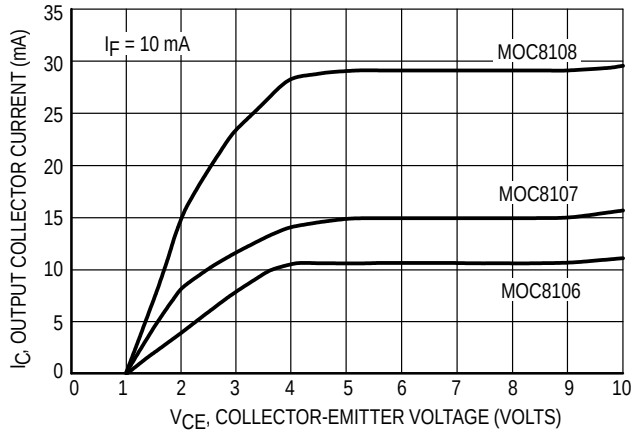


Figure 3. Collector Current versus Collector-Emitter Voltage

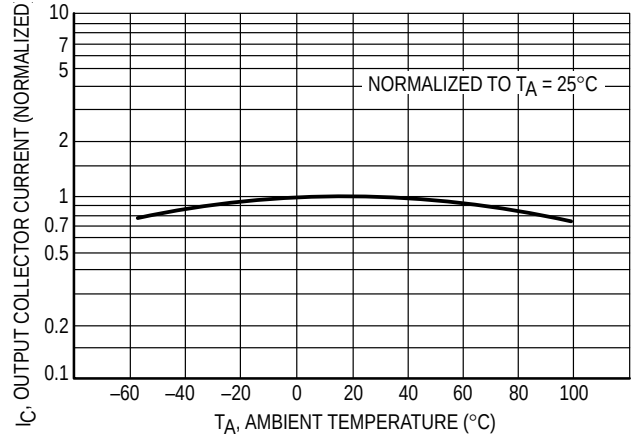


Figure 4. Output Current versus Ambient Temperature

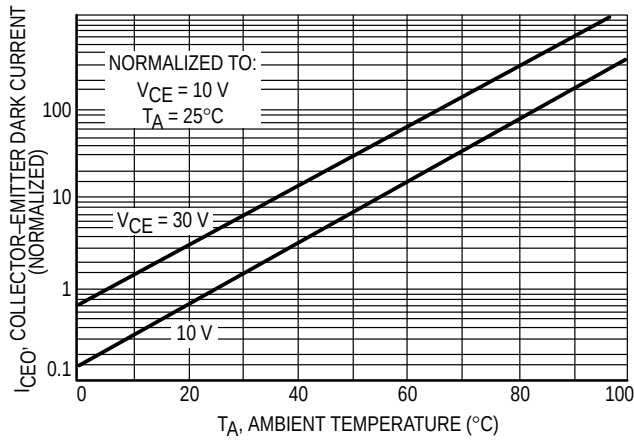


Figure 5. Dark Current versus Ambient Temperature

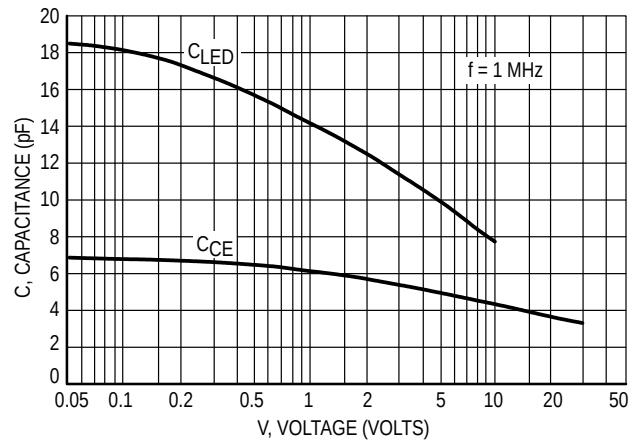


Figure 6. Capacitance versus Voltage

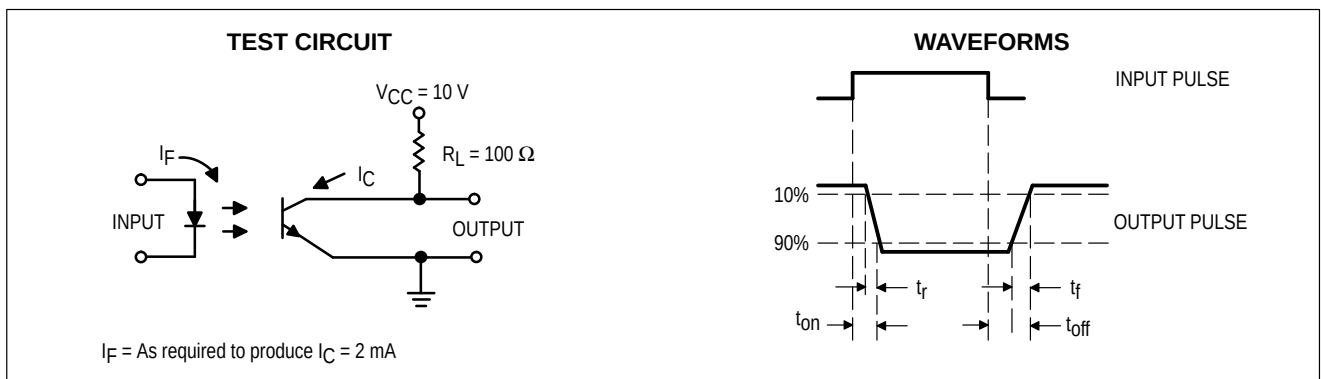
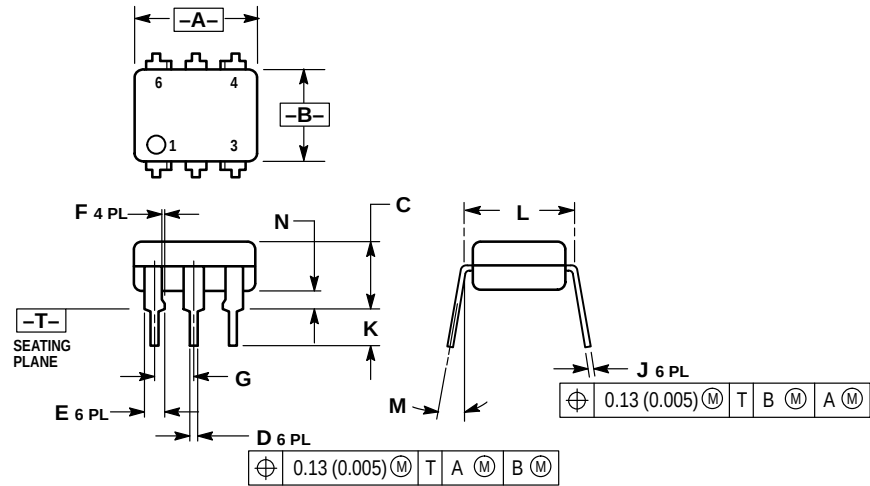


Figure 7. Switching Time Test Circuit and Waveforms

MOC8106 MOC8107 MOC8108

PACKAGE DIMENSIONS

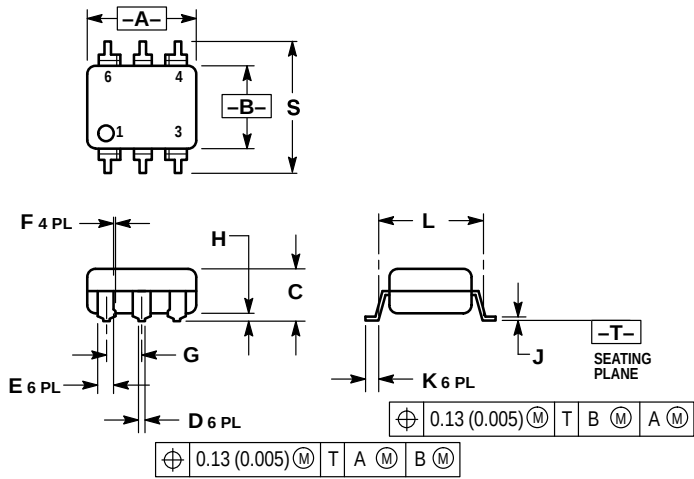


- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.320	0.350	8.13	8.89
B	0.240	0.260	6.10	6.60
C	0.115	0.200	2.93	5.08
D	0.016	0.020	0.41	0.50
E	0.040	0.070	1.02	1.77
F	0.010	0.014	0.25	0.36
G	0.100 BSC		2.54 BSC	
J	0.008	0.012	0.21	0.30
K	0.100	0.150	2.54	3.81
L	0.300 BSC		7.62 BSC	
M	0°	15°	0°	15°
N	0.015	0.100	0.38	2.54

- STYLE 3:
- PIN 1: ANODE
 - CATHODE
 - NC
 - EMITTER
 - COLLECTOR
 - NC

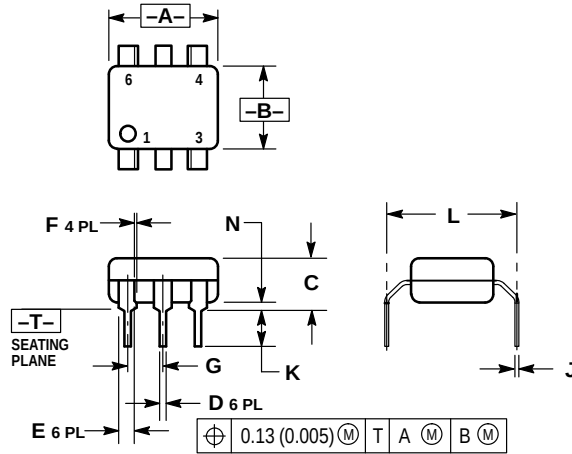
CASE 730A-04
ISSUE G



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.320	0.350	8.13	8.89
B	0.240	0.260	6.10	6.60
C	0.115	0.200	2.93	5.08
D	0.016	0.020	0.41	0.50
E	0.040	0.070	1.02	1.77
F	0.010	0.014	0.25	0.36
G	0.100 BSC		2.54 BSC	
H	0.020	0.025	0.51	0.63
J	0.008	0.012	0.20	0.30
K	0.006	0.035	0.16	0.88
L	0.320 BSC		8.13 BSC	
S	0.332	0.390	8.43	9.90

CASE 730C-04
ISSUE D



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.320	0.350	8.13	8.89
B	0.240	0.260	6.10	6.60
C	0.115	0.200	2.93	5.08
D	0.016	0.020	0.41	0.50
E	0.040	0.070	1.02	1.77
F	0.010	0.014	0.25	0.36
G	0.100 BSC		2.54 BSC	
J	0.008	0.012	0.21	0.30
K	0.100	0.150	2.54	3.81
L	0.400	0.425	10.16	10.80
N	0.015	0.040	0.38	1.02

**CASE 730D-05
ISSUE D**

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