

No.4955

LA7152

VCR Electronic Switch

Overview

The LA7152 is a three-input (clamped input) single-output analog switch for video signals. The LA7152 high input impedance structure allows $0.01~\mu F$ ceramic capacitors to be used as the input coupling capacitors.

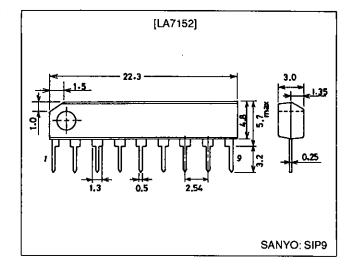
Features

- Three inputs one output
- · Built-in video clamping circuits
- · Built-in muting function

Package Dimensions

unit: mm

3017C-SIP9



Specifications

Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		7.0	V
Allowable power dissipation	Pd max		100	mW
Operating temperature	Topr		-10 to +70	°C
Storage temperature	Tstg		-40 to +150	•c

Recommended Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	v _c c		5.0	V
Operating supply voltage range	Vopg		4.5 to 6.0	٧

Operating Characteristics at Ta = 25°C, $V_{CC} = 5$ V

Parameter	Symbol	Conditions	min	typ	max	Unit
Current drain	lcc	*1	3.0	4.1	5.2	mA
Maximum input level	V _{IN} max	•2	2.0	2.5		Vp-p
Frequency characteristics	Gf	•3		0	±0.5	dB
Total harmonic distortion	THD	*4		0.03	0.1	%
Inter-channel crosstalk	ст _с	*5		-65	-60	dB
Muting circuit crosstalk	CTM	*6		-55	-50	dB
Output DC offset	ΔV _{OUT}	*7		5	20	mV
Differential gain	DG	*8		0.5	1	%
Differential phase	DP	*9		0.5	1	deg

Note: 1. Current drain

S1 = S2 = S3 = 2, S4 = S5 = S6 = 3

2. Maximum input level (input $C = 10 \mu F$)

S1 = 1, S4 = 1, S2 = S3 = 2, S5 = S6 = 3,

S2 = 1, S5 = 1, S1 = S3 = 2, S4 = S6 = 3,

S3 = 1, S1 = S2 = 2, S4 = S5 = S6 = 3

For each of the above three conditions, with an input signal frequency f = 1 kHz, gradually increase the input signal level and determine the level where the total harmonic distortion reaches 0.1%.

3. Frequency characteristics

For each of the three conditions in Note 2,

 V_{IN} = 2.0 V_{p-p} , V_{OUT} (5 MHz)/ V_{OUT} (100 KHz) 4. Total harmonic distortion (input C = 10 μ F)

For each of the three conditions in item 2, measure the total harmonic distortion with $V_{IN} = 2.0 V_{p-p}$ and f = 1 kHz.

5. Crosstalk

With S6 = 3, measure in the modes for all combinations of S1 to S5 except for the following three conditions: a) S1 = S4 = 1, b) S2 = S5 = 1 and c) S3 = 1, S4 = S5 = 3.

 $V_{IN} = 2.0 \ V_{p\cdot p}, f = 4.43 \ MHz, V_{OUT}/V_{IN}$ 6. Muting circuit crosstalk

With S6 = 1, measure in the modes for all combinations of S1 to S5.

 V_{IN} = 2.0 V_{p-p} , f = 4.43 MHz, V_{OUT}/V_{IN} 7. Output DC offset

Measure the output DC voltage difference between the following modes with S1 = S2 = S3 = 2.

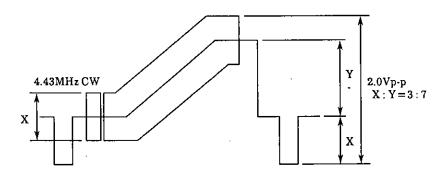
a. S4 = 1, S5 = S6 = 3

b. S5 = 1, S4 = S6 = 3

c. \$4 = \$5 = \$6 = 3

d. S6 = 1, S4 = S5 = 1 or 2

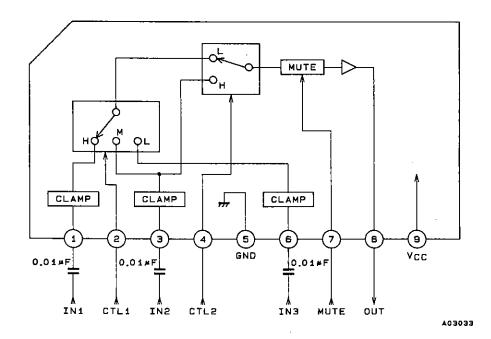
8, 9. Differential gain, differential phase



Switching Characteristics at $Ta = 25^{\circ}C$, $V_{CC} = 5 \text{ V}$

	Parameter	Symbol	Conditions	min	typ	max	Unit
	н	V _{C1} H		3.5		v _{cc}	V
CTL1	М	V _{C1} M		1.5		3.0	V
	L	V _{C1} L	In the state with S4 = 2, S5 = 2 and S6 = 2, measure the control voltage level when the input signal switches.	0		1.0	٧
CTL2	н	V _{C2} H		2.5		Vcc	٧
CILZ	L	V _{C2} L		0		1.5	V
MUTE	Н	V _{CM} H		3.0		V _{CC}	٧
MOTE	L	V _{CM} L		0		1.5	V

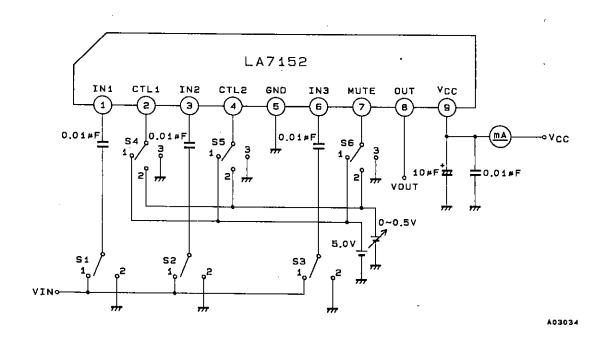
Equivalent Circuit Block Diagram and Application Circuit Diagram



Truth Table

CTL1	L	L	М	М	Н	Н	
CTL2	Ļ	Η	L	Н	L	н	_
MUTE	L	L	L	L	L	L	н
OUT	IN3	IN2	IN2	IN2	IN1	IN2	DC

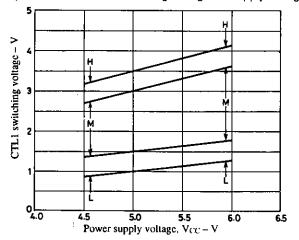
Test Circuit

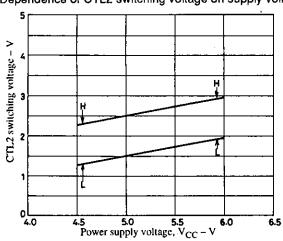


Pin Functions

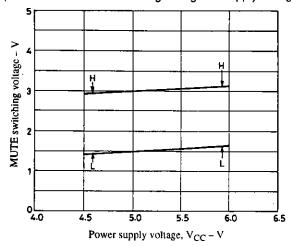
## Processor Fig. 2 Fig. 2	Pin No.	Symbol	I/O type	Note
Tie CONTROL2 (pin 4) to ground when using three-value control by CONTROL1 (pin 2). Tie CONTROL2 (pin 4) to ground when using three-value control by CONTROL1 (pin 2). Tie CONTROL2 (pin 4) to ground when using three-value control by CONTROL1 (pin 2). A03038 Tie CONTROL2 (pin 4) to ground when using three-value control by CONTROL1 (pin 2). Collector current: 1.3 mA	3	IN2	30nA \$0.2kg	High impedance input
7 MUTE CONTROL 50k n \$ 5kn			₹50ka Θ	Tie CONTROL2 (pin 4) to ground when using three-value control by CONTROL1 (pin 2).
7 MUTE CONTROL 50k 0 A03037 OUT B Collector current: 1.3 mA	5	GND		
B OUT Coilector current: 1.3 mA	7	MUTE CONTROL	5ka ₩ 50ka \$	
	8	ОИТ	(a) (b) (c) (c) (c) (c) (c) (c) (c) (c) (c) (c	Collector current: 1.3 mA
	. 9	V _{cc}	A03030	

Dependence of CTL1 switching voltage on supply voltage Dependence of CTL2 switching voltage on supply voltage





Dependence of MUTE switching voltage on supply voltage



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