

**Triacs**

**BT138F series**

**GENERAL DESCRIPTION**

Glass passivated triacs in a full pack plastic envelope, intended for use in applications requiring high bidirectional transient and blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

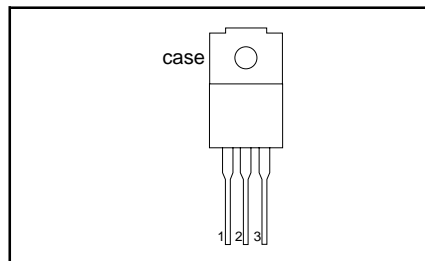
**QUICK REFERENCE DATA**

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
		500	600	800	
$V_{DRM}$	Repetitive peak off-state voltages	500	600	800	V
$I_{T(RMS)}$	RMS on-state current	12	12	12	A
$I_{TSM}$	Non-repetitive peak on-state current	90	90	90	A

**PINNING - SOT186**

PIN	DESCRIPTION
1	main terminal 1
2	main terminal 2
3	gate
case	isolated

**PIN CONFIGURATION**



**SYMBOL**



**LIMITING VALUES**

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.			UNIT
				-500	-600	-800	
$V_{DRM}$	Repetitive peak off-state voltages		-	500 <sup>1</sup>	600 <sup>1</sup>	800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{hs} \leq 56^\circ C$	-	12			A
$I_{TSM}$	Non-repetitive peak on-state current	full sine wave; $T_j = 125^\circ C$ prior to surge; with reapplied $V_{DRM(max)}$ $t = 20$ ms	-	90			A
		$t = 16.7$ ms	-	100			A
		$t = 10$ ms	-	40			A <sup>2</sup> s
$I^2t$	$I^2t$ for fusing		-				A
$di_T/dt$	Repetitive rate of rise of on-state current after triggering	$I_{TM} = 20$ A; $I_G = 0.2$ A; $di_G/dt = 0.2$ A/ $\mu$ s	-				A/ $\mu$ s
		T2+ G+	-	50			A/ $\mu$ s
		T2+ G-	-	50			A/ $\mu$ s
		T2- G-	-	50			A/ $\mu$ s
		T2- G+	-	10			A/ $\mu$ s
$I_{GM}$	Peak gate current		-	2			A
$V_{GM}$	Peak gate voltage		-	5			V
$P_{GM}$	Peak gate power		-	5			W
$P_{G(AV)}$	Average gate power	over any 20 ms period	-	0.5			W
$T_{stg}$	Storage temperature		-40	150			$^\circ C$
$T_j$	Operating junction temperature		-	125			$^\circ C$

<sup>1</sup> Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15 A/ $\mu$ s.

## Triacs

## BT138F series

**ISOLATION LIMITING VALUE & CHARACTERISTIC** $T_{hs} = 25\text{ °C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{isol}$	Repetitive peak voltage from all three terminals to external heatsink	R.H. $\leq$ 65% ; clean and dustfree	-		1500	V
$C_{isol}$	Capacitance from T2 to external heatsink	$f = 1\text{ MHz}$	-	12	-	pF

**THERMAL RESISTANCES**

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{th\ j-hs}$	Thermal resistance junction to heatsink	full or half cycle with heatsink compound	-	-	4.0	K/W
$R_{th\ j-a}$	Thermal resistance junction to ambient	without heatsink compound in free air	-	55	5.5	K/W

**STATIC CHARACTERISTICS** $T_j = 25\text{ °C}$  unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.			UNIT
$I_{GT}$	Gate trigger current	<b>BT138F-</b> $V_D = 12\text{ V}; I_T = 0.1\text{ A}$			...	...F	...G	
		T2+ G+	-	5	35	25	50	mA
		T2+ G-	-	8	35	25	50	mA
		T2- G-	-	10	35	25	50	mA
		T2- G+	-	22	70	70	100	mA
$I_L$	Latching current	$V_D = 12\text{ V}; I_{GT} = 0.1\text{ A}$						
		T2+ G+	-	7	40	40	60	mA
		T2+ G-	-	20	60	60	90	mA
		T2- G-	-	8	40	40	60	mA
		T2- G+	-	10	60	60	90	mA
$I_H$	Holding current	$V_D = 12\text{ V}; I_{GT} = 0.1\text{ A}$	-	6	30	30	60	mA
$V_T$	On-state voltage	$I_T = 15\text{ A}$	-	1.4	1.65			V
$V_{GT}$	Gate trigger voltage	$V_D = 12\text{ V}; I_T = 0.1\text{ A}$	-	0.7	1.5			V
		$V_D = 400\text{ V}; I_T = 0.1\text{ A};$ $T_j = 125\text{ °C}$	0.25	0.4	-			V
$I_D$	Off-state leakage current	$V_D = V_{DRM(max)};$ $T_j = 125\text{ °C}$	-	0.1	0.5			mA

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## BT138F series

**DYNAMIC CHARACTERISTICS** $T_j = 25\text{ °C}$  unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.			TYP.	MAX.	UNIT
			...	...F	...G			
$dV_D/dt$	Critical rate of change of off-state voltage	<b>BT138F-</b> $V_{DM} = 67\% V_{DRM(max)}$ ; $T_j = 125\text{ °C}$ ; exponential waveform; gate open circuit	100	50	200	250	-	V/ $\mu$ s
$dV_{com}/dt$	Critical rate of change of commutating voltage	$V_{DM} = 400\text{ V}$ ; $T_j = 95\text{ °C}$ ; $I_{T(RMS)} = 12\text{ A}$ ; $dl_{com}/dt = 5.4\text{ A/ms}$ ; gate open circuit	-	-	10	20	-	V/ $\mu$ s
$t_{gt}$	Gate controlled turn-on time	$I_{TM} = 16\text{ A}$ ; $V_D = V_{DRM(max)}$ ; $I_G = 0.1\text{ A}$ ; $dl_G/dt = 5\text{ A}/\mu$ s	-	-	-	2	-	$\mu$ s

Triacs

BT138F series

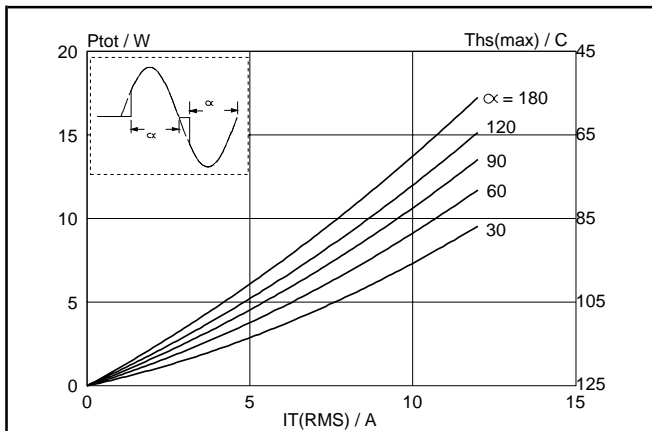


Fig.1. Maximum on-state dissipation,  $P_{tot}$ , versus rms on-state current,  $I_{T(RMS)}$ , where  $\alpha =$  conduction angle.

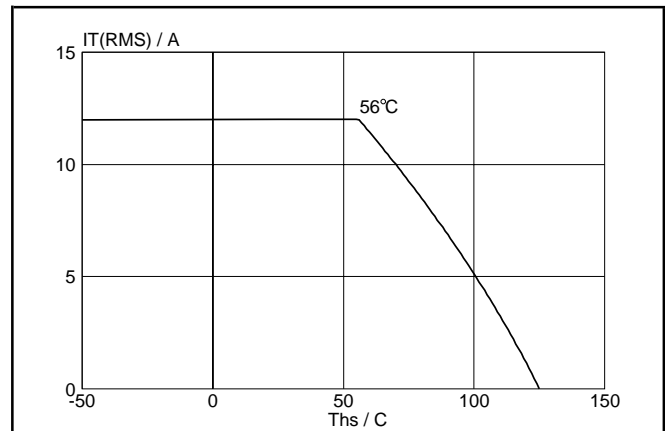


Fig.4. Maximum permissible rms current  $I_{T(RMS)}$ , versus heatsink temperature  $T_{hs}$ .

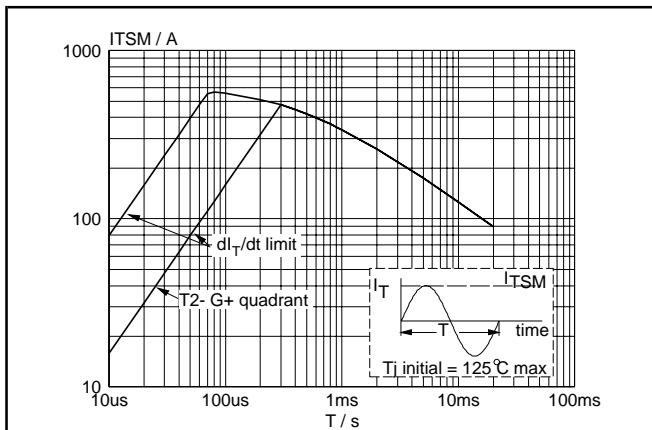


Fig.2. Maximum permissible non-repetitive peak on-state current  $I_{TSM}$ , versus pulse width  $t_p$ , for sinusoidal currents,  $t_p \leq 20ms$ .

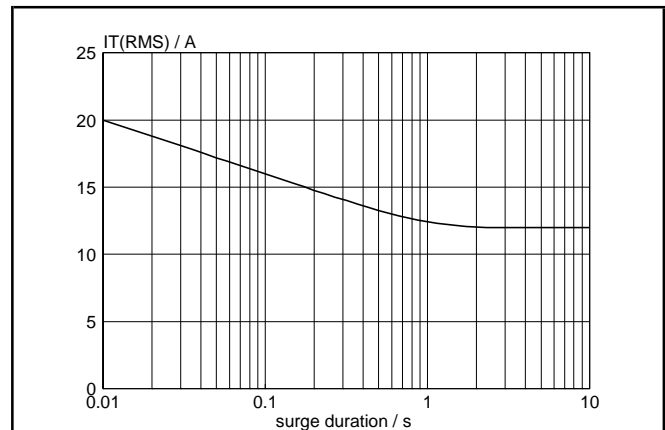


Fig.5. Maximum permissible repetitive rms on-state current  $I_{T(RMS)}$ , versus surge duration, for sinusoidal currents,  $f = 50$  Hz;  $T_{hs} \leq 56$  C.

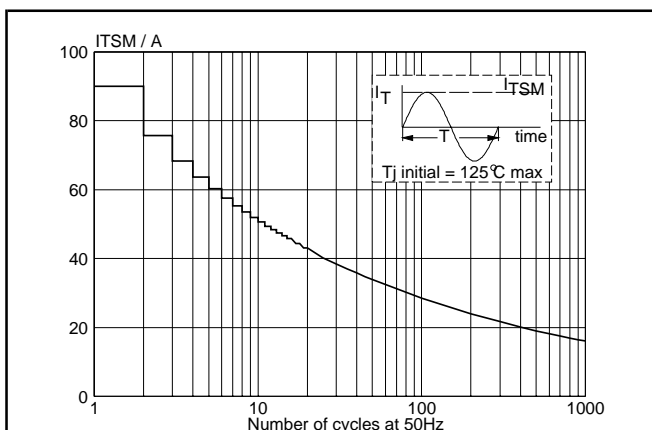


Fig.3. Maximum permissible non-repetitive peak on-state current  $I_{TSM}$ , versus number of cycles, for sinusoidal currents,  $f = 50$  Hz.

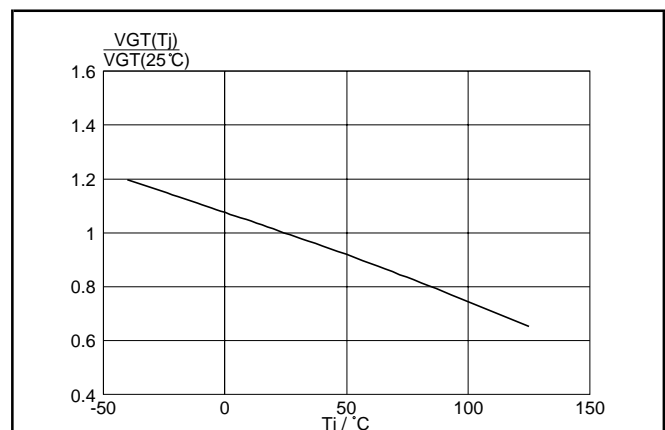
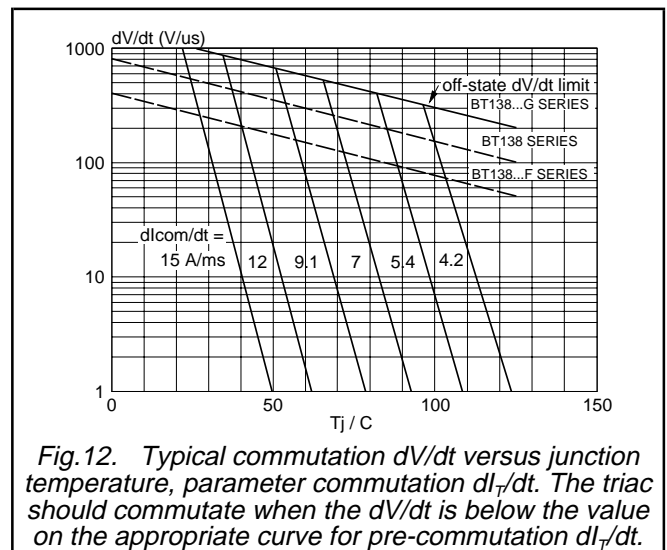
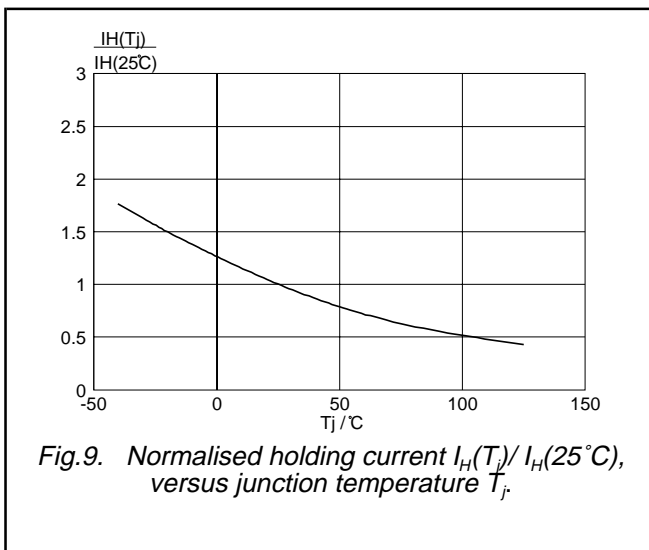
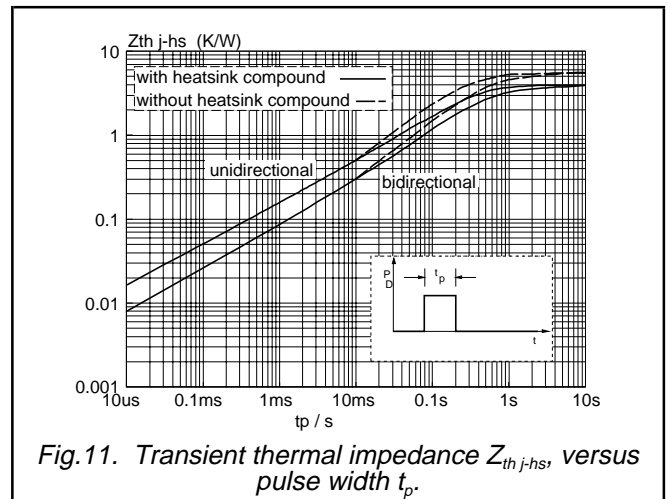
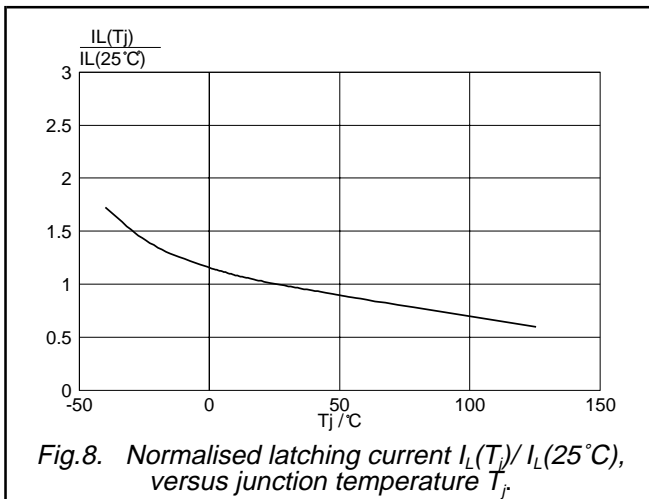
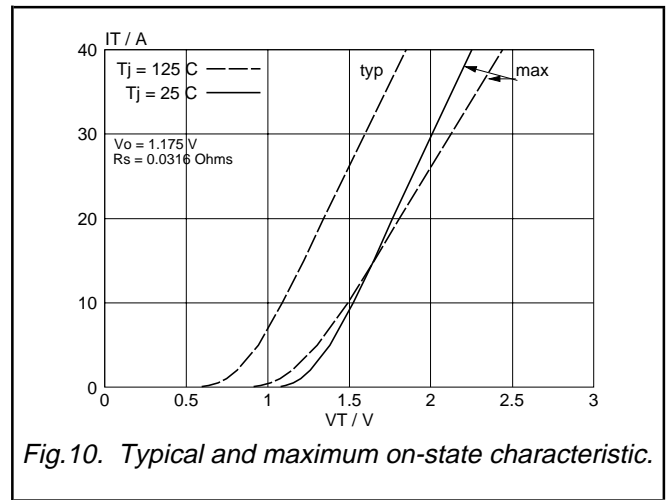
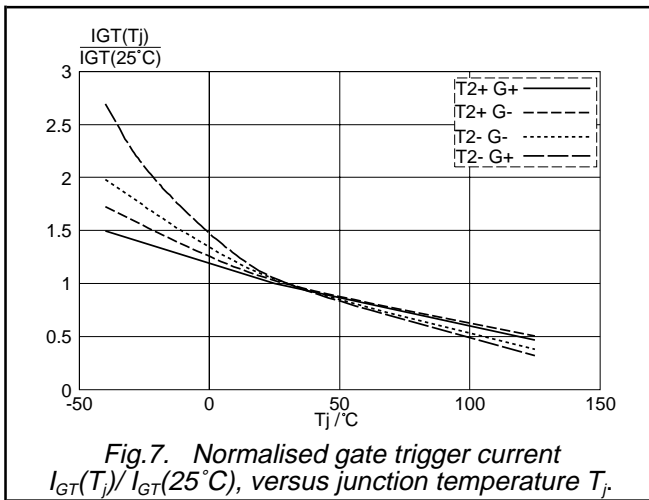


Fig.6. Normalised gate trigger voltage  $V_{GT}(T_j) / V_{GT}(25^\circ C)$ , versus junction temperature  $T_j$ .

Triacs

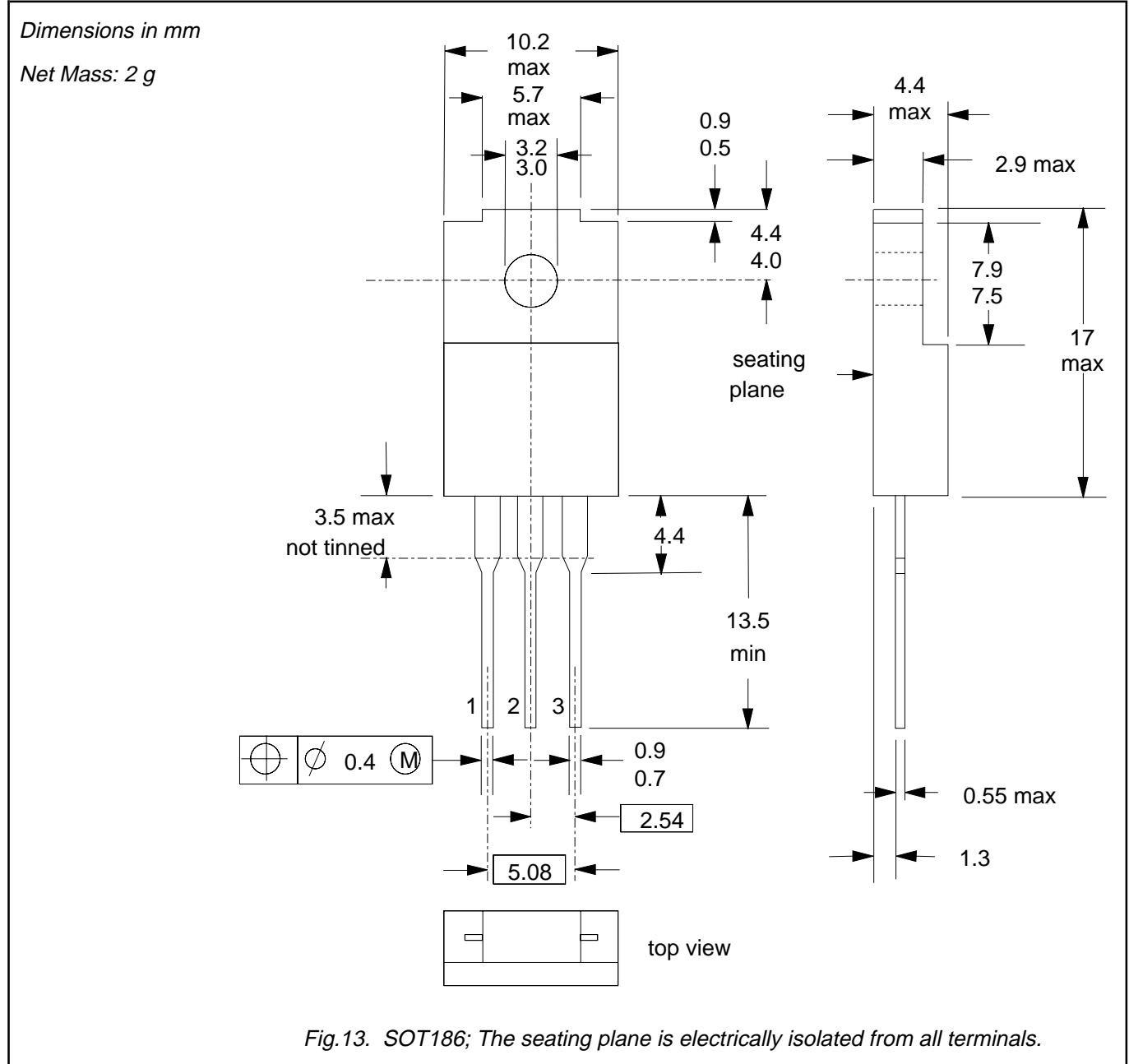
BT138F series



Triacs

BT138F series

**MECHANICAL DATA**



**Notes**

1. Accessories supplied on request: refer to mounting instructions for F-pack envelopes.
2. Epoxy meets UL94 V0 at 1/8".

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**DEFINITIONS**

<b>Data sheet status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
Where application information is given, it is advisory and does not form part of the specification.	
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