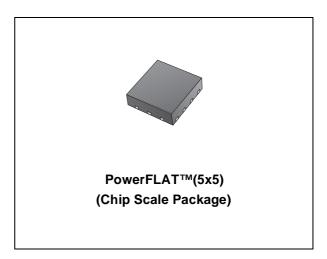


STL6NK55Z

N-CHANNEL 550V - 1.2 Ω - 5.2A PowerFLATTM Zener-Protected SuperMESHTMPower MOSFET

TYPE	V _{DSS}	R _{DS(on)}	I _D (1)	Pw (1)
STL6NK55Z	550 V	< 1.4 Ω	5.2 A	75 W

- TYPICAL $R_{DS}(on) = 1.2 \Omega$
- EXTREMELY HIGH dv/dt CAPABILITY
- IMPROVED ESD CAPABILITY
- 100% AVALANCHE RATED
- GATE CHARGE MINIMIZED
- VERY LOW INTRINSIC CAPACITANCES
- VERY GOOD MANUFACTURING REPEATIBILITY

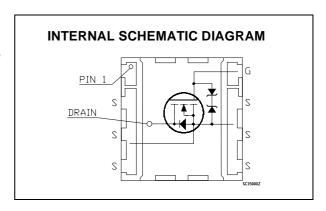


DESCRIPTION

The SuperMESH™ series is obtained through an extreme optimization of ST's well established strip-based PowerMESH™ layout. In addition to pushing on-resistance significantly down, special care is taken to ensure a very good dv/dt capability for the most demanding applications. Such series complements ST full range of high voltage MOSFETs including revolutionary MDmesh™ products.

APPLICATIONS

- LIGHTING
- IDEAL FOR OFF-LINE POWER SUPPLIES, ADAPTORS AND PFC



ORDERING INFORMATION

SALES TYPE	MARKING	PACKAGE	PACKAGING
STL6NK55Z	L6NK55Z	PowerFLAT™ (5x5)	TAPE & REEL

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ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{DS}	Drain-source Voltage (V _{GS} = 0)	550	V
V _{DGR}	Drain-gate Voltage ($R_{GS} = 20 \text{ k}\Omega$)	550	V
V _{GS}	Gate- source Voltage	± 30	V
I _D (2)	Drain Current (continuous) at T _C = 25°C (Steady State) Drain Current (continuous) at T _C = 100°C	0.86 0.54	A A
I _{DM} (2)	Drain Current (pulsed)	3.44	А
P _{TOT} (2)	Total Dissipation at T _C = 25°C (Steady State)	2.5	W
P _{TOT} (1)	Total Dissipation at T _C = 25°C (Steady State)	75	W
	Derating Factor (2)	0.02	W/°C
V _{ESD(G-S)}	Gate source ESD(HBM-C=100pF, R=1.5KΩ)	3000	V/ns
dv/dt (4)	Peak Diode Recovery voltage slope	4.5	V/ns
T _{stg}	Storage Temperature	-55 to 150	°C
Tj	Max. Operating Junction Temperature	-55 to 150	

THERMAL DATA

Symbol	Parameter	Max.	Unit
Rthj-F	Thermal Resistance Junction-Foot (Drain)	1.67	°C/W
Rthj-amb (2)	Thermal Resistance Junction-ambient	50	°C/W

Note: 1. The value is rated according to $R_{\mbox{\scriptsize thj-F}}$.

- 2. When Mounted on FR-4 Board of 1inch², 2 oz Cu
- 3. Pulse width limited by safe operating area
- 4. I_{SD}<5.7A, di/dt<300A/µs, V_{DD}<V_{(BR)DSS}, T_J<T_{JMAX}

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I _{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T_j max)	5.2	А
E _{AS}	Single Pulse Avalanche Energy (starting $T_j = 25$ °C, $I_D = I_{AR}$, $V_{DD} = 50$ V)	160	mJ

GATE-SOURCE ZENER DIODE

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
BV _{GSO}	Gate-Source Breakdown Voltage	Igs=± 1mA (Open Drain)	30			V

PROTECTION FEATURES OF GATE-TO-SOURCE ZENER DIODES

The built-in back-to-back Zener diodes have specifically been designed to enhance not only the device's ESD capability, but also to make them safely absorb possible voltage transients that may occasionally be applied from gate to source. In this respect the Zener voltage is appropriate to achieve an efficient and cost-effective intervention to protect the device's integrity. These integrated Zener diodes thus avoid the usage of external components.

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ELECTRICAL CHARACTERISTICS (TCASE =25°C UNLESS OTHERWISE SPECIFIED) ON/OFF

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source Breakdown Voltage	I _D = 1 mA, V _{GS} = 0	550			V
I _{DSS}	Zero Gate Voltage Drain Current (V _{GS} = 0)	V_{DS} = Max Rating V_{DS} = Max Rating, T_{C} = 125 °C			1 50	μA μA
I _{GSS}	Gate-body Leakage Current (V _{DS} = 0)	V _{GS} = ± 20V			±10	μA
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 50\mu A$	3	3.75	4.5	V
R _{DS(on)}	Static Drain-source On Resistance	V _{GS} = 10V, I _D = 2.6 A		1.2	1.4	Ω

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
g _{fs} (1)	Forward Transconductance	V _{DS} = 10 V _, I _D = 2.6 A		3.5		S
C _{iss} C _{oss} C _{rss}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	$V_{DS} = 25V$, $f = 1$ MHz, $V_{GS} = 0$		695 88 20		pF pF pF
C _{oss eq.} (3)	Equivalent Output Capacitance	$V_{GS} = 0V, V_{DS} = 0V \text{ to } 440 \text{ V}$		48		pF
R _G	Gate Input Resistance	f=1 MHz Gate DC Bias = 0 Test Signal Level = 20mV Open Drain		3		Ω

SWITCHING

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
t _{d(on)} t _r t _{d(off)} t _f	Turn-on Delay Time Rise time Turn-off Delay Time Fall Time	V_{DD} = 275 V, I_{D} = 2.6 A R_{G} = 4.7 Ω V _{GS} = 10 V (Resistive Load see, Figure 3)		14 20 31.5 18		ns ns ns ns
Q _g Q _{gs} Q _{gd}	Total Gate Charge Gate-Source Charge Gate-Drain Charge	$V_{DD} = 440V$, $I_{D} = 5.2 A$, $V_{GS} = 10V$		25 4.5 14	35	nC nC nC

SOURCE DRAIN DIODE

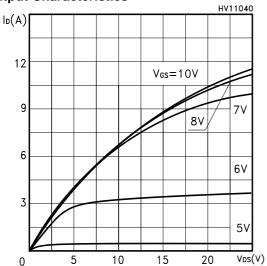
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{SD} I _{SDM} (2)	Source-drain Current Source-drain Current (pulsed)				0.86 3.44	A A
V _{SD} (1)	Forward On Voltage	I _{SD} =5.2 A, V _{GS} = 0			1.6	V
t _{rr} Q _{rr} I _{RRM}	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	I_{SD} = 5.2 A, di/dt = 100A/µs V_{DD} =40V, T_j = 150°C (see test circuit, Figure 5)		350 2.2 12.5		ns µC A

Note: 1. Pulsed: Pulse duration = 300 μ s, duty cycle 1.5 %.

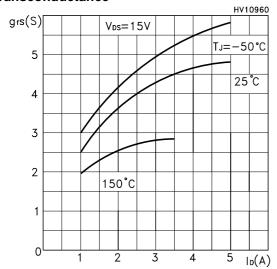
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r uise uuration = 300 µs, duty cycle 1.5 %.
 Pulse width limited by safe operating area.
 C_{oss eq.} is defined as a constant equivalent capacitance giving the same charging time as C_{oss} when V_{DS} increases from 0 to 80% V_{DSS}.

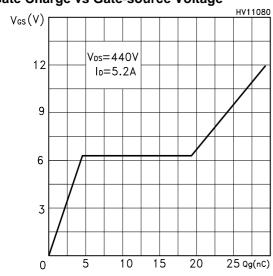
Output Characteristics



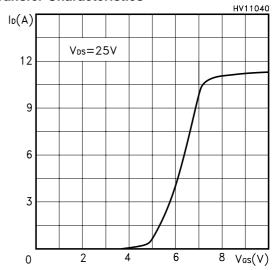
Transconductance



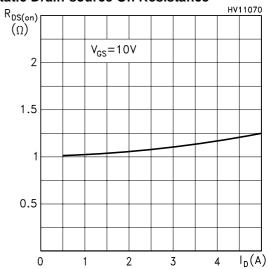
Gate Charge vs Gate-source Voltage



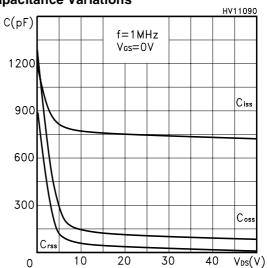
Transfer Characteristics



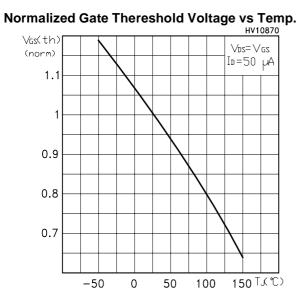
Static Drain-source On Resistance



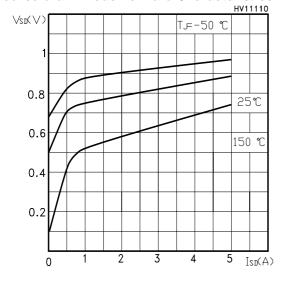
Capacitance Variations



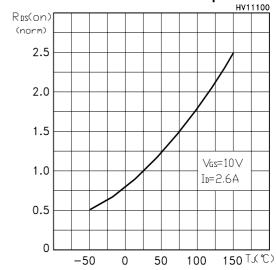
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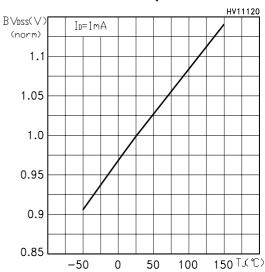
Source-drain Diode Forward Characteristics



Normalized On Resistance vs Temperature



Normalized BVDSS vs Temperature



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Fig. 1: Unclamped Inductive Load Test Circuit

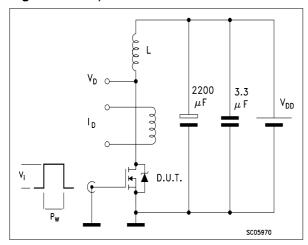


Fig. 3: Switching Times Test Circuit For Resistive Load

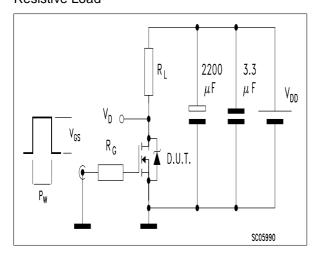


Fig. 5: Test Circuit For Inductive Load Switching And Diode Recovery Times

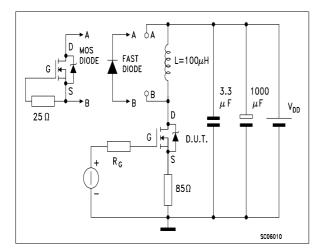


Fig. 2: Unclamped Inductive Waveform

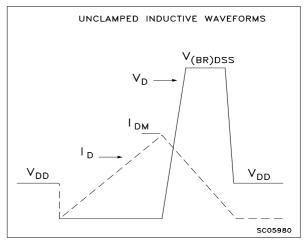
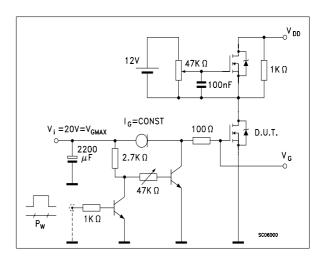


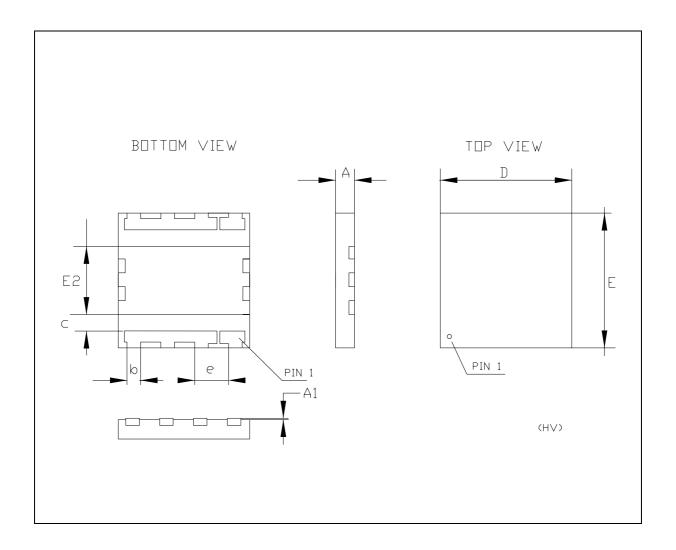
Fig. 4: Gate Charge test Circuit



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PowerFLAT™(5x5) MECHANICAL DATA

DIM		mm.			inch	
DIM. MIN.		TYP	MAX.	MIN.	TYP.	MAX.
Α		0.90	1.00		0.035	0.039
A1		0.02	0.05		0.001	0.002
b	0.43	0.51	0.58	0.017	0.020	0.023
С	0.64	0.71	0.79	0.025	0.028	0.031
D		5.00			0.197	
E		5.00			0.197	
E2	2.49	2.57	2.64	0.098	0.101	0.104
е		1.27			0.050	



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