30V N-CHANNEL ENHANCEMENT MODE MOSFET

SUMMARY

 $V_{(BR)DSS}=30V$; $R_{DS(ON)}=0.12\Omega$ ID=2.0A

DESCRIPTION

This new generation of TRENCH MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.

FEATURES

- Low on-resistance
- Fast switching speed
- Low threshold
- Low gate drive
- SOT23 package

APPLICATIONS

- DC DC Converters
- Power Management Functions
- Motor control

ORDERING INFORMATION

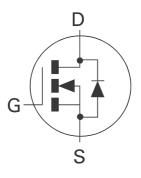
DEVICE	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
ZXMN3A01FTA	7″	12mm	1000 units
ZXMN3A01FTC	13″	12mm	4000 units

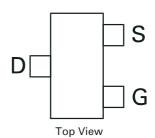
DEVICE MARKING

ISSUE 1 - MARCH 2002

• 7N3

SOT23







ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	VDSS	30	V
Gate Source Voltage	V _{GS}	±20	V
Continuous Drain Current V _{GS} =10V; T _A =25°C (b) V _{GS} =10V; T _A =70°C (b) V _{GS} =10V; T _A =25°C (a)	ID	2.0 1.64 1.81	A
Pulsed Drain Current (c)	IDM	8	А
Continuous Source Current (Body Diode) (b)	IS	1.3	А
Pulsed Source Current (Body Diode) (c)	ISM	8	А
Power Dissipation at T _A =25°C (a) Linear Derating Factor	PD	625 5	W mW/°C
Power Dissipation at T _A =25°C (b) Linear Derating Factor	PD	806 6.4	W mW/°C
Operating and Storage Temperature Range	Tj:Tstg	-55 to +150	°C

THERMAL RESISTANCE

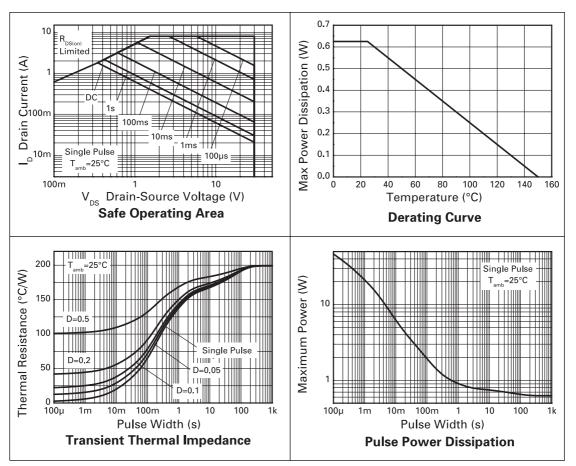
PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient (a)	R _{0JA}	200	°C/W
Junction to Ambient (b)	R _{0JA}	155	°C/W

NOTES

(a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions (b) For a device surface mounted on FR4 PCB measured at $t \le 5$ secs.

(c) Repetitive rating 25mm x 25mm FR4 PCB, D = 0.05, pulse width 10μ s - pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.





CHARACTERISTICS



ELECTRICAL CHARACTERISTICS (at T _A	= 25°C unless otherwise stated).
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PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS.	
STATIC		1		1	1		
Drain-Source Breakdown Voltage	V(BR)DSS	30			V	I _D =250μA, V _{GS} =0V	
Zero Gate Voltage Drain Current	IDSS			0.5	μA	V _{DS} =30V, V _{GS} =0V	
Gate-Body Leakage	IGSS			100	nA	$V_{GS}=\pm 20V, V_{DS}=0V$	
Gate-Source Threshold Voltage	VGS(th)	1			V	I _D =250μA, V _{DS} = V _{GS}	
Static Drain-Source On-State Resistance (1)	R _{DS(on)}		0.106	0.12 0.18	Ω Ω	V _{GS} =10V, I _D =2.5A V _{GS} =4.5V, I _D =2.0A	
Forward Transconductance (1)(3)	9fs		3.5		S	V _{DS} =4.5V,I _D =2.5A	
DYNAMIC (3)							
Input Capacitance	C _{iss}		190		pF		
Output Capacitance	C _{oss}		38		pF	V _{DS} =25 V, V _{GS} =0V, f=1MHz	
Reverse Transfer Capacitance	C _{rss}		20		pF	-	
SWITCHING(2) (3)	1						
Turn-On Delay Time	^t d(on)		1.7		ns	V _{DD} =15V, I _D =2.5A	
Rise Time	tr		2.3		ns		
Turn-Off Delay Time	^t d(off)		6.6.		ns	$R_{G}=6.0\Omega$, $V_{GS}=10V$	
Fall Time	tf		2.9		ns		
Gate Charge	٥ _g		2.3		nC	V _{DS} =15V,V _{GS} =5V, I _D =2.5A	
Total Gate Charge	٥ _g		3.9		nC	151/1/ 401/	
Gate-Source Charge	0 _{gs}		0.6		nC	V _{DS} =15V,V _{GS} =10V, I _D =2.5A	
Gate-Drain Charge	0 _{gd}		0.9		nC	-	
SOURCE-DRAIN DIODE							
Diode Forward Voltage (1)	VSD		0.85	0.95	V	TJ=25°C, IS=1.7A, VGS ^{=0V}	
Reverse Recovery Time (3)	t _{rr}		17.7		ns	TJ=25°C, IF=2.5A,	
Reverse Recovery Charge (3)	0 _{rr}		13.0		nC	di/dt= 100A/µs	

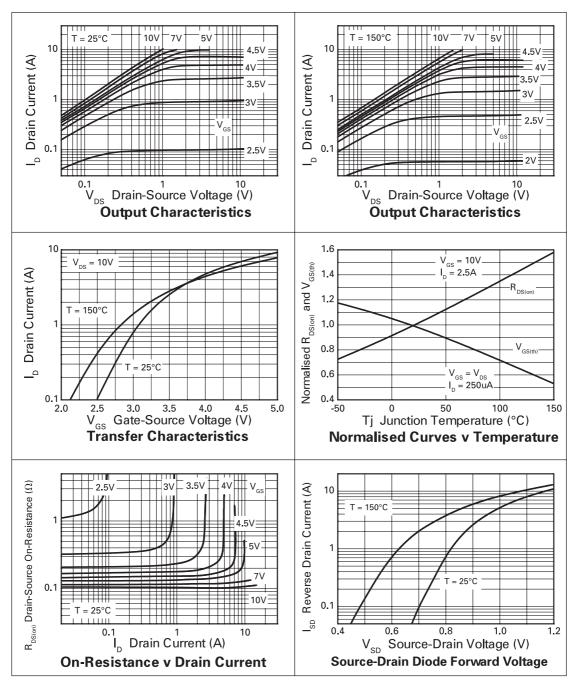
NOTES

(1) Measured under pulsed conditions. Width=300 $\mu s.$ Duty cycle $\leq~2\%$.

(2) Switching characteristics are independent of operating junction temperature.

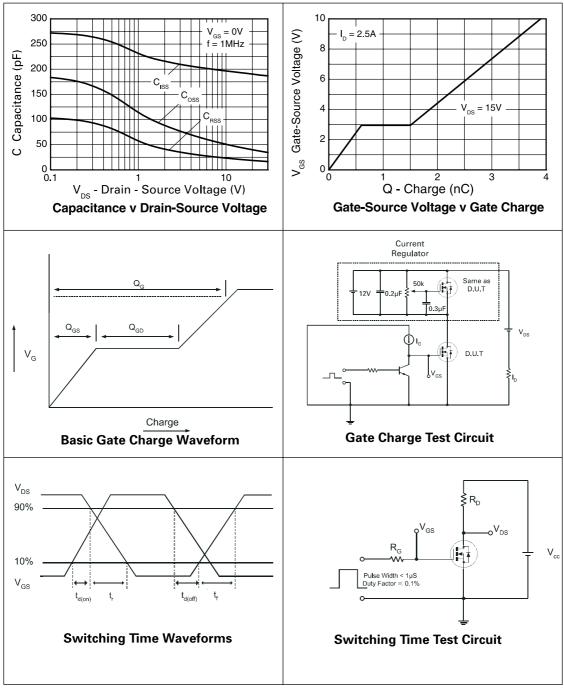
(3) For design aid only, not subject to production testing.





TYPICAL CHARACTERISTICS

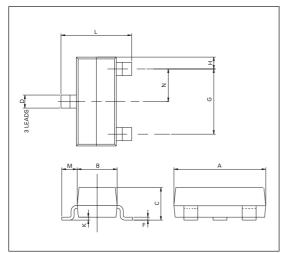


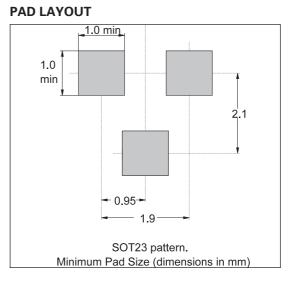


TYPICAL CHARACTERISTICS

ZETEX

PACKAGE OUTLINE





PACKAGE DIMENSIONS

DIM	MILLIMETRES		DIM	MILLIMETRES	
	MIN	MAX	Dinn	MIN	MAX
A	2.67	3.05	Н	0.33	0.51
В	1.20	1.40	К	0.01	0.10
С	_	1.10	L	2.10	2.50
D	0.37	0.53	М	0.45	0.64
F	0.085	0.15	Ν	0.95 NO	M

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