

No.1793B

2SK583

N-Channel Enhancement MOS Silicon FET

Analog Switch Applications

Applications

. Analog switches, low-pass filters

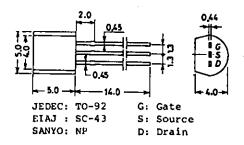
Features

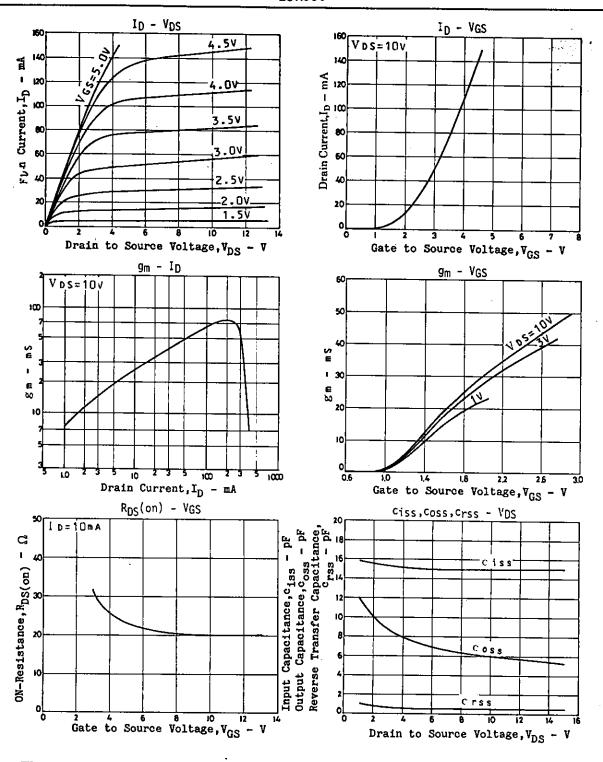
- . Large |yfs
- . Enhancement type
- . Small ON-resistance

Absolute Maximum Ratings at Ta-	:25 ⁰ C		unit
Drain to Source Voltage	$v_{ m DS}$	50	V
Gate to Source Voltage	V _{GS}	±12	V
Drain Current .	$\mathbf{I}_{\mathbf{D}}^{-}$	200	mA
Drain Current (Pulse)	I_{DP}^{-}	300	mA
Allowable Power Dissipation	$P_{\mathbf{D}}$	600	$\mathbf{m}W$
Channel Temperature	Tch	125	°C
Storage Temperature	Tstg	-55 to +125	°C

Precruical characteristics at	Ta=25°C		min	typ	max	unit	
Drain to Source Voltage	V(BR)DS	$I_D = 10\mu A$, $V_{GS} = 0V$	50	-		V	
Gate Cutoff Current	IGSS	$V_{GS} = 10V, V_{DS} = 0V$		0.01	10	nÁ	
Cutoff Voltage	VGS(off)	$V_{DS} = 10V, I_{D} = 100 \mu A$	0.3	0.9	1.5	V	
Drain Current		$V_{DS}^{DS}=20V$, $V_{GS}^{DS}=0V$	•	•	1	μÁ	
Forward Transfer Admittance	¹ DSS yfs	$V_{DS}^{DS} = 10V, I_{D}^{SS} = 50 \text{mA}, f = 1 \text{kHz}$	25	40	•	mS	
Intput Capacitance	c _{iss}	V_{DS}^{DS} =10V, V_{GS}^{D} =0,f=1MHz		15		pF	
Output Capacitance	CORR	$V_{DS} = 10V$, $V_{GS} = 0$, $f = 1MHz$		6		рF	
Reverse Transfer Capacitance	crss	$V_{DS} = 10V$, $V_{GS} = 0$, $f = 1MHz$		0.5		рF	
ON-Resistance	r _{DS(on)}	V _{GS} =10V, I _D =10mA		20		Ω	
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Package Dimensions 2005A (unit: mm)





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