OKI Semiconductor **MSM6597A-xxx**

3-Mbit Serial Voice ROM

GENERAL DESCRIPTION

The MSM6597A is a MSM6597 short TAT process version.

The MSM6597A is a serial voice ROM with a 1,048,576-word × 1-bit × 3-bank configuration. The MSM6597A has a built-in internal address-generating circuit. A single, external clock input allows continuous, serial read operations. The internal addresses are automatically incremented by 1 by read operation. 1024 words in X direction and 1024 words in Y direction can be addressed by inputting external serial addresses. Banks are switched with $\overline{CS1}$, $\overline{CS2}$, and $\overline{CS3}$. A read and playback device with predetermined messages can easily be configured by storing voice data into the MSM6597A and by combining it with one of Oki's recording and playback ICs and with one of Oki's serial registers.

A serial register is required to drive the MSM6597A when used with the MSM6388 or MSM6588. (The MSM6597A does not operate without a serial register.)

The major differences between the MSM6597A and MSM6597 are shown below.

MSM6597A DC Characteristics

				$V_{DD} = 3.5$	to 5.5 V, T	$a = -40$ to \cdot	+85°C
Parameter	Symbol	Condi	tion	Min.	Тур.	Max.	Unit
Current Consumption (1)	I _{DD}	t _{RDC} = 2.5 μs			9	20	mA
Current Consumption (2)	I _{DS}	<u>CS1</u> = <u>CS2</u> = <u>CS3</u> = V _{DD} – 0.2 V	Ta = -40 to +70°C	_	_	10	- μΑ
			Ta = -40 to +85°C	_		50	

MSM6597 DC Characteristics

V_{DD} = 3.5 to 5.5 V, Ta = -40 to +85C°

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Current Consumption (1)	I _{DD}	t _{RDC} = 2.5 μs	—		15	mA
Current Consumption (2)	I _{DS}	$\overline{\text{CS1}} = \overline{\text{CS2}} = \overline{\text{CS3}} = \text{V}_{\text{DD}} - 0.2 \text{ V}$	_		10	μA

Typical values are at $V_{DD} = 5.0 \text{ V}$, Ta = 25°C

For other detailes, refer to individual sections in this data sheet.

FEATURES

Configuration : 1,048,576 words × 1 bit × 3 banks
Serial access : Read cycle time of 2.5 μs
Shorter-TAT processing
Power-supply voltage : 5 V single supply
Package options : 24-pin plastic SOP (SOP24-P-430-1.27-K) (Product name : MSM6597A-xxxGS-K) 30-pin plastic SSOP (SSOP30-P-56-0.65-K) (Product name : MSM6597A-xxxGS-AK)

BLOCK DIAGRAM



PIN CONFIGURATION (TOP VIEW)

NC : No connection

NC : No connection

30-Pin Plastic SSOP

PIN DESCRIPTIONS

Pin		.	-	Description
SOP	SSOP	Symbol	Гуре	Description
12	15	V _{DD}		Power supply pin. Insert a bypass capacitor of 0.1 μF or more between this pin and the GND pin.
24	30	GND		Ground pin
9	12	SADX	I	(SERIAL ADDRESS) This pin inputs the starting X address of a read operation. Addressing in units of 1024 words is possible. The 1024-word address data can be input as 10-bit (AXO - AX9) serial data via the SADX pin.
1	1	SADY	I	(SERIAL ADDRESS) This pin inputs the starting Y address of a read operation. Addressing in units of 1024 words is possible. The 1024-word address data can be input as 10-bit (AY0 - AY9) serial data via the SADY pin.
10	13	SASX	I	(SERIAL ADDRESS STROBE) This is the clock input pin which is used to store the serial address data of the X address into the device's internal register.
2	2	SASY	I	(SERIAL ADDRESS STROBE) This is the clock input pin which is used to store the serial address data of the Y address into the device's internal register.
11	14	TAS	I	(ADDRESS TRANSFER STROBE) This is the input pin for loading the serial address data into the internal address counter. The X and Y addresses are stored at the falling edge of TAS.
15	18	RDCK	I	(READ CLOCK) This is the clock input pin for reading information out of the data register. Internal operation starts at the falling edge of $\overline{\text{RDCK}}$. The information in the data register is output on the DOUT pin. The internal address counter is automatically incremented by 1 at the falling edge of $\overline{\text{RDCK}}$.
22	28	DOUT	0	(DATA OUT) The data output pin is always kept in a high-impedance state when $\overline{CS1}$, $\overline{CS2}$, and $\overline{CS3}$ are all kept "H" or when \overline{RDCK} is kept "H". This pin reflects the "H" or "L" level data being read, and the current data is hold until \overline{RDCH} is asserted High.
4	5	CS1		(CHIP SELECT) When either $\overline{CS1}$, $\overline{CS2}$, or $\overline{CS3}$ is "L", bank 1, bank 2, or bank 3 is
3	3	CS2	I	selected, respectively. Setting all three signals "H" disables all input and output nins. These nins enable parallel use of multiple serial voice ROMs by connecting the
23	29	CS3		data output pins.
13	16	TEST	I	Pin for testing. Apply "L" level.
21	27	TEST01	0	Pine for testing Leave these pine open
14	17	TEST02	U	rins ioi lesling. Leave liese pins open.

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Condition	Rating	Unit
Power Supply Voltage	V _{DD}	Ta = 25°C	-0.3 to +7.0	V
Input Voltage	V _{IN}	Ta = 25°C	-0.3 to V _{DD} +0.3	V
Storage Temperature	T _{STG}	—	-55 to +150	°C

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Condition	Range	Unit
Power Supply Voltage	V _{DD}	GND=0V	3.5 to 5.5	V
Operating Temperature	T _{op}	_	-40 to +85	°C

ELECTRICAL CHARACTERISTICS

DC Characteristics

	V _{DD} = 3.5 to 5.5 V, Ta = -40 t				a = -40 to -	+85°C	
Parameter	Symbol	Condi	tion	Min.	Тур.	Max.	Unit
"H" Level Input Voltage	V _{IH}	_		0.8xV _{DD}	—	_	V
"L" Level Input Voltage	VIL	_		_	—	0.8	V
"H" Level Output Voltage	V _{OH}	I _{OH} = -4	0 μΑ	V _{DD} -0.3	_	_	V
"L" Level Output Voltage	V _{OL}	I _{OL} = 2 mA			—	0.45	V
"H" Level Input Current	IIH	$V_{IH} = V_{DD}$			_	10	μA
"L" Level Input Current	١ _{١L}	V _{IL} = GND		-10	_		μA
Current Consumption (1)	I _{DD}	t _{RDC} = 2.5 μs			9	20	mA
Oursest Consumption (0)	I _{DS}	$\overline{\text{CS1}} = \overline{\text{CS2}} = \overline{\text{CS3}}$ $= \text{V}_{\text{DD}} - 0.2 \text{ V}$	Ta = -40 to +70°C	_	_	10	
			Ta = -40 to +85°C			50	μΑ

Typical values are at V_{DD} = 5.0 V, Ta = 25°C

AC Characteristics

	V _{DD} = 3.5	to 5.5 V,	Ta = -40	to +85°C
Parameter	Symbol	Min.	Max.	Unit
CS, SAS Setup Time	t _{CSS}	1000		ns
SASX, SASY Cycle Time	t _{SSC}	500		ns
SASX, SASY Precharge Time	t _{SAP}	250		ns
SASX, SASY Pulse Width	t _{SAS}	250		ns
Address Setup Time	t _{AS}	100		ns
Address Hold Time	t _{AH}	100		ns
TAS Setup Time	t _{ATS}	500		ns
TAS, RDCK Setup Time	t _{TRS}	500		ns
TAS Pulse Width	t _{TAS}	250	_	ns
Read Cycle Time	t _{RDC}	2500		ns
Access Time	t _{ACC}		1500	ns
Output Turn-off Delay Time	t _{OFF}	0	200	ns
RDCK Precharge Time	t _{RDP}	1000	_	ns
RDCK Pulse Width	t _{RD}	1500	_	ns

TIMING DIAGRAMS

Serial Address Input Timing

Read Access Timing

APPLICATION CIRCUIT

MSM6588 Playback Storage Example

Note: When the MSM6597A is driven by the MSM6388 or MSM6588, a serial register is required. (The MSM6597A does not operate without it.)

PACKAGE DIMENSIONS

(Unit : mm)

Notes for Mounting the Surface Mount Type Package

The SOP, QFP, TSOP, TQFP, LQFP, SOJ, QFJ (PLCC), SHP, and BGA are surface mount type packages, which are very susceptible to heat in reflow mounting and humidity absorbed in storage. Therefore, before you perform reflow mounting, contact Oki's responsible sales person on the product name, package name, pin number, package code and desired mounting conditions (reflow method, temperature and times).

(Unit : mm)

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