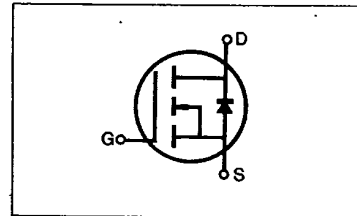
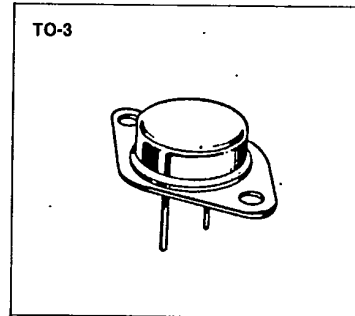


IRF450/451/452/453**N-CHANNEL
POWER MOSFETS****FEATURES**

- Low $R_{DS(on)}$ at high voltage
- Improved inductive ruggedness
- Excellent high voltage stability
- Fast switching times
- Rugged polysilicon gate cell structure
- Low input capacitance
- Extended safe operating area
- Improved high temperature reliability
- TO-3 package (High voltage)

**PRODUCT SUMMARY**

Part Number	V_{DS}	$R_{DS(on)}$	I_D
IRF250	500V	0.4 Ω	13A
IRF251	450V	0.4 Ω	13A
IRF252	500V	0.5 Ω	12A
IRF253	450V	0.5 Ω	12A

MAXIMUM RATINGS

Characteristic	Symbol	IRF450	IRF451	IRF452	IRF453	Unit
Drain-Source Voltage (1)	V_{DS}	500	450	500	450	Vdc
Drain-Gate Voltage ($R_{GS}=1.0M\Omega$)(1)	V_{DGR}	500	450	500	450	Vdc
Gate-Source Voltage	V_{GS}	± 20				Vdc
Continuous Drain Current $T_C=25^\circ C$	I_D	13	13	12	12	Adc
Continuous Drain Current $T_C=100^\circ C$	I_D	8.0	8.0	7.0	7.0	Adc
Drain Current—Pulsed (3)	I_{DM}	52	52	48	48	Adc
Gate Current—Pulsed	I_{GM}	± 1.5				Adc
Total Power Dissipation @ $T_C=25^\circ C$ Derate above $25^\circ C$	P_D	150 1.2				Watts W/ $^\circ C$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-55 to 150				$^\circ C$
Maximum Lead Temp. for Soldering Purposes, 1/8" from case for 5 seconds	T_L	300				$^\circ C$

Notes: (1) $T_J=25^\circ C$ to $150^\circ C$

(2) Pulse test: Pulse width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

(3) Repetitive rating: Pulse width limited by max. junction temperature

IRF450/451/452/453**N-CHANNEL
POWER MOSFETS**

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ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$ unless otherwise specified)

Characteristic	Symbol	Type	Min	Typ	Max	Units	Test Conditions
Drain-Source Breakdown Voltage	BV_{DSS}	IRF450 IRF452	500	—	—	V	$V_{GS}=0V$
		IRF451 IRF453	450	—	—	V	$I_D=250\mu A$
Gate Threshold Voltage	$V_{GS(th)}$	ALL	2.0	—	4.0	V	$V_{DS}=V_{GS}$, $I_D=250\mu A$
Gate-Source Leakage Forward	I_{GSS}	ALL	—	—	100	nA	$V_{GS}=20V$
Gate-Source Leakage Reverse	I_{GSS}	ALL	—	—	-100	nA	$V_{GS}=-20V$
Zero Gate Voltage Drain Current	I_{DSS}	ALL	—	—	250	μA	$V_{DS}=\text{Max. Rating}$, $V_{GS}=0V$
		—	—	—	1000	μA	$V_{DS}=\text{Max. Rating}\times 0.8$, $V_{GS}=0V$, $T_C=125^\circ\text{C}$
On-State Drain-Source Current (2)	$I_{D(on)}$	IRF450 IRF451	13	—	—	A	$V_{DS}>I_{D(on)}\times R_{DS(on) \text{ max.}}$, $V_{GS}=10V$
		IRF452 IRF453	12	—	—	A	
Static Drain-Source On-State Resistance (2)	$R_{DS(on)}$	IRF450 IRF451	—	0.38	0.4	Ω	$V_{GS}=10V$, $I_D=7.0A$
		IRF452 IRF453	—	0.4	0.5	Ω	
Forward Transconductance (2)	g_{fs}	ALL	6.0	10.8	—	Ω	$V_{DS}>I_{D(on)}\times R_{DS(on) \text{ max.}}$, $I_D=7.0A$
Input Capacitance	C_{iss}	ALL	—	2850	3000	pF	
Output Capacitance	C_{oss}	ALL	—	350	600	pF	$V_{GS}=0V$, $V_{DS}=25V$, $f=1.0\text{MHz}$
Reverse Transfer Capacitance	C_{rss}	ALL	—	150	200	pF	
Turn-On Delay Time	$t_{d(on)}$	ALL	—	—	35	ns	
Rise Time	t_r	ALL	—	—	50	ns	$V_{DD}=0.5BV_{DSS}$, $I_D=7.0A$, $Z_o=4.7\Omega$ (MOSFET switching times are essentially independent of operating temperature.)
Turn-Off Delay Time	$t_{d(off)}$	ALL	—	—	150	ns	
Fall Time	t_f	ALL	—	—	70	ns	
Total Gate Charge (Gate-Source Plus Gate-Drain)	Q_g	ALL	—	77	120	nC	$V_{GS}=10V$, $I_D=16A$, $V_{DS}=0.8 \text{ Max. Rating}$ (Gate charge is essentially independent of operating temperature. See Fig. 8 page 21)
Gate-Source Charge	Q_{gs}	ALL	—	11	—	nC	
Gate-Drain ("Miller") Charge	Q_{gd}	ALL	—	66	—	nC	

THERMAL RESISTANCE

Junction-to-Case	R_{thJC}	ALL	—	—	0.83	K/W	
Case-to-Sink	R_{thCS}	ALL	—	0.1	—	K/W	Mounting surface flat, smooth, and greased
Junction-to-Ambient	R_{thJA}	ALL	—	—	30	K/W	Free Air Operation

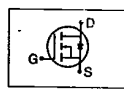
Notes: (1) $T_J=25^\circ\text{C}$ to 150°C (2) Pulse test: Pulse width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

(3) Repetitive rating: Pulse width limited by max. junction temperature

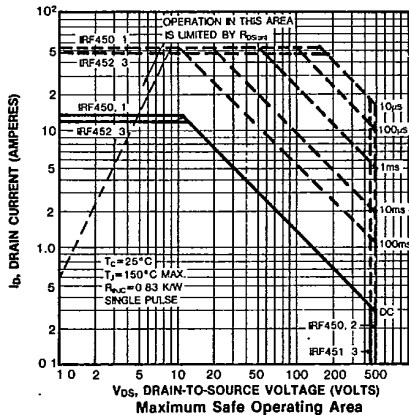
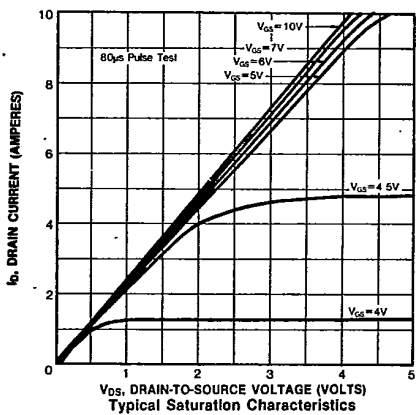
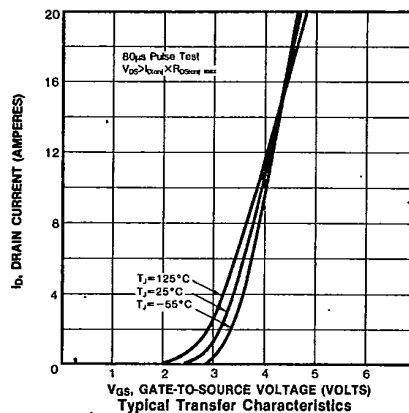
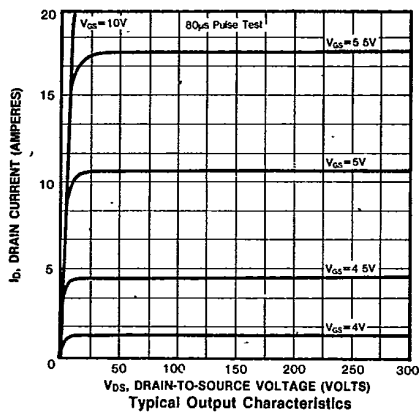
**N-CHANNEL
POWER MOSFETS**

IRF450/451/452/453

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

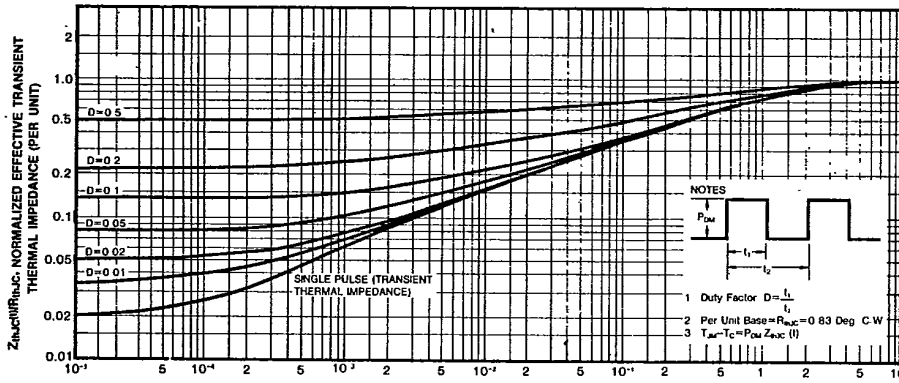
Characteristic	Symbol	Type	Min	Typ	Max	Units	Test Conditions
Continuous Source Current (Body Diode)	I _S	IRF450	—	—	13	A	Modified MOSFET symbol showing the integral reverse P-N junction rectifier 
		IRF451	—	—	12	A	
		IRF452	—	—	12	A	
Pulse Source Current (Body Diode) (3)	I _{SM}	IRF450	—	—	52	A	
		IRF451	—	—	48	A	
		IRF452	—	—	48	A	
Diode Forward Voltage (2)	V _{SD}	IRF450	—	—	1.4	V	T _C =25°C, I _S =13A, V _{GS} =0V
		IRF451	—	—	1.3	V	T _C =25°C, I _S =12A, V _{GS} =0V
Reverse Recovery Time	t _{rr}	ALL	—	1300	—	ns	T _J =150°C, I _F =13A, dI _F /dt=100A/μs

Notes: (1) T_J=25°C to 150°C (2) Pulse test: Pulse width ≤ 300μs, Duty Cycle ≤ 2%
(3) Repetitive rating: Pulse width limited by max. junction temperature

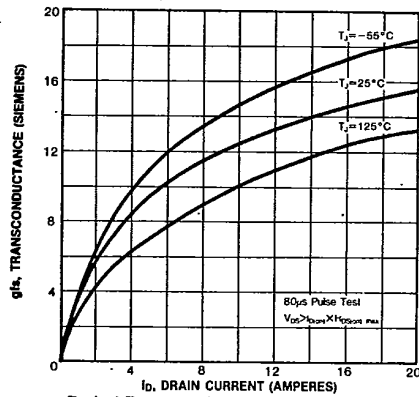


IRF450/451/452/453

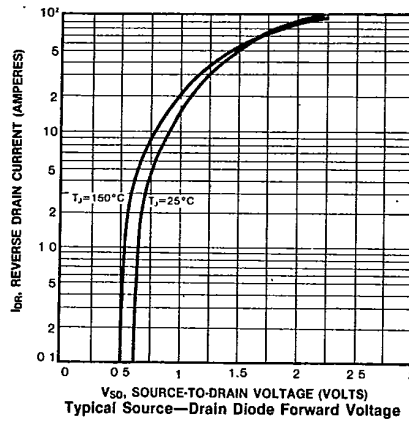
N-CHANNEL POWER MOSFETS



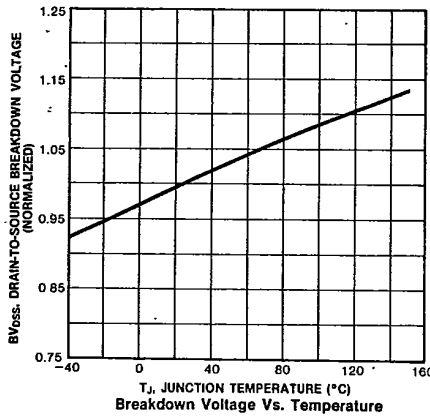
11. SQUARE WAVE PULSE DURATION (SECONDS)
Maximum Effective Transient Thermal Impedance Junction-to-Case Vs. Pulse Duration



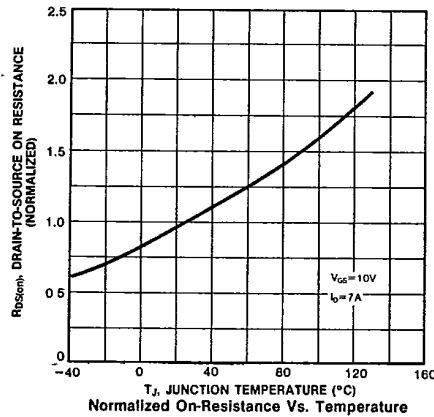
Typical Transconductance Vs. Drain Current



Typical Source-Drain Diode Forward Voltage



Breakdown Voltage Vs. Temperature



Normalized On-Resistance Vs. Temperature

IRF450/451/452/453

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