

## 10/100BASE-TX/FX HEX- $\Phi^{\text {TM }}$ TRANSCEIVER

BCM5226FEATURES

- Single-chip six-port Fast Ethernet transceiver
- HP Auto-MDIX
- Low-power 2.5V CMOS technology
- Compatible with 2.5 V I/O and 3.3 V I/O
- Reduced Media Independent Interface (RMII)
- Serial Media Independent Interface (SMII)
- Fully integrated digital adaptive equalization
- On-chip multimode transmit waveshaping
- Edge-rate control eliminates external filters
- Integrated baseline wander correction
- Full-duplex support
- Twisted-pair or fiber support on any or all ports
- Interrupt output capability
- Loopback mode for diagnostics
- 160-pin PQFP (BCM5226R)
- 128-pin PQFP (BCM5226S)
- Proven Broadcom proprietary DSP Digi- $\Phi^{\text {TM }}$ architecture


## SUMMARYOFBENEFITS

- Target usage: Fast Ethernet switches.
- Low power < 300 milliwatts per port.
- Auto-MDIX detects and corrects cabling errors.
- Provides robust performance over a broad range of operating conditions.
- Single-chip device contains six independent Fast Ethernet transceivers.
- Performs all physical layer interface functions for 100BASE-TX full-duplex or half-duplex Ethernet on Category 5 twisted-pair cable and 10BASE-T full- or half-duplex Ethernet on Category 3, 4, or 5 cable.
- Permits transmission over fiber-optic cabling when paired with an external fiber-optic transceiver.
- Compliant with IEEE 802.3 standard.
- IEEE 1149.1 (JTAG) scan chain support.

Quad BCM5208 Versus HEX BCM5226 Comparison

## 6 Active PHY components + 6 MACs including switch fabric



4 Active SMII PHY components + SMII MACs embedded in switch chip



The Broadcom ${ }^{\text {® }}$ BCM5226 is a single-chip device containing six independent Fast Ethernet transceivers. Each performs all the physical layer interface functions for 100BASE-TX full-duplex or half-duplex Ethernet on Category 5 twisted-pair cable and 10BASE-T full or half-duplex Ethernet on Category 3, 4 or 5 cable. Each port may also be configured for 100BASE-FX full or half-duplex transmission over fiber-optic cabling when paired with an external fiber-optic line transceiver.
The BCM5226 chip performs 4B5B, MLT3, NRZI, and Manchester encoding and decoding, clock and data recovery, stream cipher scrambling/descrambling, digital adaptive equalization, line transmission, carrier sense and link integrity monitoring, auto-negotiation and RMII management functions.

The BCM5226 can be connected to a switch controller through the RMII on one side and connects directly to the network media on the other side through isolation transformers for UTP modes or fiber-optic transceiver components for FX mode. The BCM5226 is compliant with the IEEE 802.3 standard.

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